

HERMES B1A

Define name rule :

V==>Chipset : G2,IOTA,Flash+SRAM,MMP,NAND,Audio Codec,Melody

U==>LDO,charger IC,EMI filter,

X==>crystal,OSC,microphone,speaker,receiver

M==>Module/connector :SIM, LCDM,DSC,IrDA,SD-CARD,

CON==>connector : battery connector,headset connector

B==>BJT

F==>Fuse

FN==>FET MOS N channel

FP==>FET MOS P channel

D==>Diode

L==>Inductor,Bead

RB==>Resistor

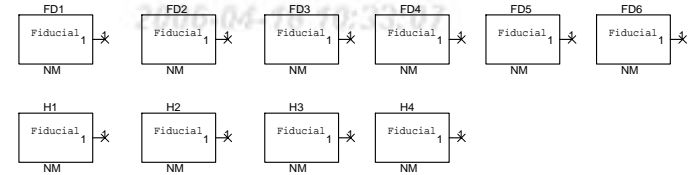
CB==>Capacitor

JP==>Jump

TP==>Test Point for G2 , IOTA

TPL==>Test Point for others

Rev	Description	Date



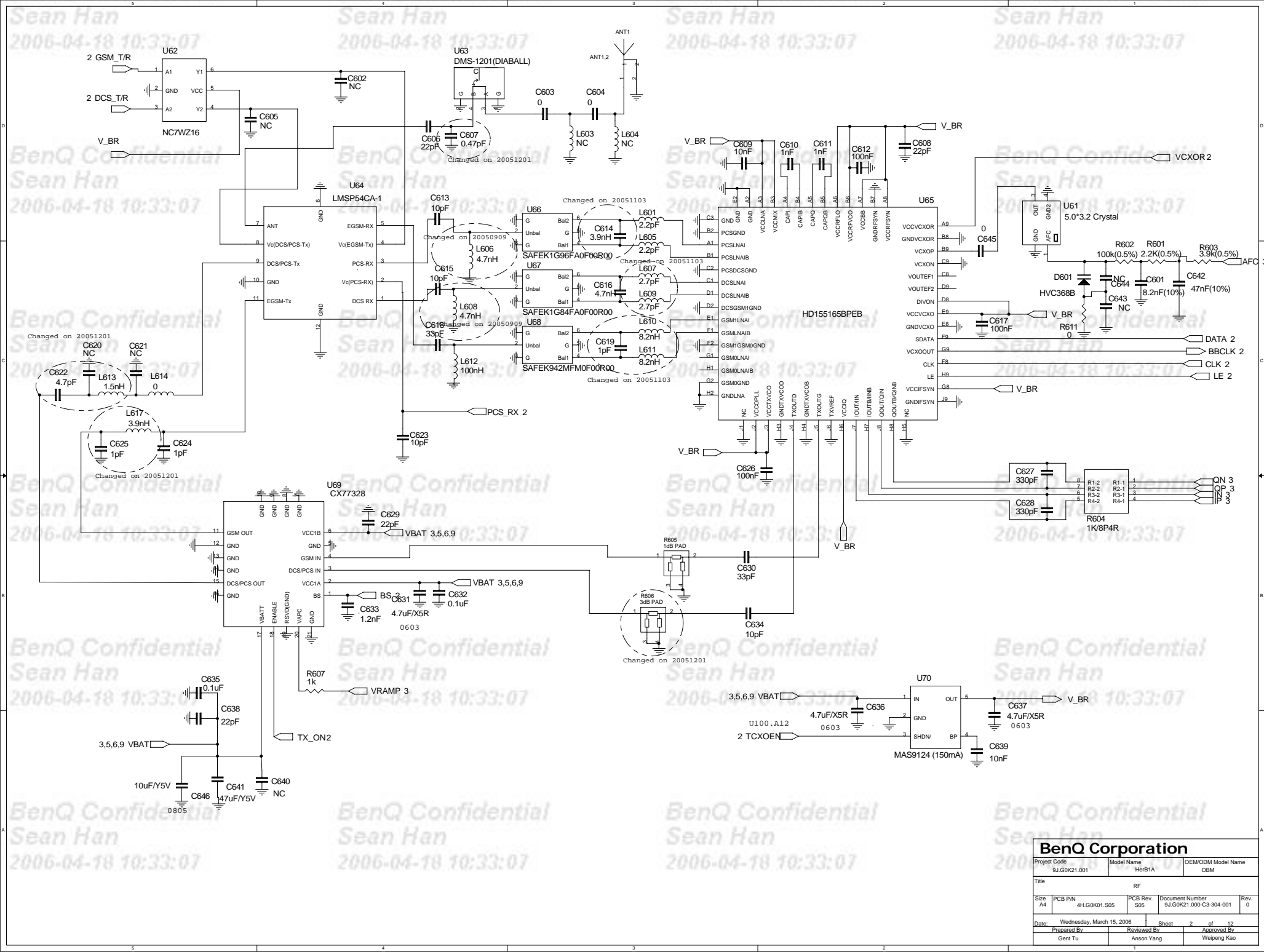
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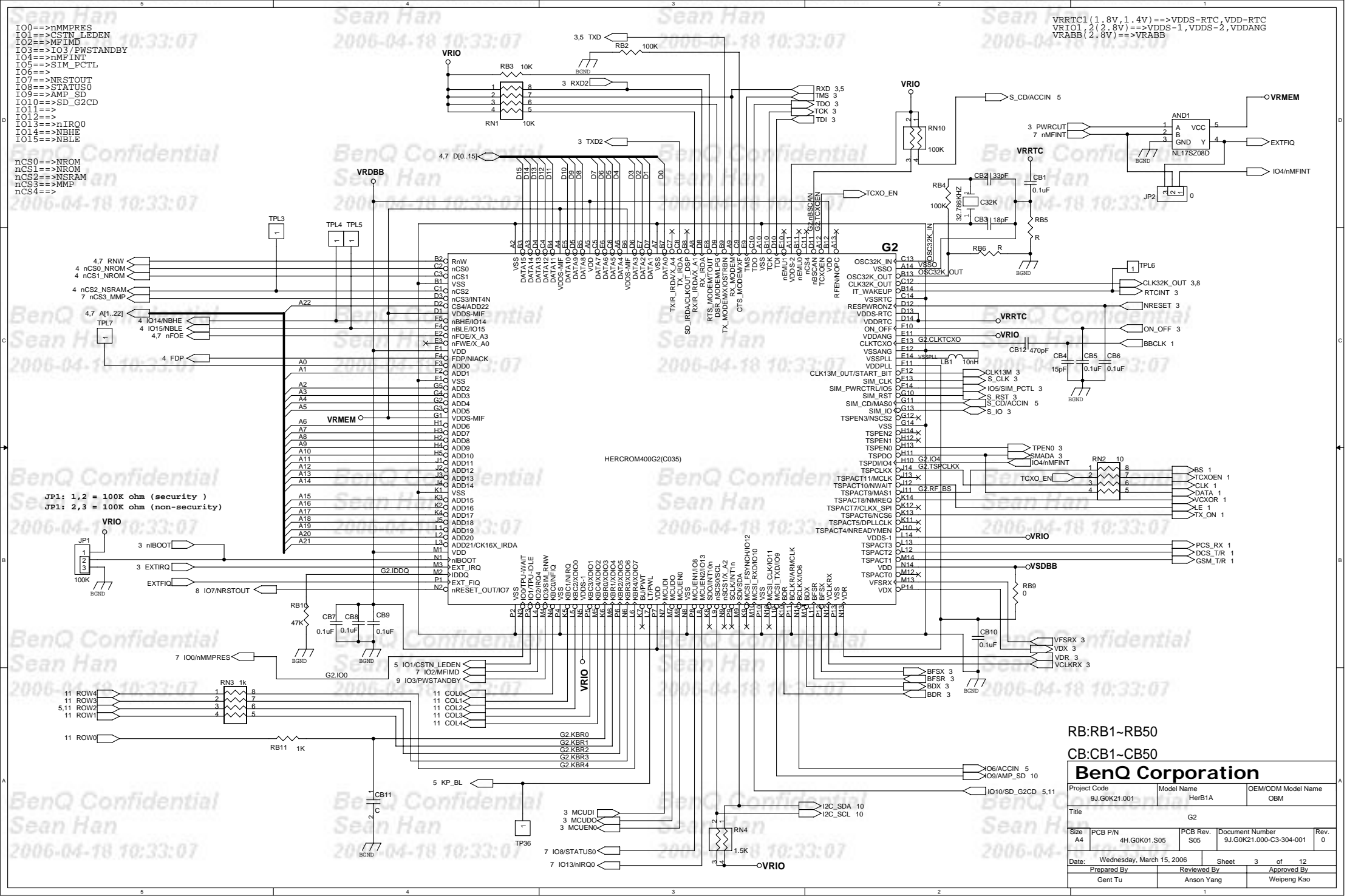
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BenQ Corporation				
Project Code 9J.G0K21.001		Model Name HerB1A		OEM/ODM Model Name OBM
Title Title page				
Size A4	PCB P/N 4H.G0K01.S05	PCB Rev. S05	Document Number 9J.G0K21.000-C3-304-001	Rev. 0
Date: Wednesday, March 15, 2006		Sheet 1 of 12		
Prepared By Gent Tu		Reviewed By Anson Yang	Approved By Weipeng Kao	

Benq Corporation		
Project Code <ProjectCode>	Model Name <Model Name>	OEM/ODM Model Name <OEM/ODM>



BenQ Corporation					
Project Code	9J.G0K21.001	Model Name	HerB1A	OEM/ODM Model Name	OBM
Title	RF				
Size	A4	PCB P/N	4H.G0K01.S05	Document Number	9J.G0K21.000-C3-304-001
Date:	Wednesday, March 15, 2006	Sheet	2	of	12
Prepared By	Gent Tu	Reviewed By	Anson Yang	Approved By	Weipeng Kao



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IO0==>nMMPRES
IO1==>CSTN_LEDEN
IO2==>MIFIMD
IO3==>IO3/PWSTANDBY
IO4==>nMFINIT
IO5==>SIM_PCTL
IO6==>
IO7==>NRSTOUT
IO8==>STATUS0
IO9==>AMP_SD
IO10==>SD_G2CD
IO11==>
IO12==>nIRQ0
IO14==>NBHE
IO15==>NBLE
  
```

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nCS0==>NROM
nCS1==>NROM
nCS2==>NSRAM
nCS3==>MMP
nCS4==>
  
```

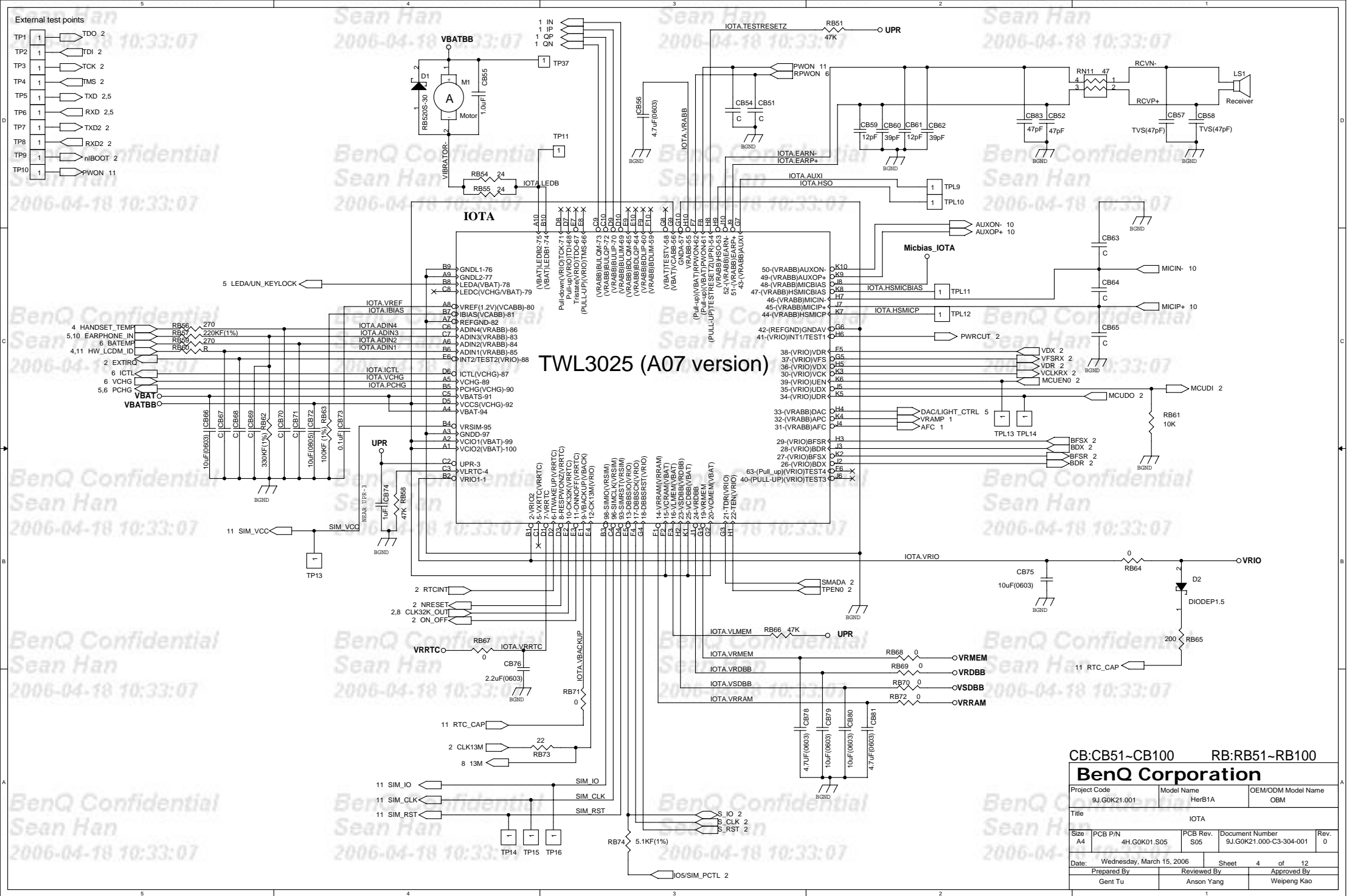
```

VRRTC1(1.8V,1.4V)==>VDD5-RTC,VDD-RTC
VRRTC1(2.8V)==>VDD5-1,VDD5-2,VDDANG
VRABB(2.8V)==>VRABB
  
```

JPL: 1,2 = 100K ohm (security)
 JPL: 2,3 = 100K ohm (non-security)

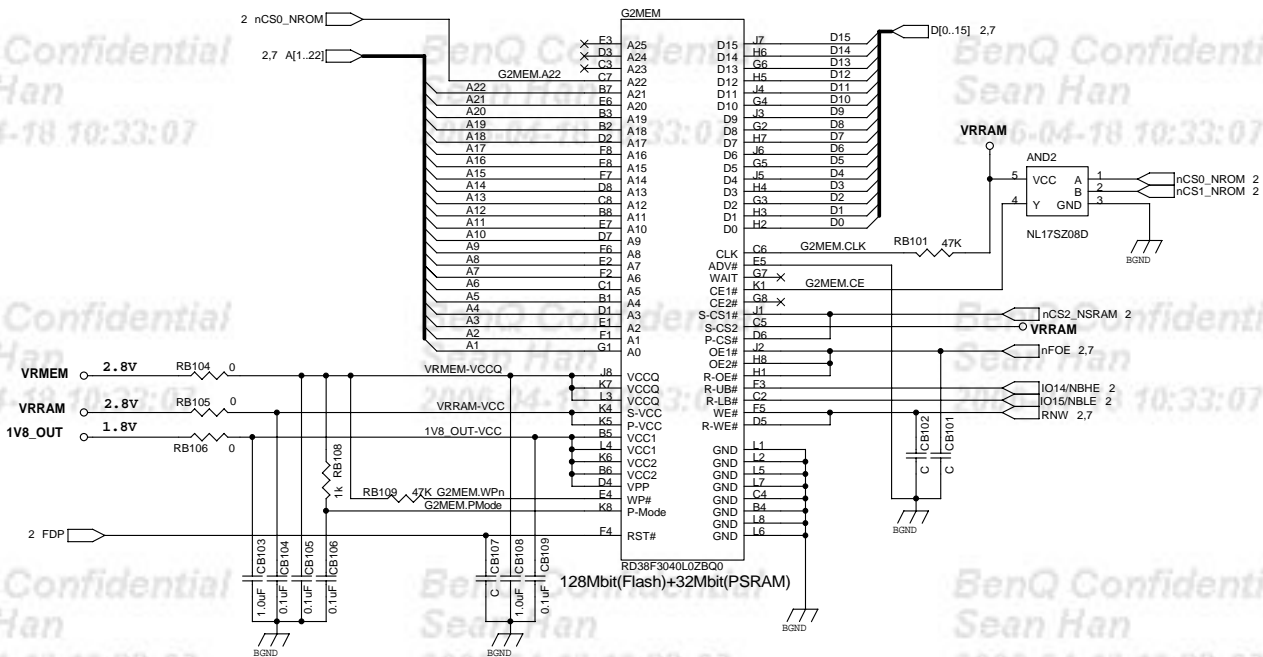
RB:RB1-RB50
 CB:CB1-CB50

BenQ Corporation				
Project Code	Model Name	OEM/ODM Model Name		
9J.G0K21.001	HerB1A	OBM		
Title	G2			
Size	PCB P/N	PCB Rev.	Document Number	Rev.
A4	4H.G0K01.S05	S05	9J.G0K21.000-C3-304-001	0
Date:	Wednesday, March 15, 2006	Sheet	3 of 12	
Prepared By	Reviewed By	Approved By		
Gent Tu	Anson Yang	Weipeng Kao		

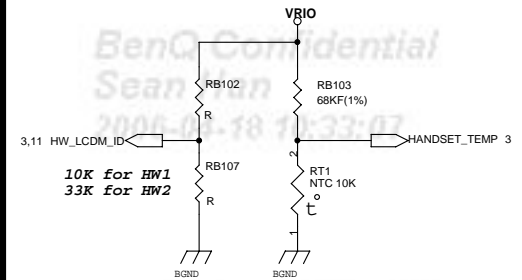


CB:CB51~CB100 RB:RB51~RB100				
BenQ Corporation				
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9J.GOK21.001	HerB1A		OBM	
Title: IOTA				
Size	PCB P/N	PCB Rev.	Document Number	Rev.
A4	4H.GOK01.S05	S05	9J.GOK21.000-C3-304-001	0
Date:	Wednesday, March 15, 2006	Sheet	4 of 12	
Prepared By		Reviewed By	Approved By	
Gent Tu		Anson Yang	Weipeng Kao	

G2 memory SKT

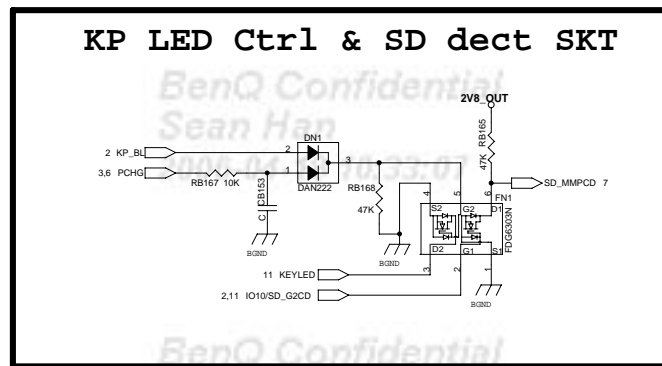
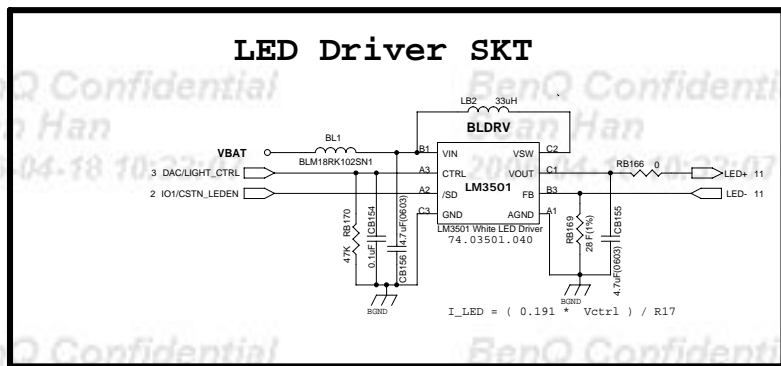
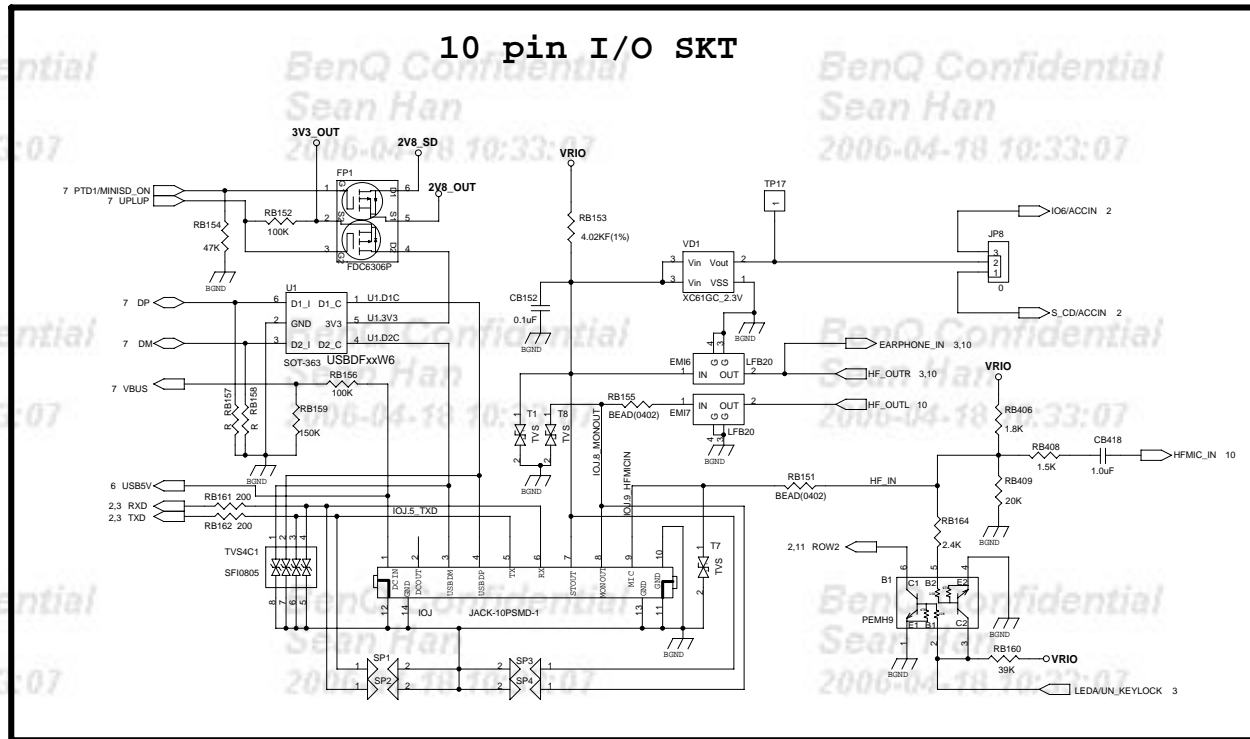


HW ID & HS temp SKT



RB:RB101~RB150
CB:CB101~CB150

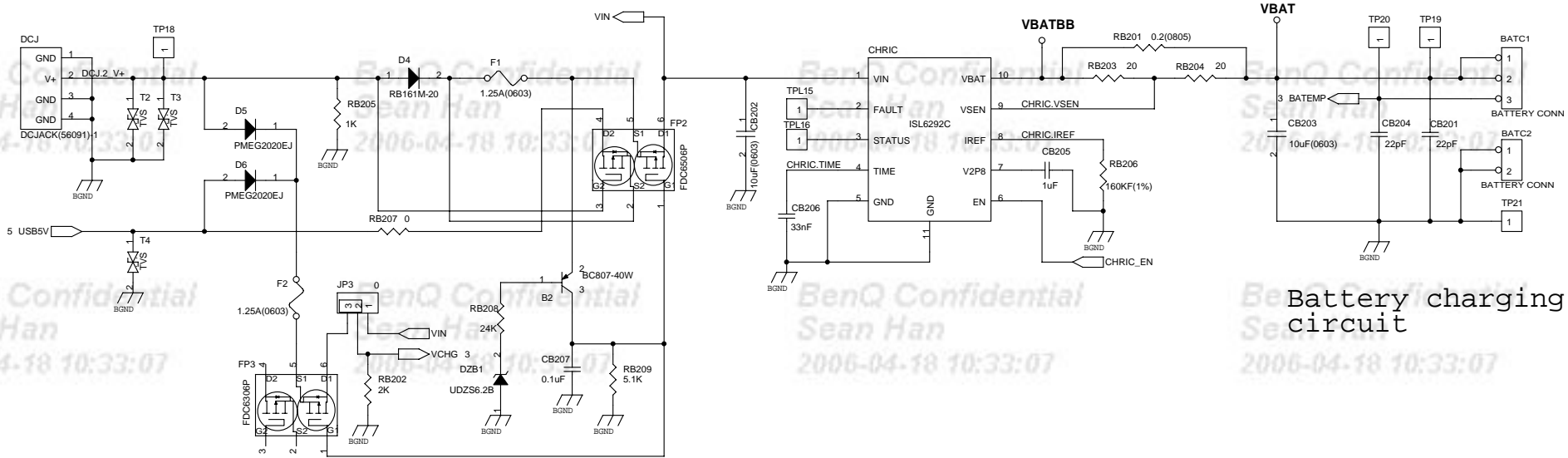
BenQ Corporation					
Project Code	Model Name	OEM/ODM Model Name			
9J.G0K21.001	HerB1A	OBM			
Title: Memory					
Size	PCB P/N	PCB Rev.	Document Number	Rev.	
A4	4H.G0K01.S05	S05	9J.G0K21.000-C3-304-001	0	
Date:	Wednesday, March 15, 2006	Sheet	5	of 12	
Prepared By	Reviewed By	Approved By			
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RB:RB151~RB200
CB:CB151~CB200

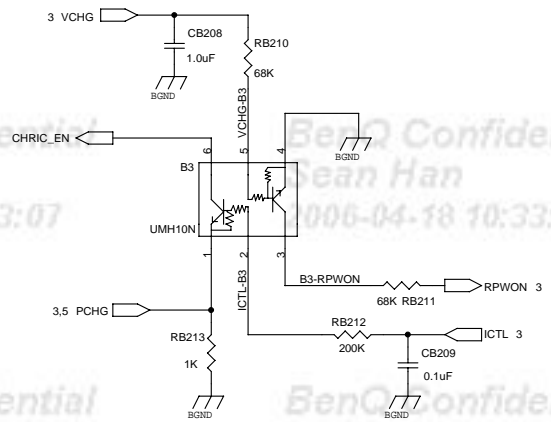
BenQ Corporation			
Project Code	Model Name	OEM/ODM Model Name	
9J.G0K21.001	HerB1A	OBM	
Title: LED Driver & 10pin IO			
Size	PCB P/N	PCB Rev.	Document Number
A4	4H.G0K01.S05	S05	9J.G0K21.000-C3-304-001
Date: Wednesday, March 15, 2006 Sheet 6 of 12			
Prepared By		Reviewed By	
Gent Tu		Anson Yang	
		Approved By	
		Weipeng Kao	

CHRIC SKT



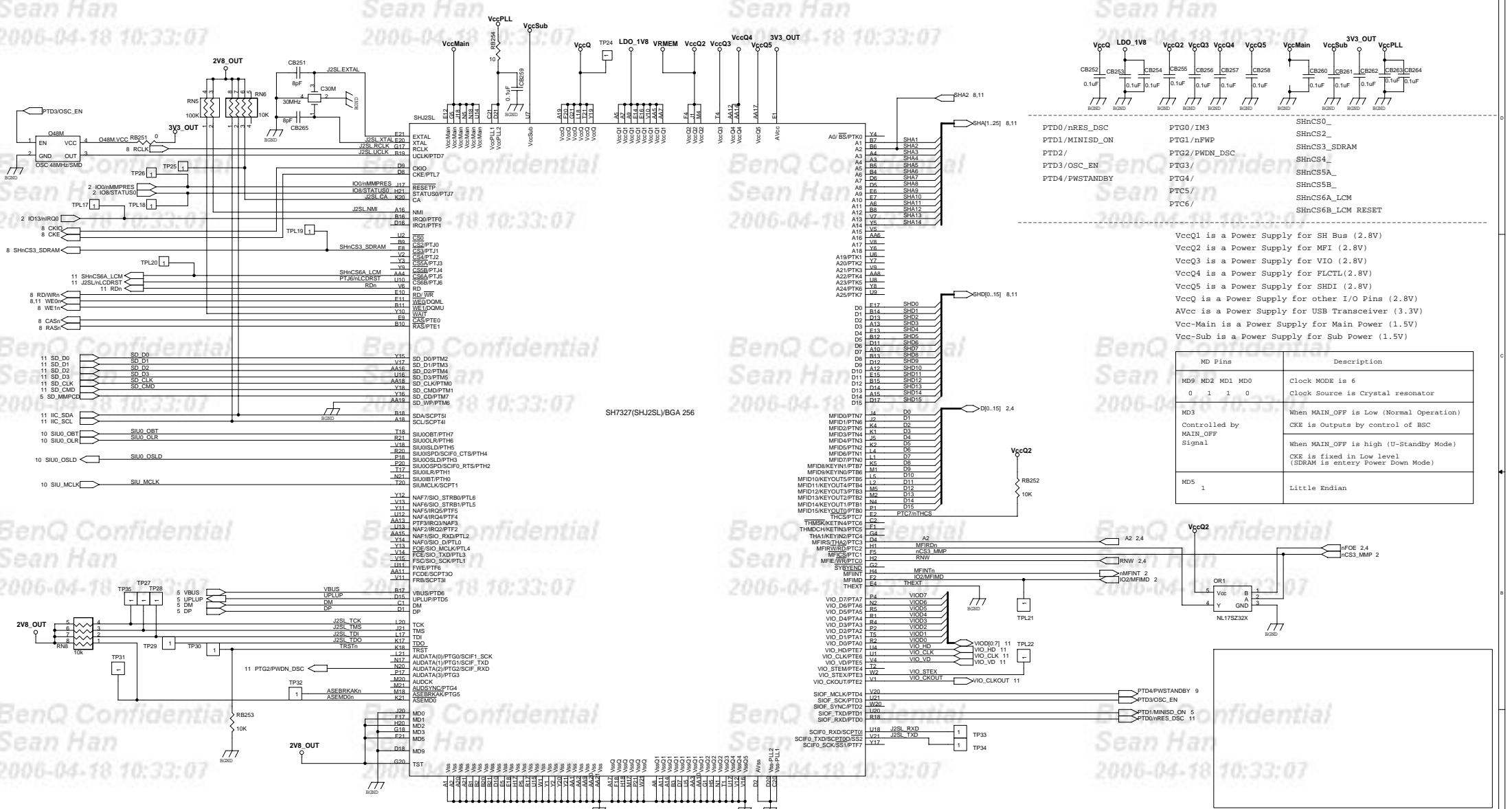
Battery charging circuit

CHRIC control SKT



RB:RB201~RB250
CB:CB201~CB250

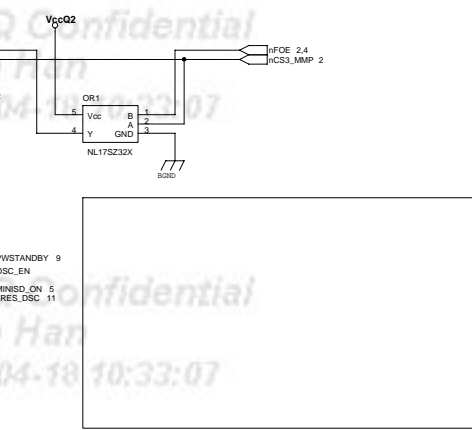
BenQ Corporation				
Project Code	Model Name	OEM/ODM Model Name		
9J.G0K21.001	HerB1A	OBM		
Title: Charging				
Size	PCB P/N	PCB Rev.	Document Number	Rev.
A4	4H.G0K01.S05	S05	9J.G0K21.000-C3-304-001	0
Date:	Wednesday, March 15, 2006	Sheet	7	of 12
Prepared By	Reviewed By	Approved By		
Gent Tu	Anson Yang	Weipeng Kao		



PTD0/nRES_DSC	PTG0/IM3	SHNC50_
PTD1/MINISD_ON	PTG1/fW/P	SHNC52_
PTD2/	PTG2/PWDN_DSC	SHNC53_SDRAM
PTD3/OSC_EN	PTG3/	SHNC54_
PTD4/fWSTANDBY	PTG4/	SHNC55A_
	PTCS/	SHNC55B_
	PTCC/	SHNC56A_LCM
		SHNC56B_LCM RESET

VccQ1 is a Power Supply for SH Bus (2.8V)
VccQ2 is a Power Supply for MFI (2.8V)
VccQ3 is a Power Supply for VIO (2.8V)
VccQ4 is a Power Supply for PLCTL (2.8V)
VccQ5 is a Power Supply for SHDI (2.8V)
VccQ is a Power Supply for other I/O Pins (2.8V)
AVcc is a Power Supply for USB Transceiver (3.3V)
Vcc-Main is a Power Supply for Main Power (1.5V)
Vcc-Sub is a Power Supply for Sub Power (1.5V)

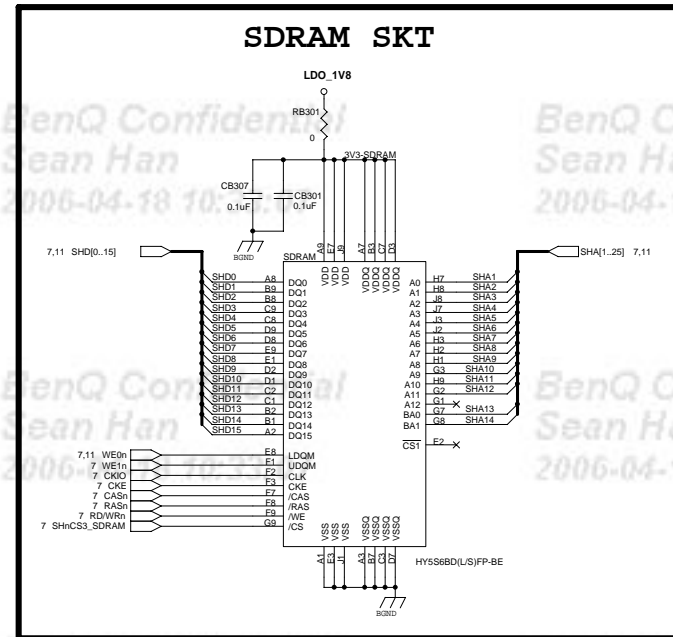
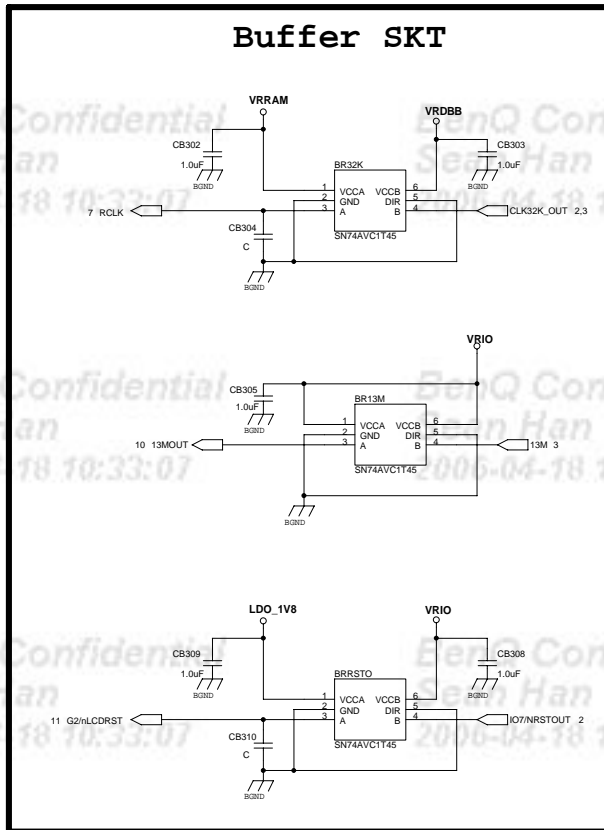
MD Pins	Description
MD9 MD2 MD1 MD0	Clock MODE is 6 Clock Source is Crystal resonator
MD3	When MAIN_OFF is Low (Normal Operation) CKE is Outputs by control of BSC When MAIN_OFF is high (U-Standby Mode) CKE is fixed in Low level (SDRAM is entry Power Down Mode)
MD5 1	Little Endian



RB:RB251-RB300
CB:CB251-CB300

BenQ Corporation

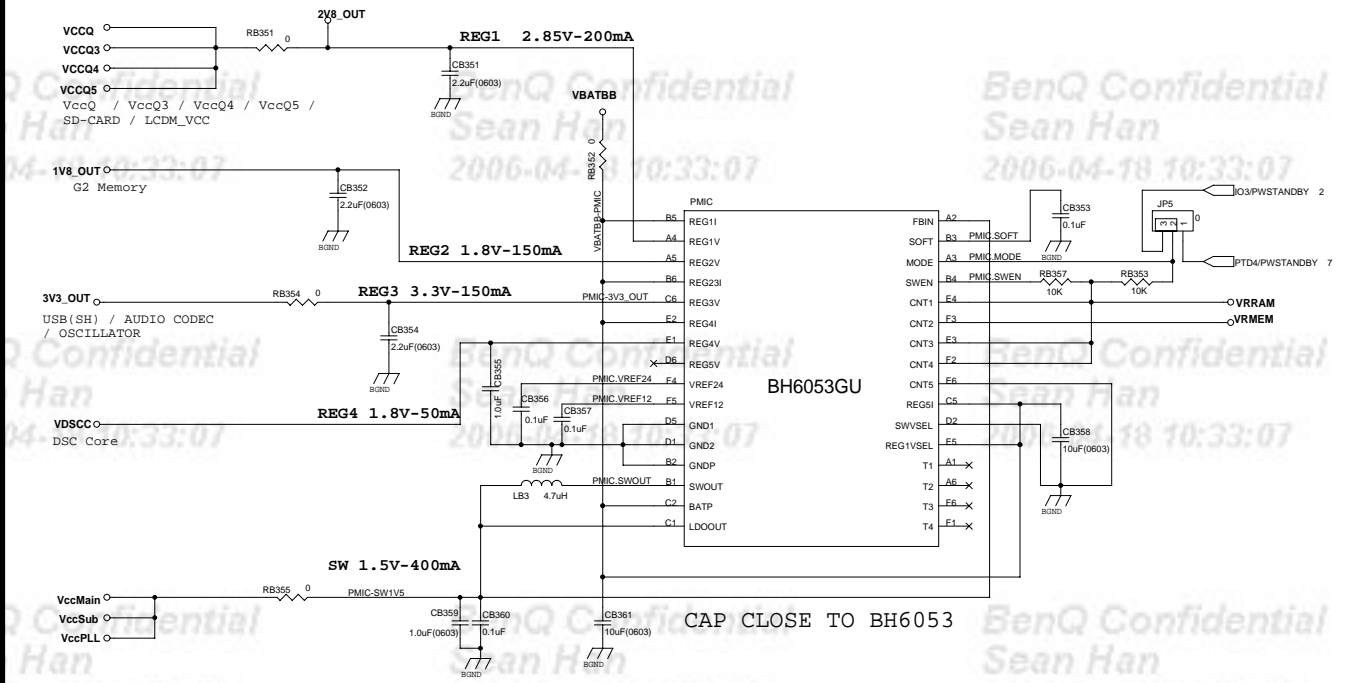
Project Code 9J.GK021.001	Model Name HerB1A	CEM/COM Model Name OBM
Title SH-Mobile J2SL		
Size A4	PCB P/N 4H.GK01.S05	Document Number 9J.GK021.000-C3-304-001
Date Wednesday, March 15, 2006	Prepared By Gent Tu	Sheet 8 of 12
	Reviewed By Arison Yang	Approved By Weiping Kao



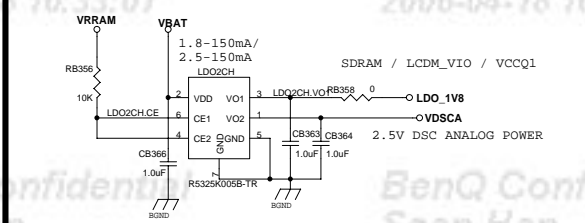
RB:RB301~RB350
CB:CB301~CB350

BenQ Corporation			
Project Code	Model Name	OEM/ODM Model Name	
9J.G0K21.001	HerB1A	OBM	
Title: SDRAM			
Size	PCB P/N	Document Number	Rev.
A4	4H.G0K01.S05	9J.G0K21.000-C3-304-001	0
Date:	Wednesday, March 15, 2006	Sheet	9 of 12
Prepared By	Reviewed By	Approved By	
Gent Tu	Anson Yang	Weipeng Kao	

PM IC SKT



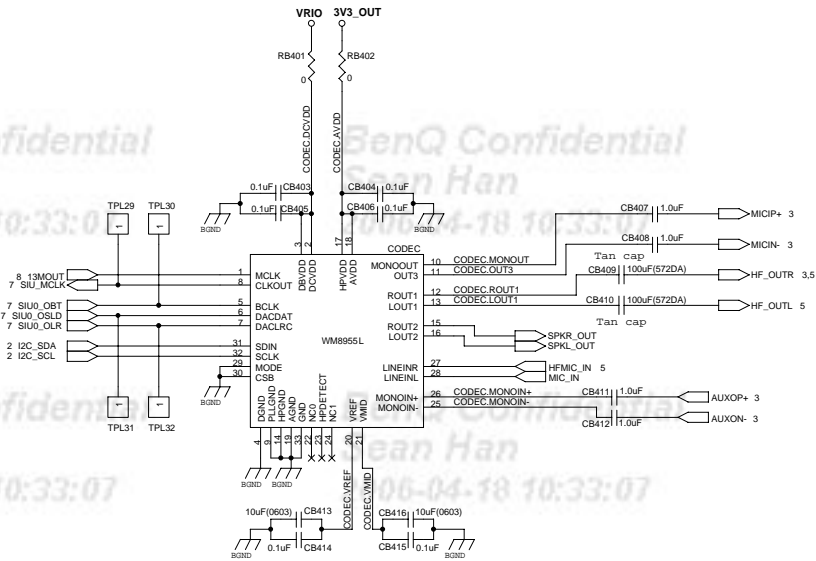
2_CH LDO SKT



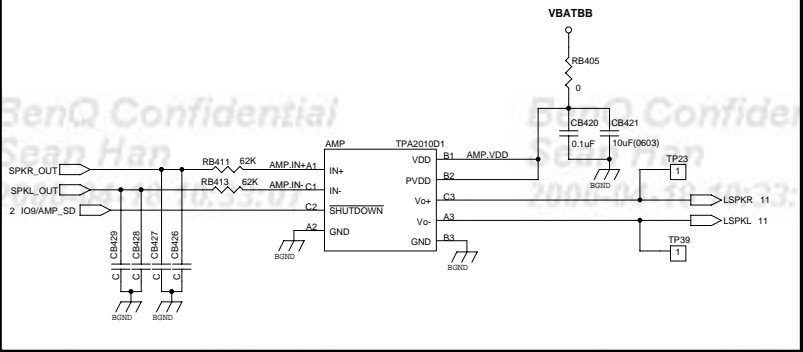
RB:RB351~RB400
 CB:CB351~CB400

BenQ Corporation		
Project Code	Model Name	OEM/ODM Model Name
9J.G0K21.001	HerB1A	OBM
Title: External Power		
Size	PCB P/N	PCB Rev.
A4	4H.G0K01.S05	S05
Document Number	Rev.	0
9J.G0K21.000-C3-304-001		
Date:	Wednesday, March 15, 2006	Sheet 10 of 12
Prepared By	Reviewed By	Approved By
Gent Tu	Anson Yang	Weipeng Kao

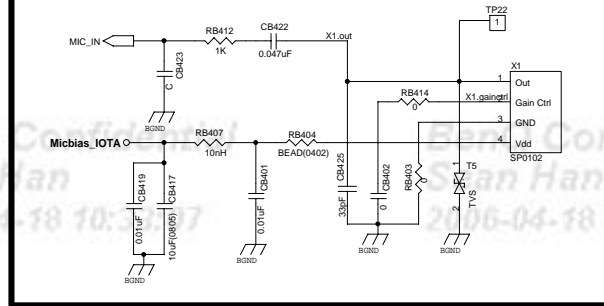
AUDIO CODEC SKT



LSPK& Audio AMP SKT

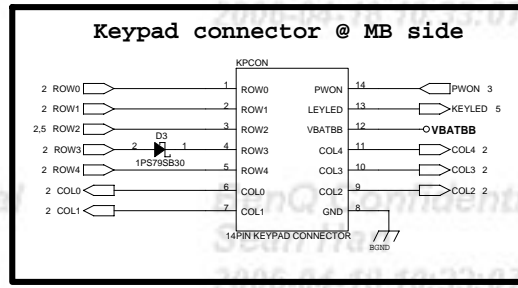
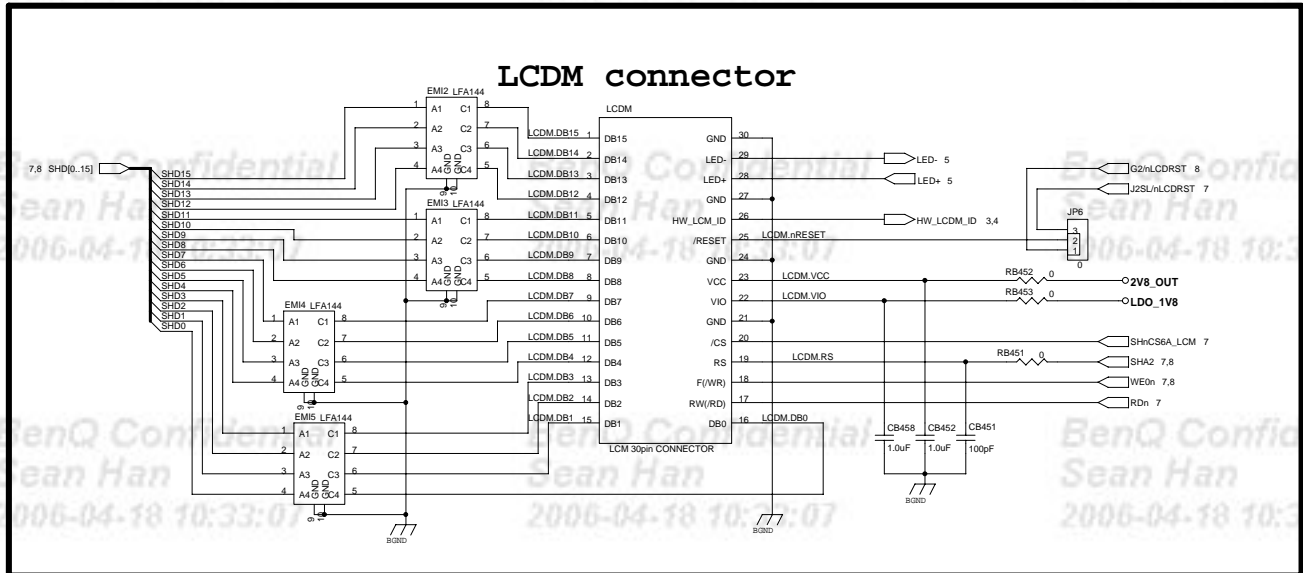


Microphone SKT

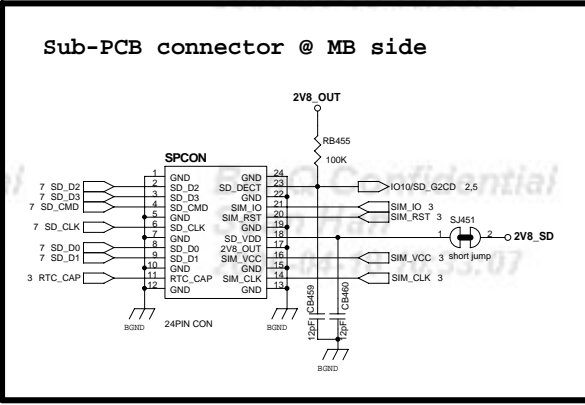
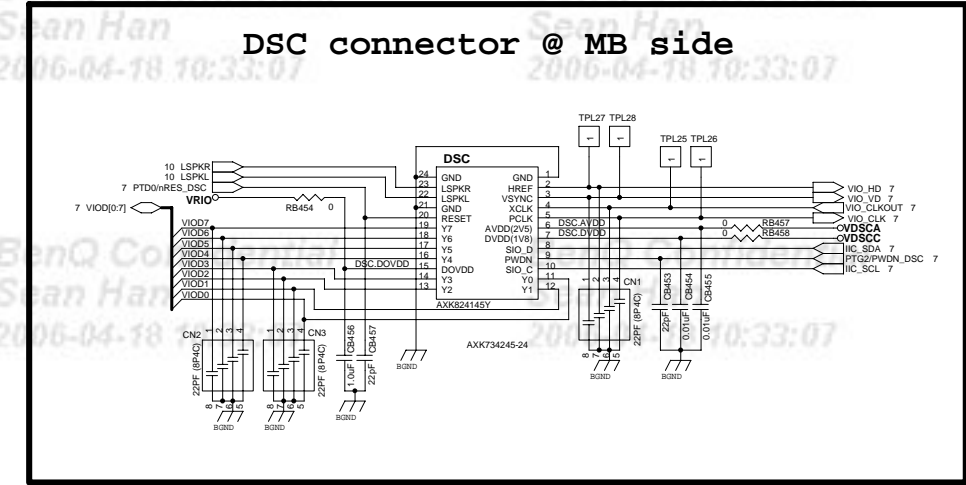
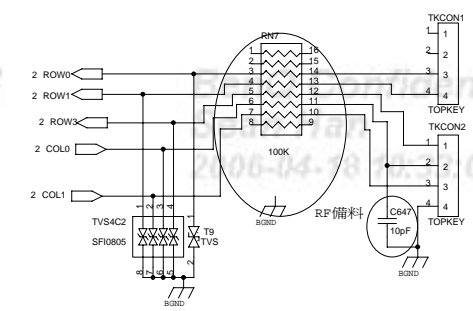


RB:RB401-RB450
CB:CB401-CB450

BenQ Corporation			
Project Code	9J.G0K21.001	Model Name	HerB1A
		OEM/ODM Model Name	OBM
Title	Audio codec		
Size	A4	PCB P/N	4H.G0K01.S05
		Document Number	9J.G0K21.000-C3-304-001
		Rev.	0
Date:	Wednesday, March 15, 2006	Sheet	11 of 12
Prepared By	Gent Tu	Reviewed By	Anson Yang
		Approved By	Weipeng Kao



TopKey connector @ MB side



RB:RB451-RB500
CB:CB451-CB500

BenQ Corporation			
Project Code	Model Name	OEM/ODM Model Name	
9J.G0K21.001	HerB1A	OBM	
Title: B2B Connector			
Size A4	PCB P/N 4H.G0K01.S05	Document Number 9J.G0K21.000-C3-304-001	Rev. 0
Date: Wednesday, March 15, 2006	Sheet 12 of 12		
Prepared By: Gent Tu	Reviewed By: Anson Yang	Approved By: Weipeng Kao	