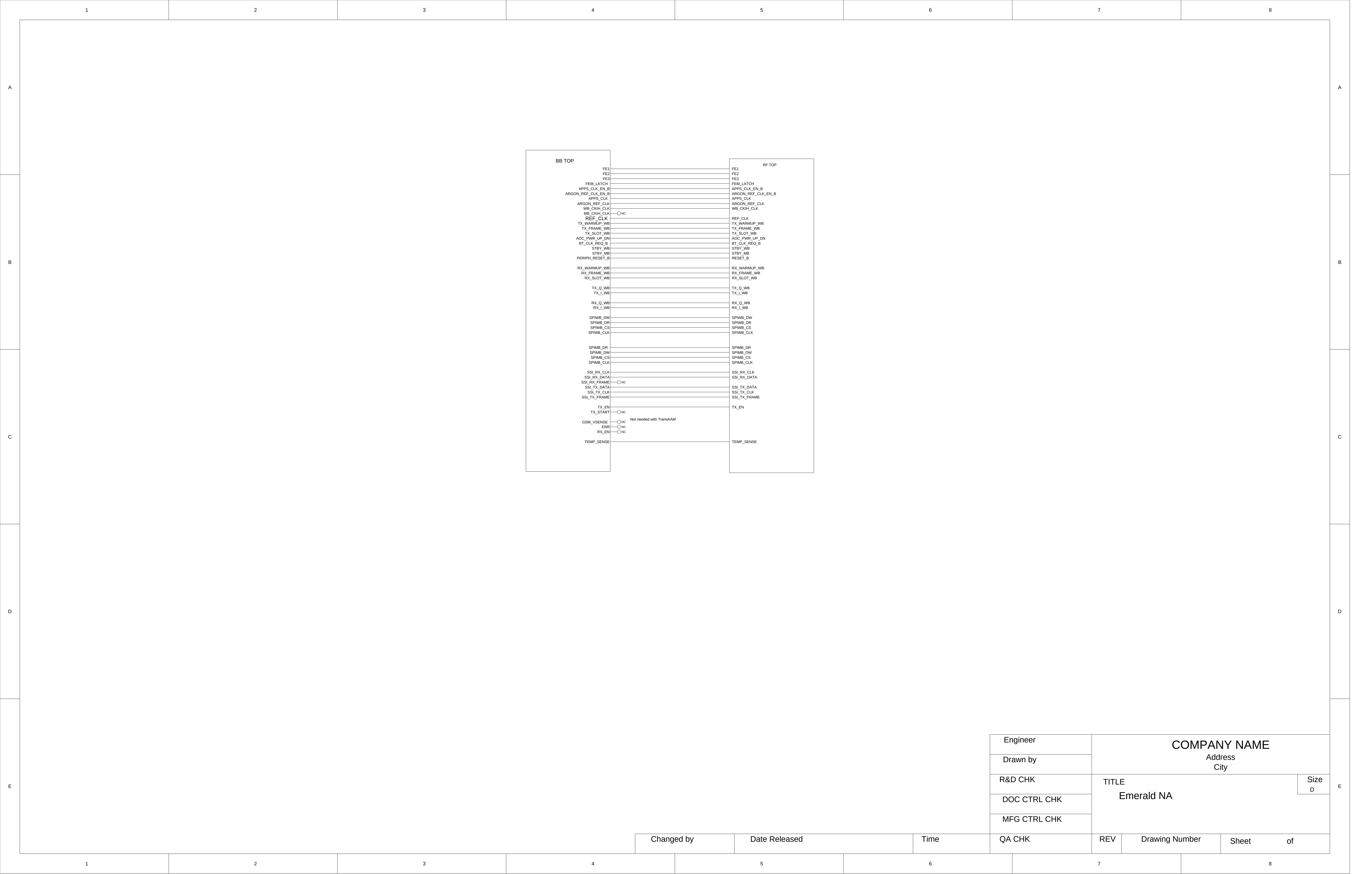
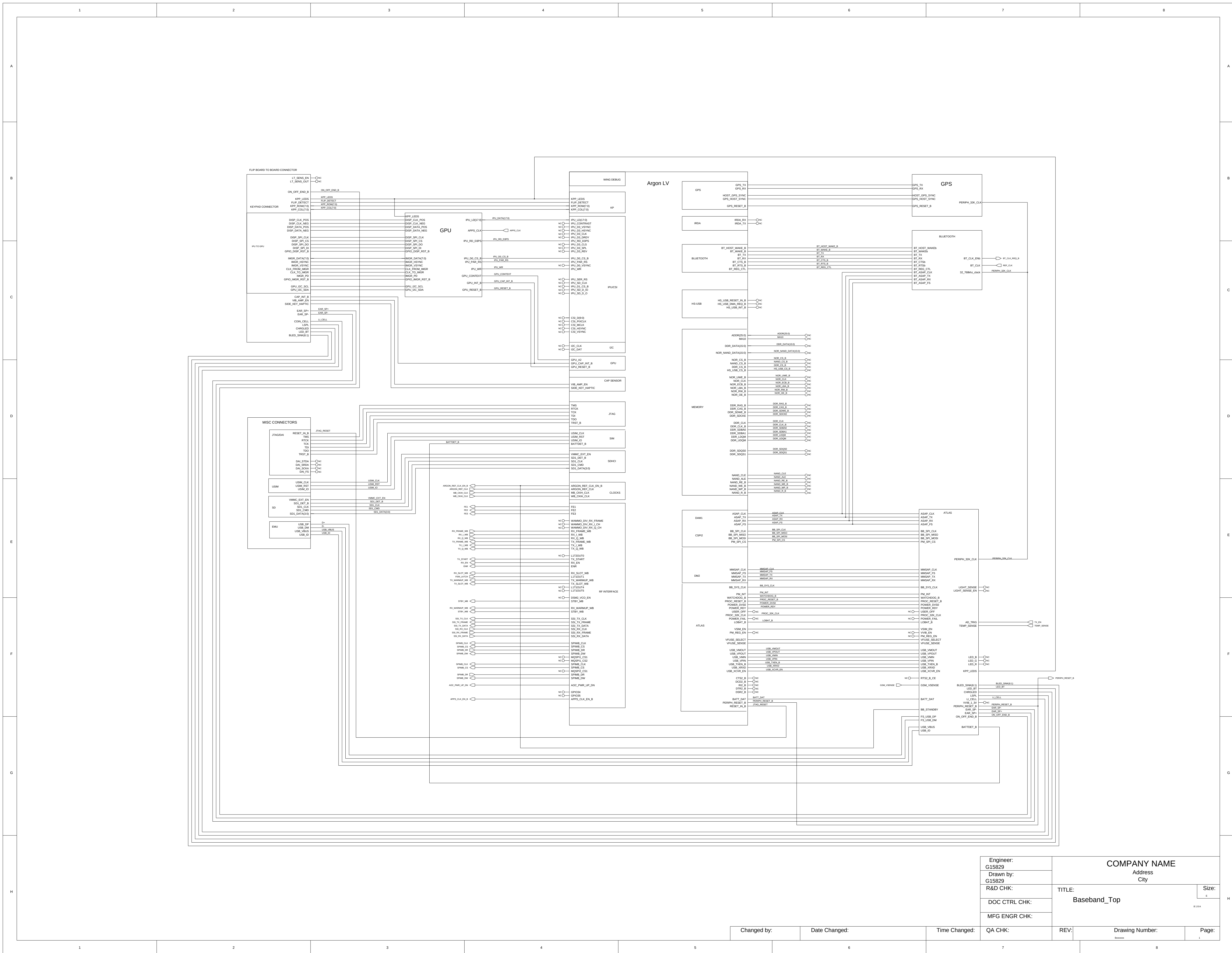




Engineer	COMPANY NAME Address City		
Drawn by			
R&D CHK	TITLE Emerald NA		Size D
DOC CTRL CHK			
MFG CTRL CHK			
QA CHK	REV	Drawing Number	Sheet of

Changed by	Date Released	Time
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1

2

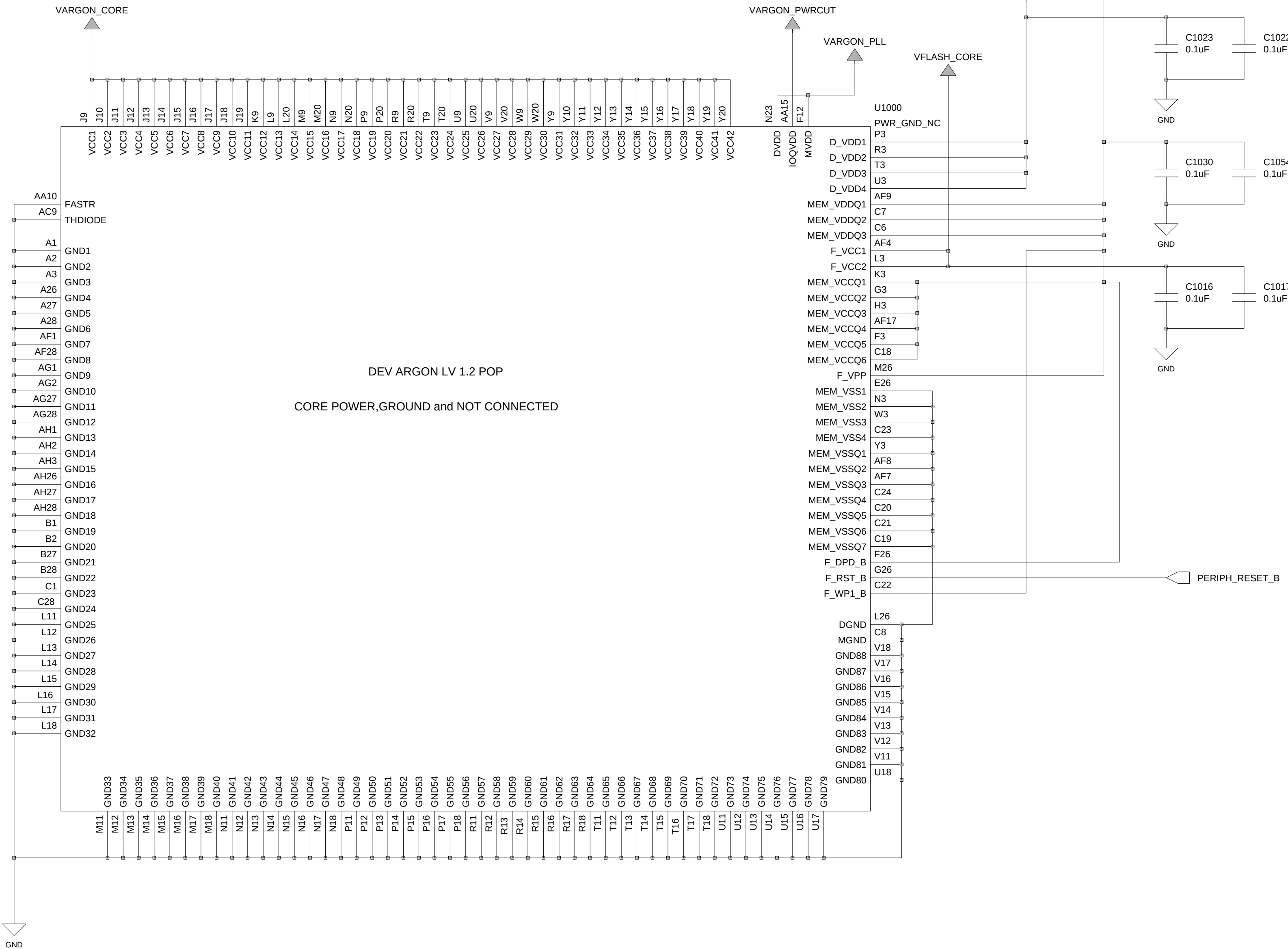
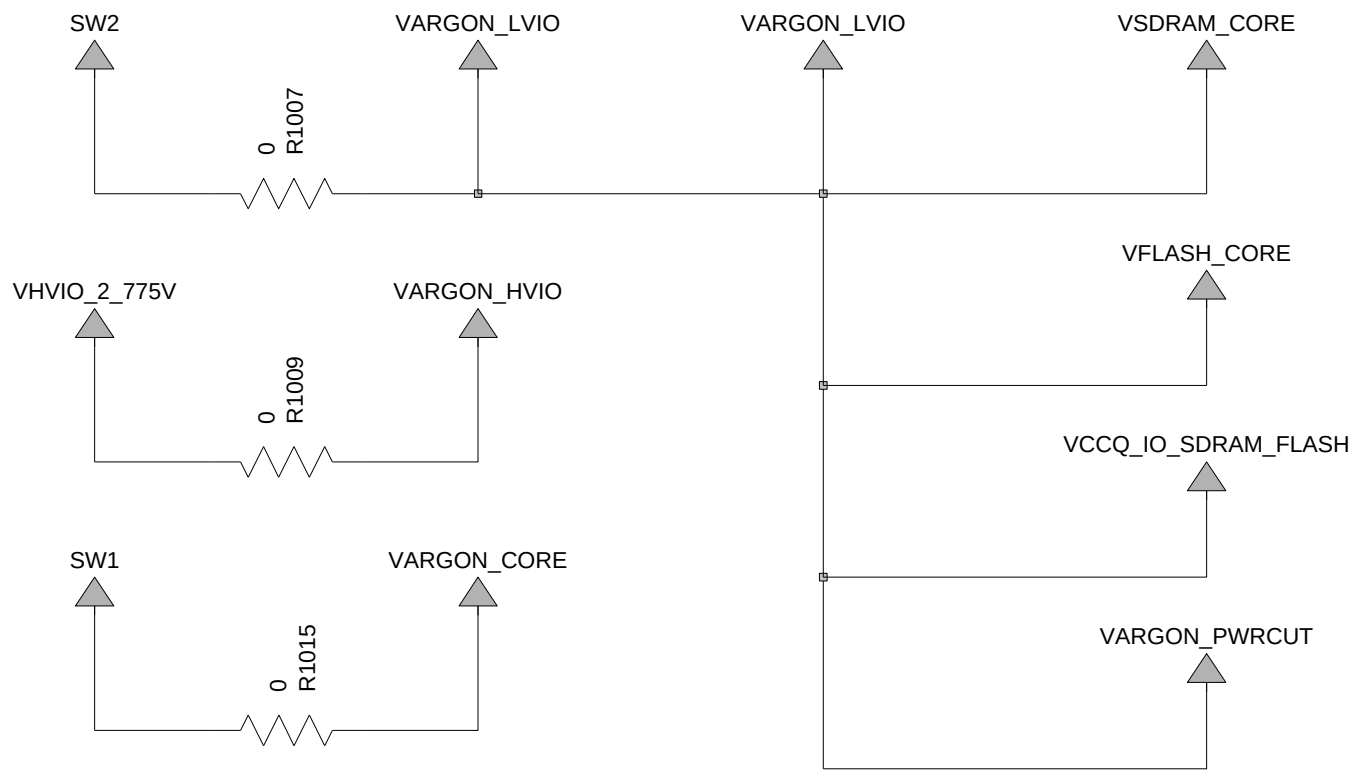
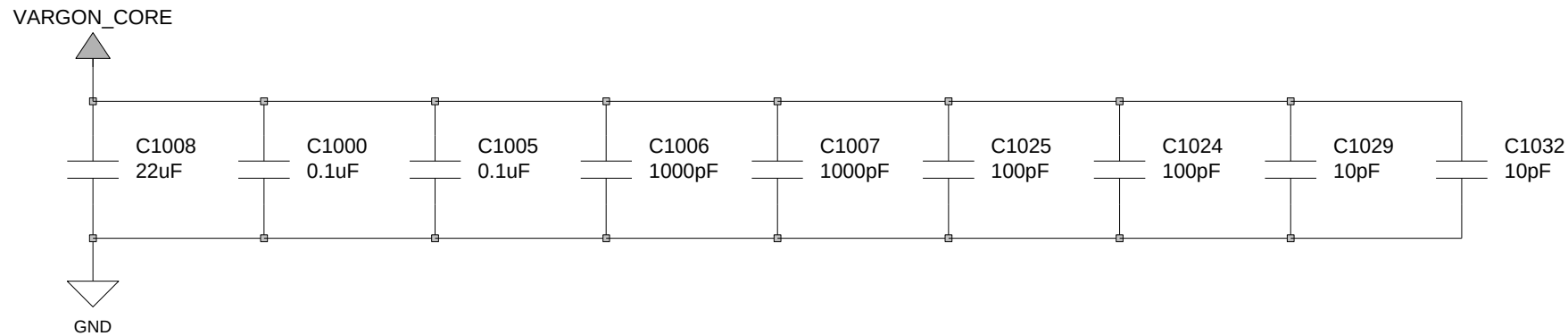
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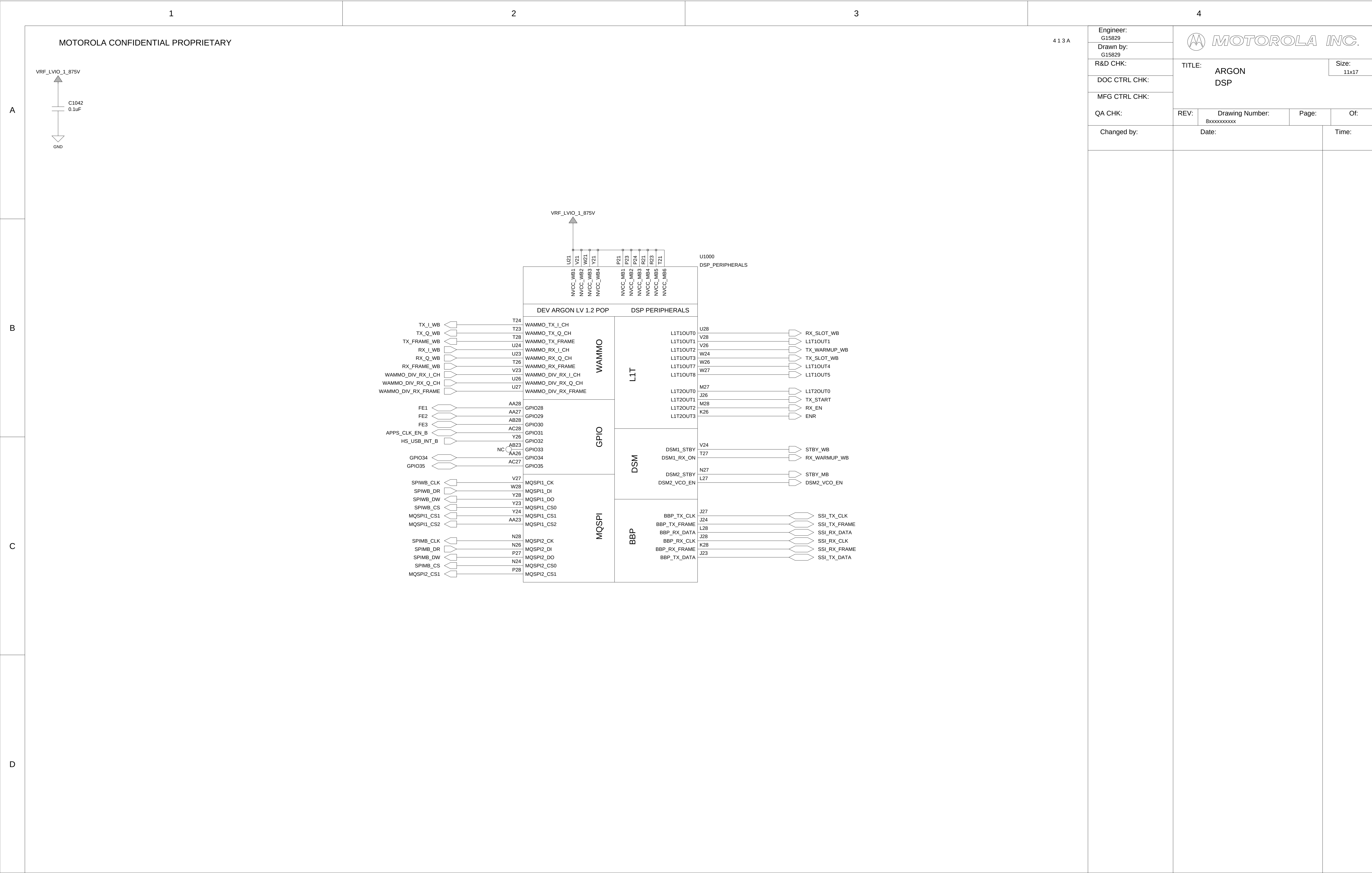
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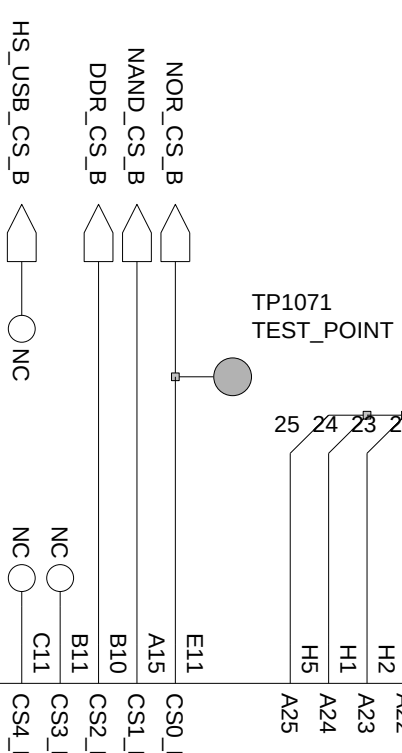
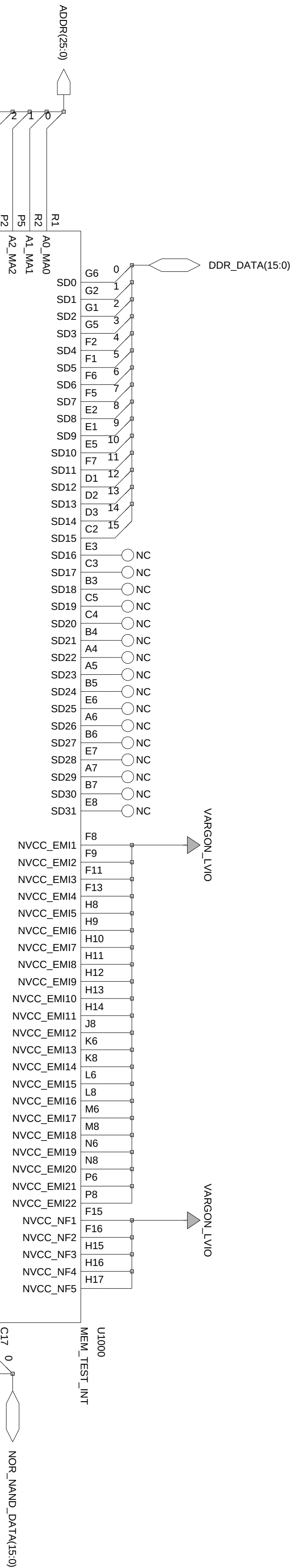
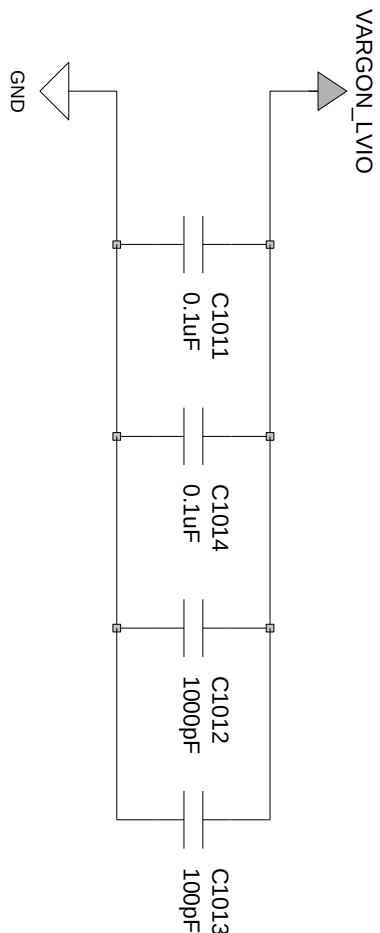
4 1 3 A

Engineer: G15829	 MOTOROLA INC.			
Drawn by: G15829				
R&D CHK:	TITLE: ARGON Core_Power			Size: 11x17
DOC CTRL CHK:				
MFG CTRL CHK:				
QA CHK:	REV:	Drawing Number:	Page:	Of:
Changed by:	Date:			Time:





MOTOROLA CONFIDENTIAL PROPRIETARY



5199213K02 (2x32/64/64 - Spansion NOR / Hynix NAND / Micron DDR)
Placeholder for 5199213K03 (64/64/64 - Spansion NOR / Hynix NAND / Micron DDR)

4.13 A

Engineer:
G15829

Drawn by:
G15829

DOC CTRL CHK:

MFG CTRL CHK:

QA CHK:

Changed by:

MOTOROLA INC.

TITLE: ARGON Memory

Size: 11x17

REV: 8xxxxxxx

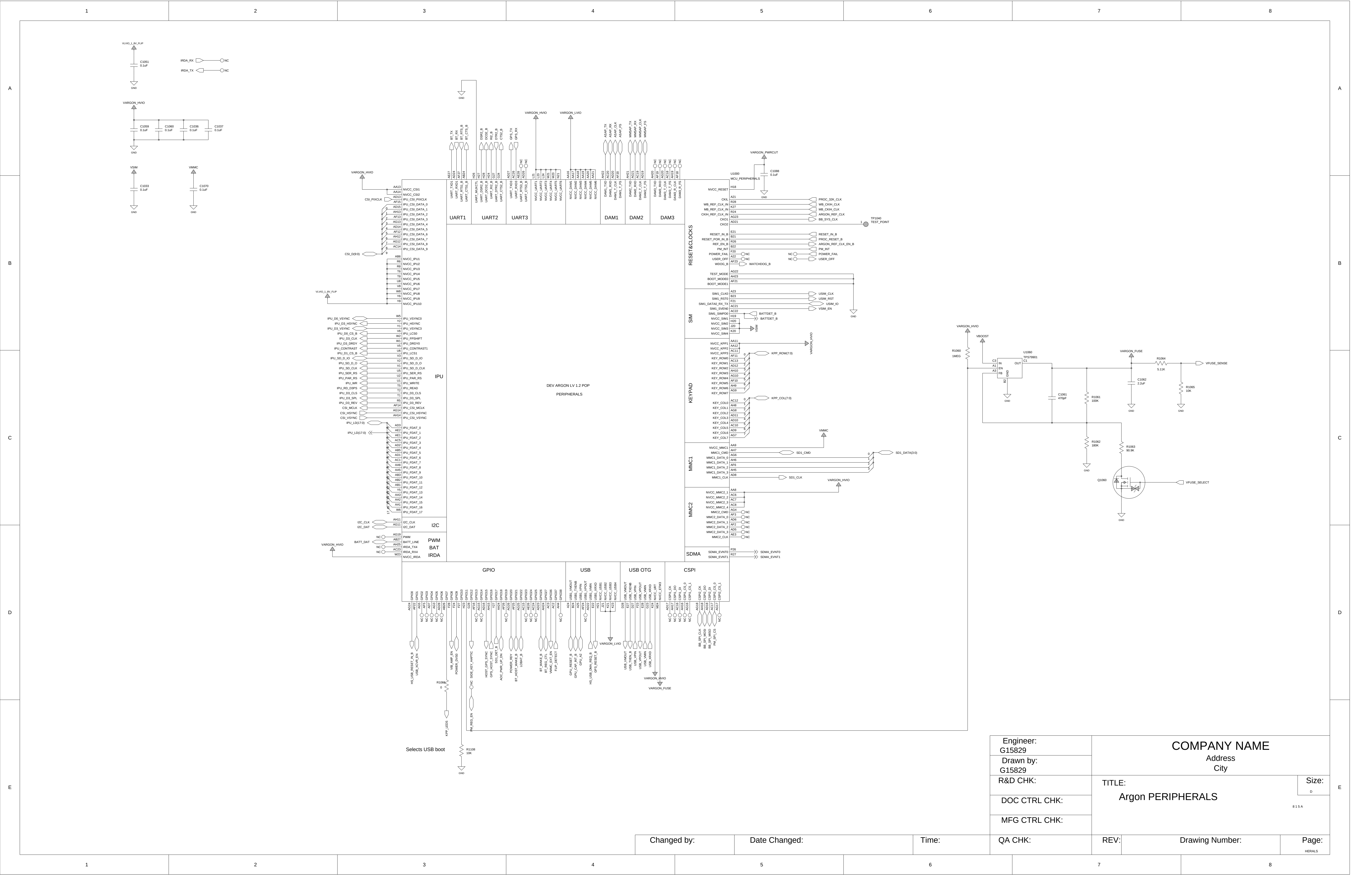
Drawing Number:

Page:

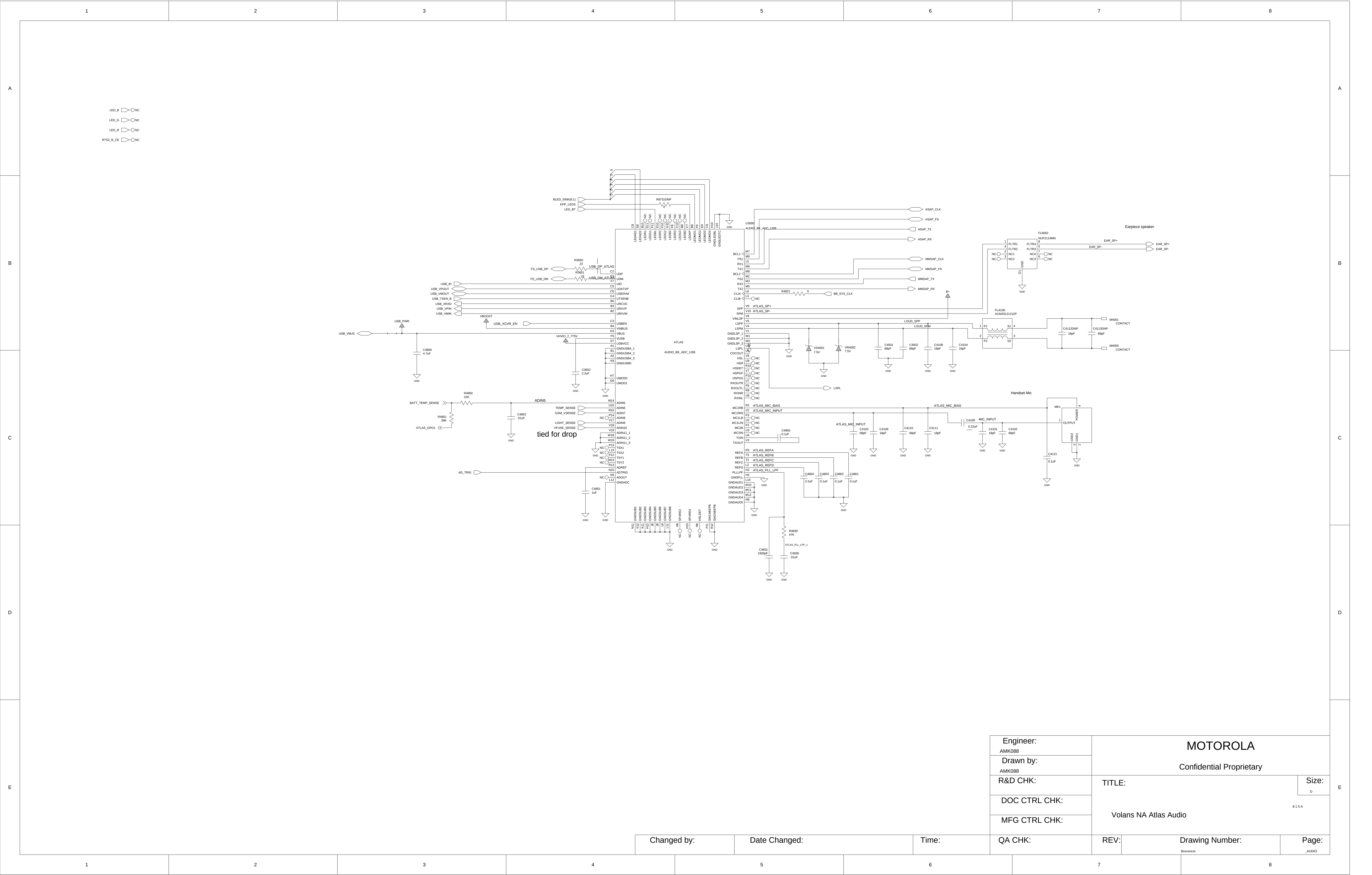
Of:

Date:

Time:

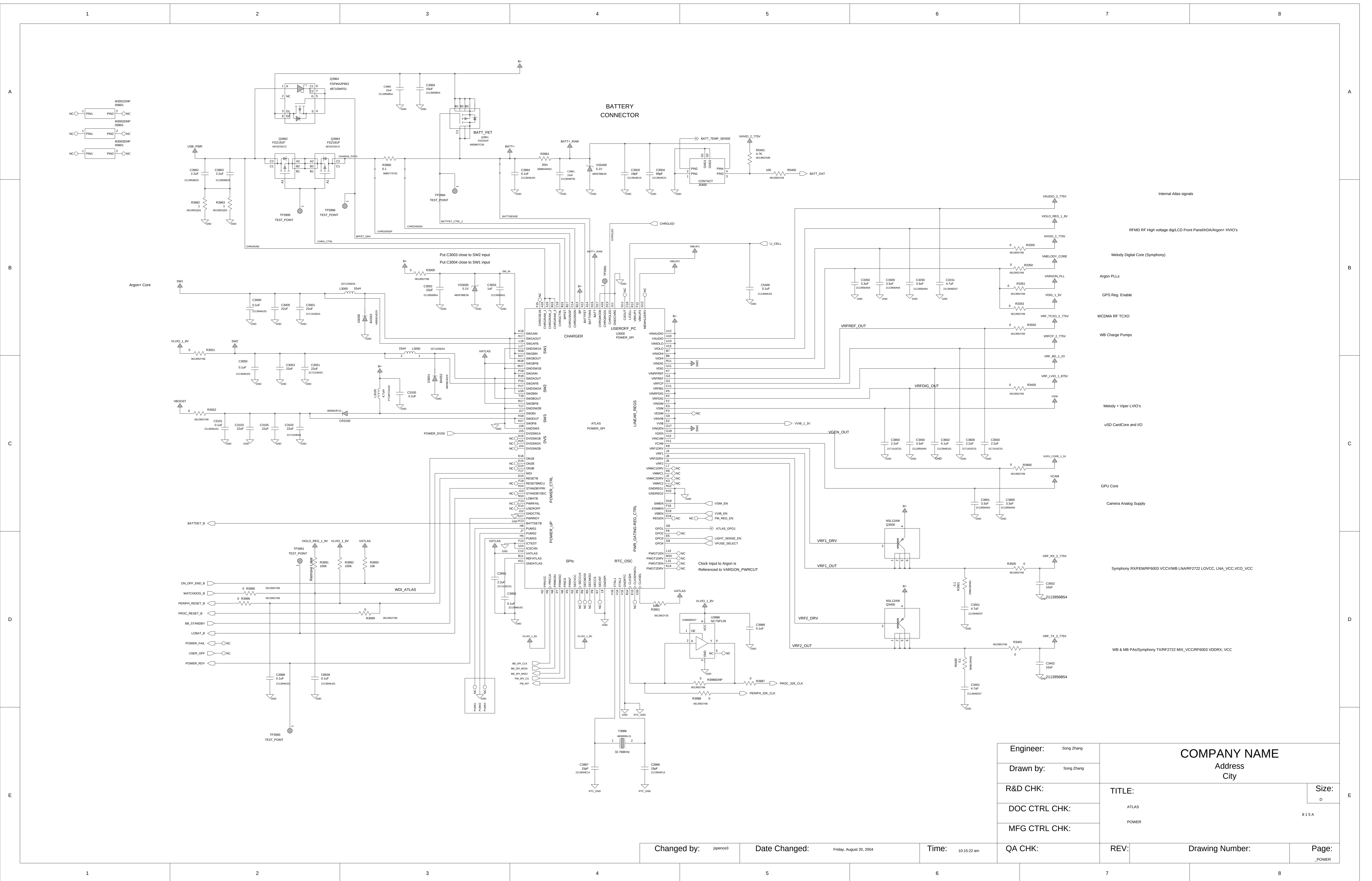


Engineer: G15829	COMPANY NAME Address City		
Drawn by: G15829			
R&D CHK:	TITLE: Argon PERIPHERALS	Size: D	
DOC CTRL CHK:			
MFG CTRL CHK:			
QA CHK:	REV:	Drawing Number:	Page: HERALS



Engineer: AMK088	MOTOROLA Confidential Proprietary		
Drawn by: AMK088			
R&D CHK:	TITLE: Volans NA Atlas Audio		Size: D
DOC CTRL CHK:			815A
MFG CTRL CHK:			
QA CHK:	REV:	Drawing Number: Sxxxxxxx	Page: _AUDIO

Changed by:	Date Changed:	Time:
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MOTOROLA CONFIDENTIAL PROPRIETARY

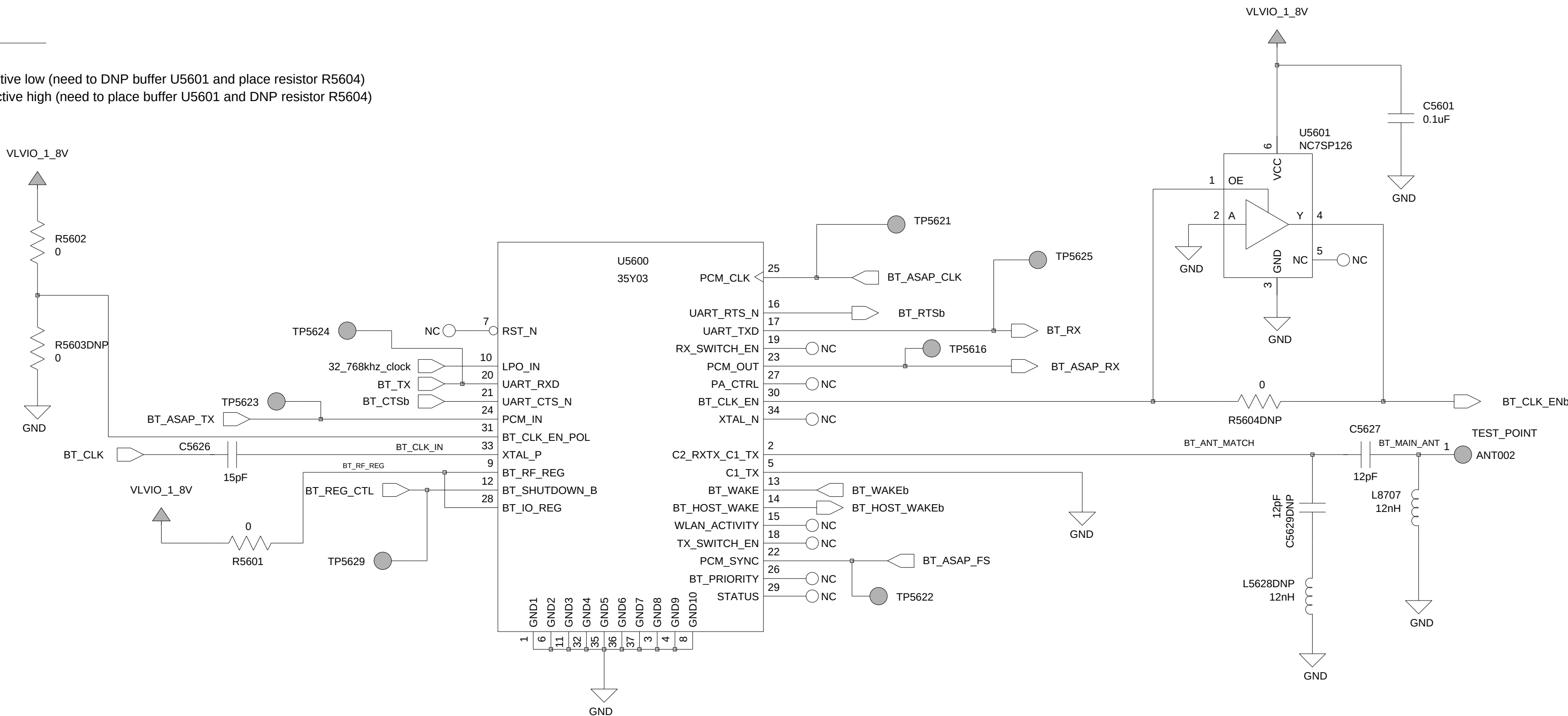
MOTOROLA INTERNAL USE ONLY

4 1 3 A

Engineer: W17698					
Drawn by: W17698					
R&D CHK:		TITLE: BLUETOOTH		Size: 11x17	
DOC CTRL CHK:					
MFG CTRL CHK:					
QA CHK:	REV: 8xxxxxxx	Drawing Number:		Page:	Of:
Changed by:		Date:		Time:	

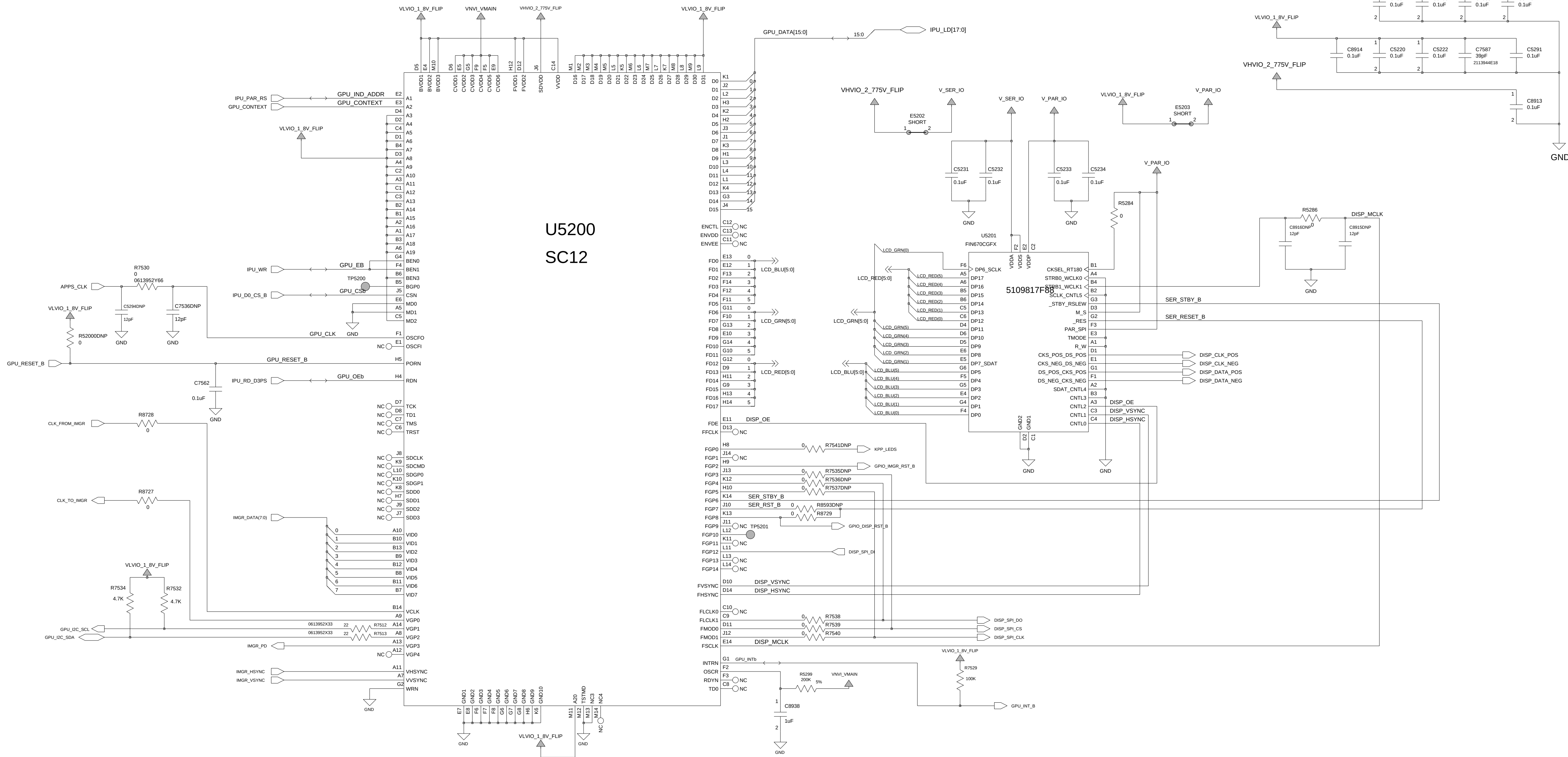
BT_CLK_EN polarity

BT_CLK_EN_POL	Description
0	Active low (need to DNP buffer U5601 and place resistor R5604)
1	Active high (need to place buffer U5601 and DNP resistor R5604)



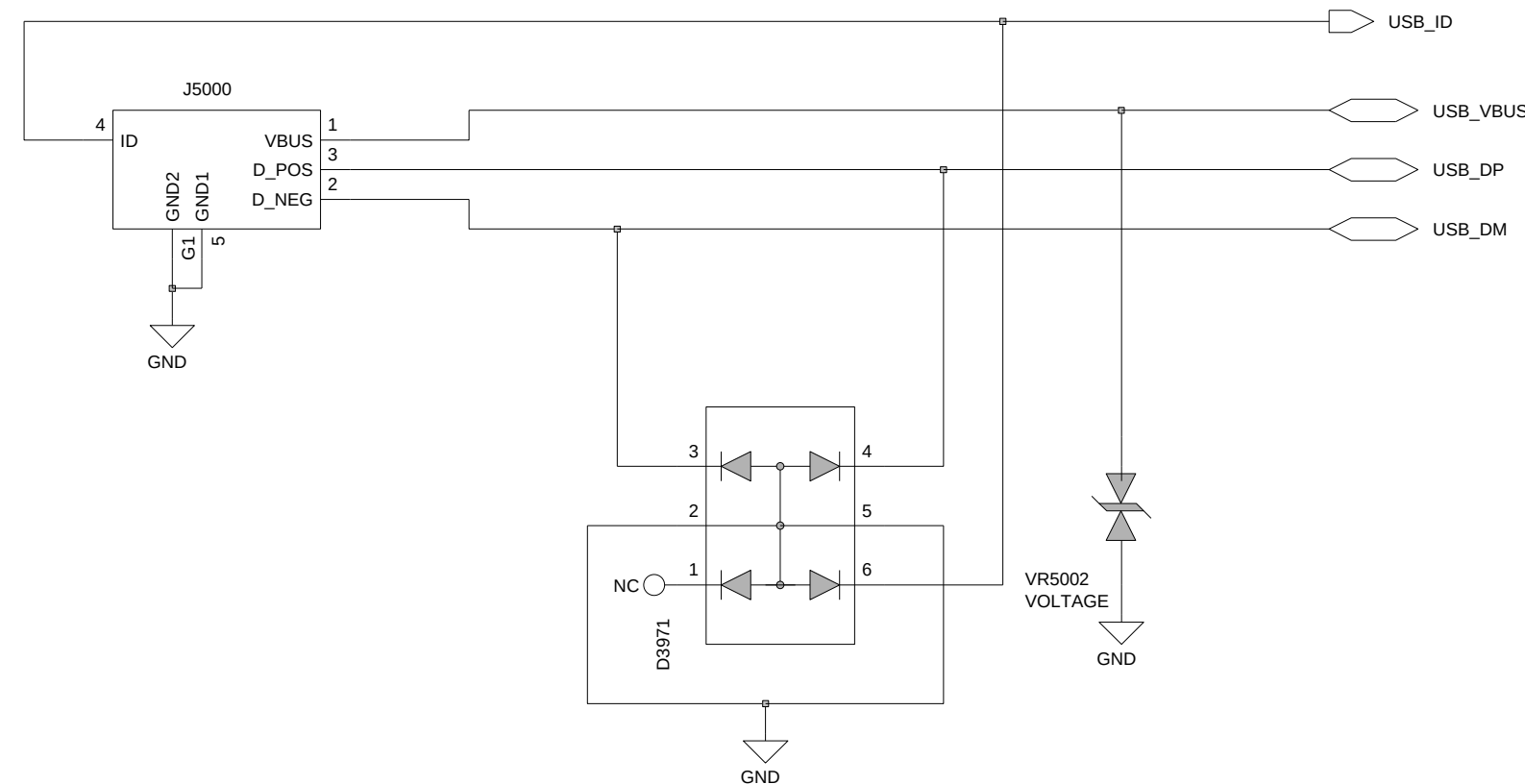
413A

Date:	Time:
Wednesday, June 7, 2006	3:13:10 pm

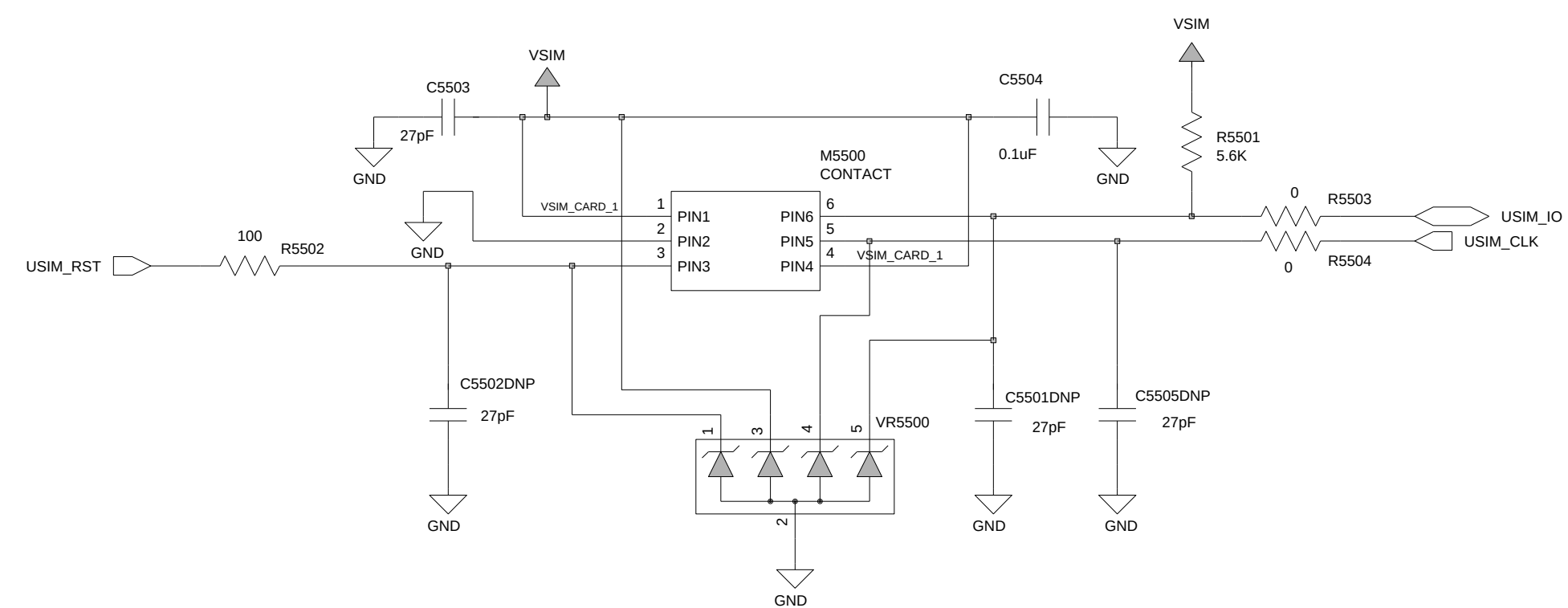


MOTOROLA CONFIDENTIAL PROPRIETARY

EMU CONNECTOR



USIM



Engineer:

Drawn by:	
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R&D CHK:

TITLE:

Size:

MFG CTRL CHK:

QA CHK:

	D
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Page:

Of:

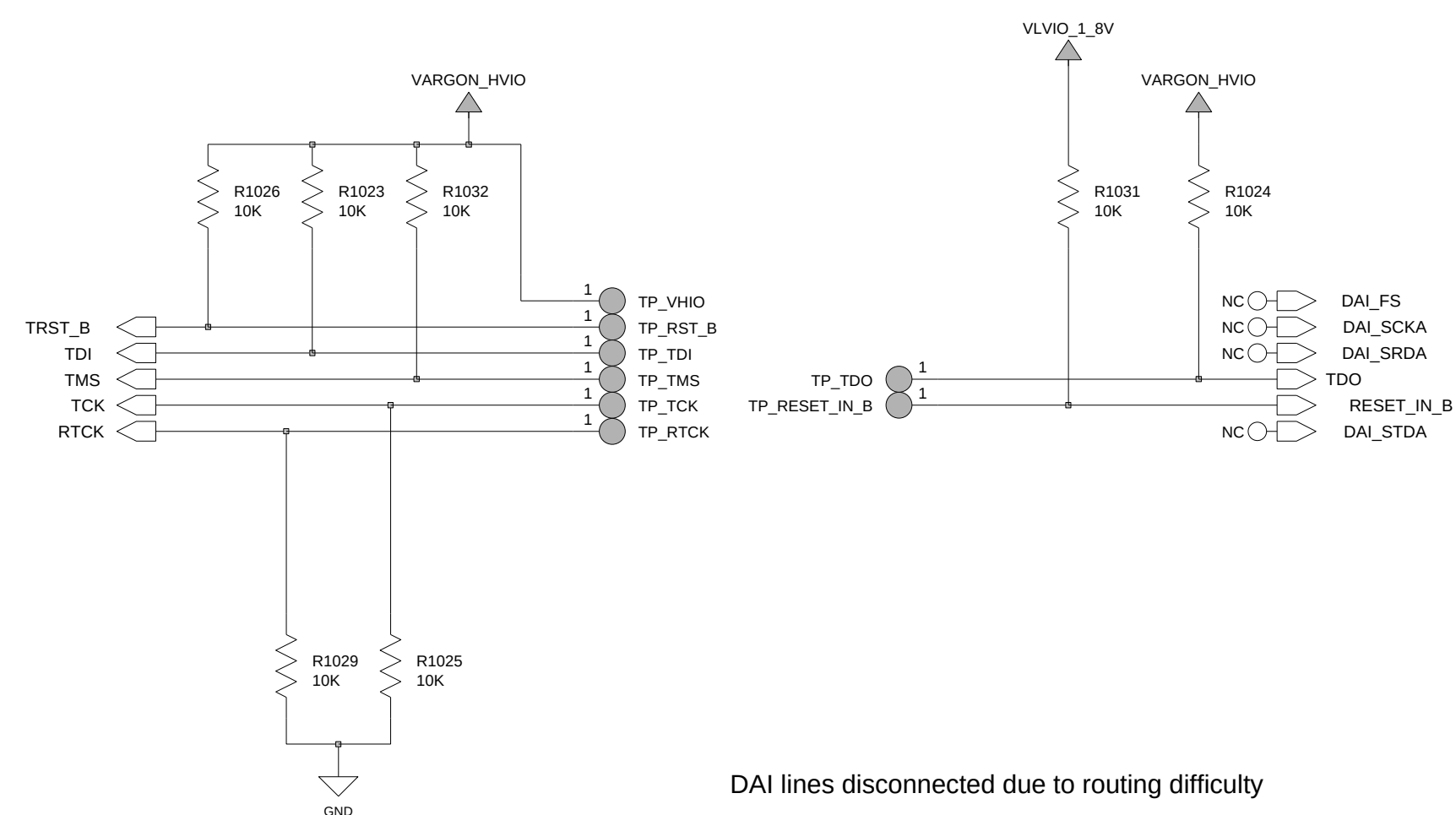
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Date:

time:

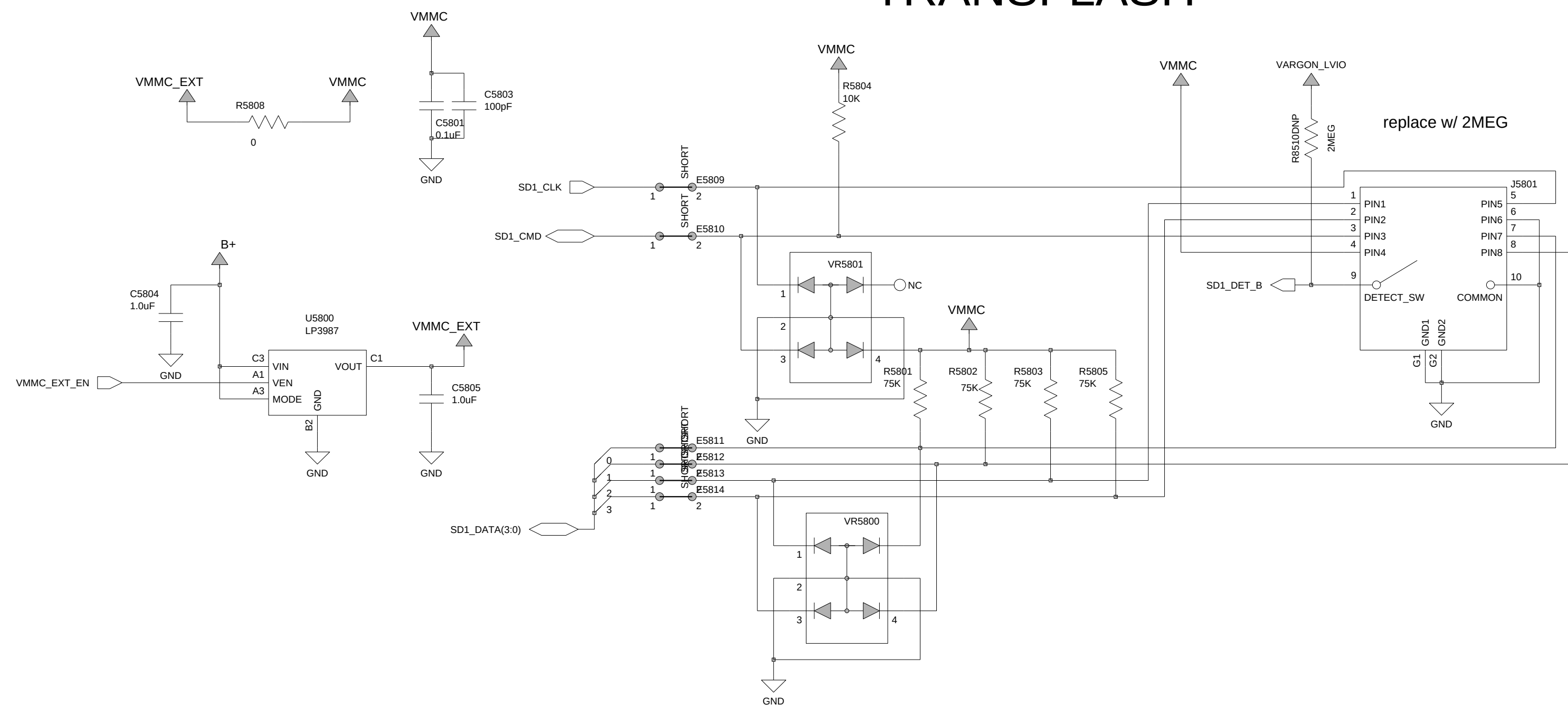
JTAG/DAI

12-pin JTAG connector



DAI lines disconnected due to routing difficulty

TRANSFLASH



replace w/ 2MEG

