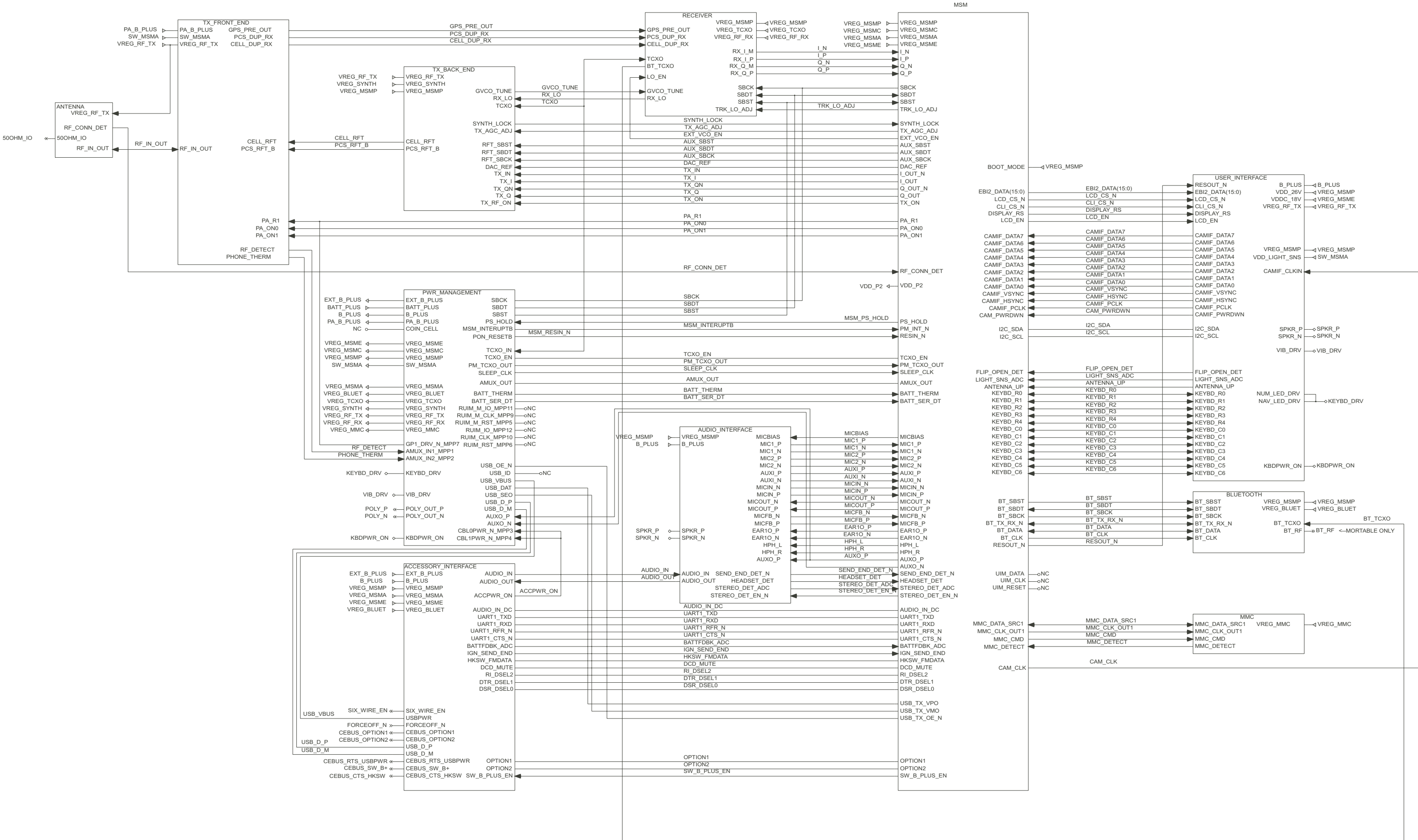
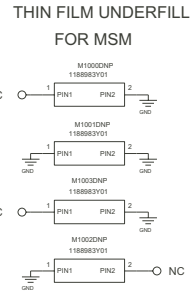
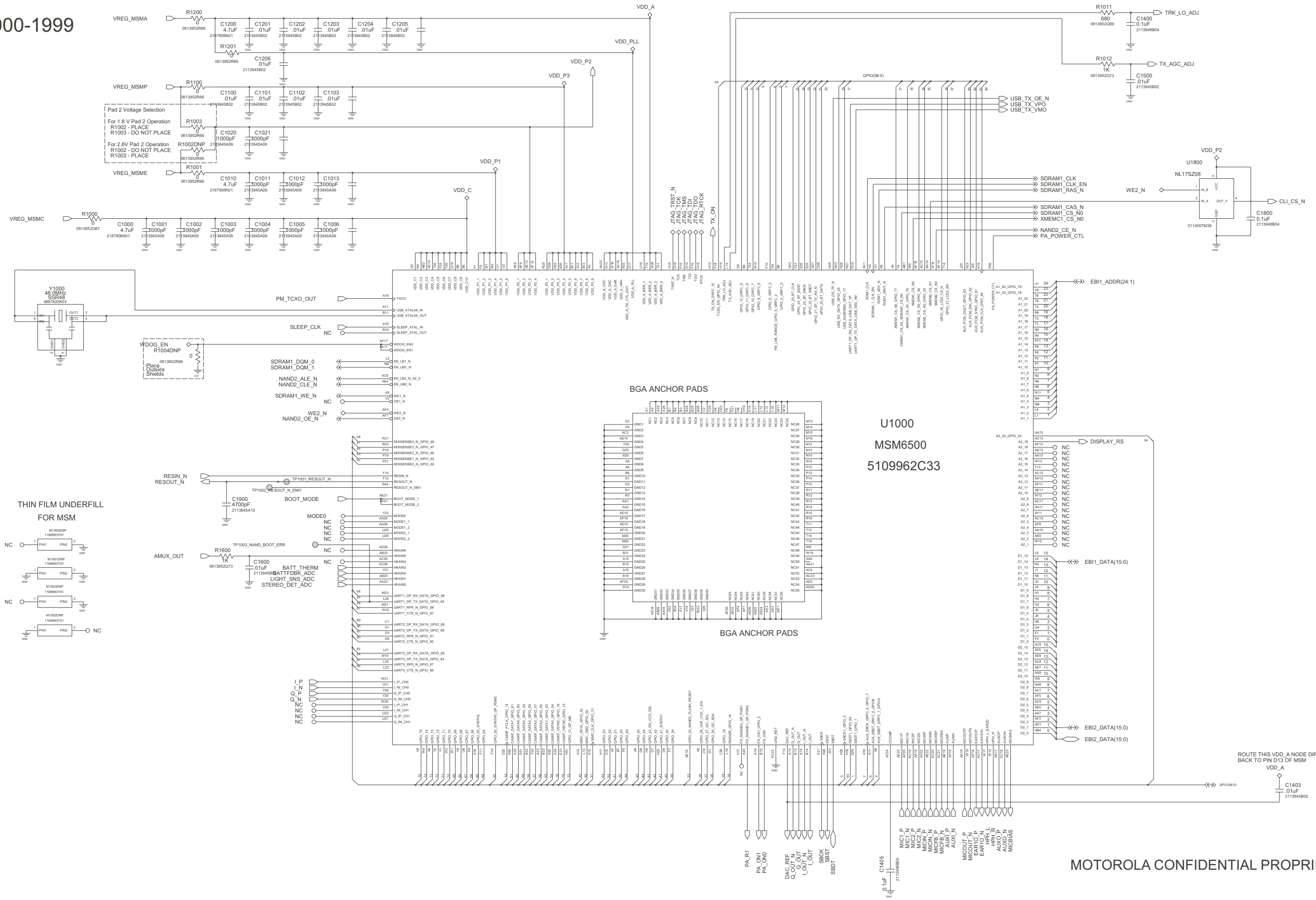


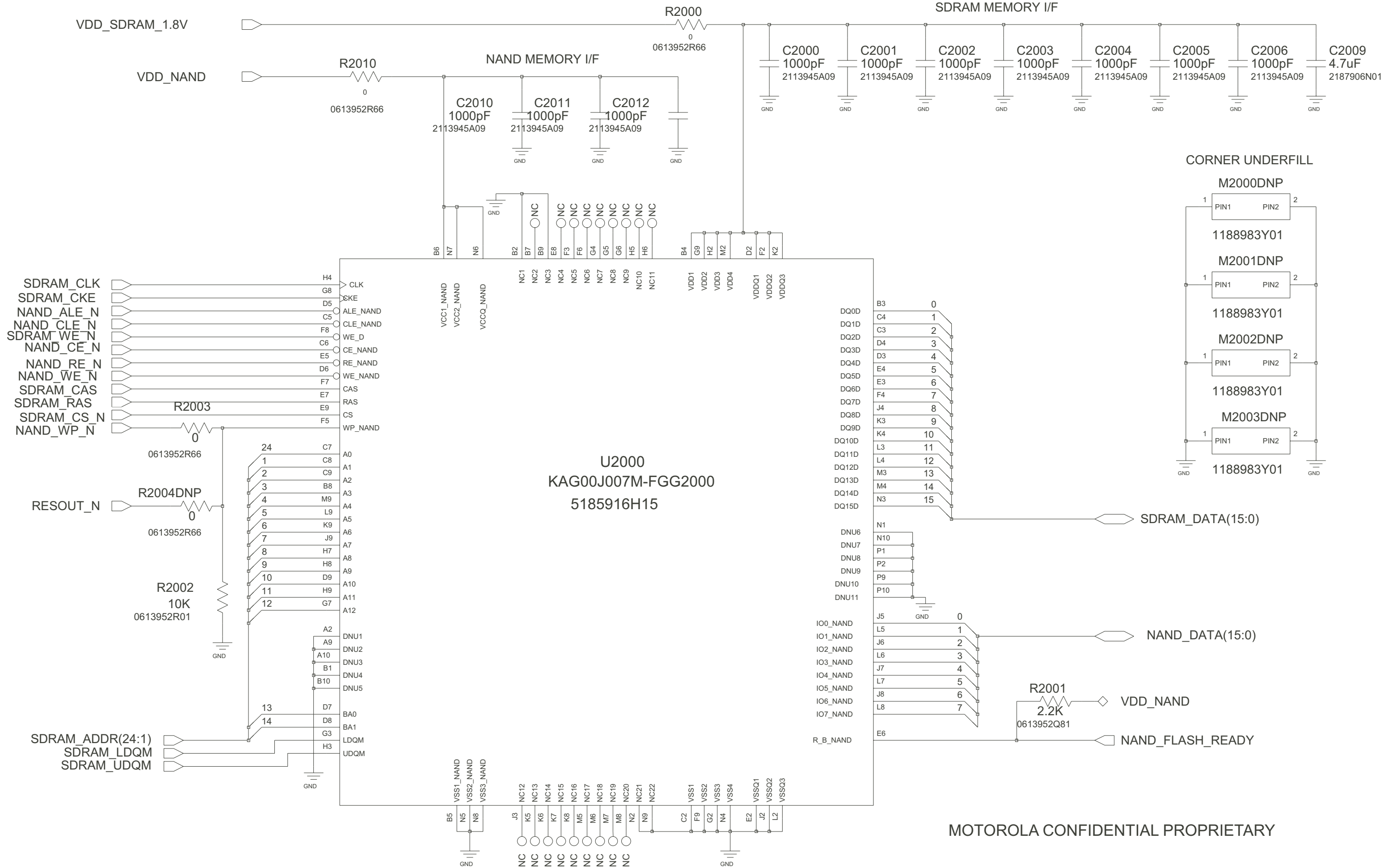
Block Diagram



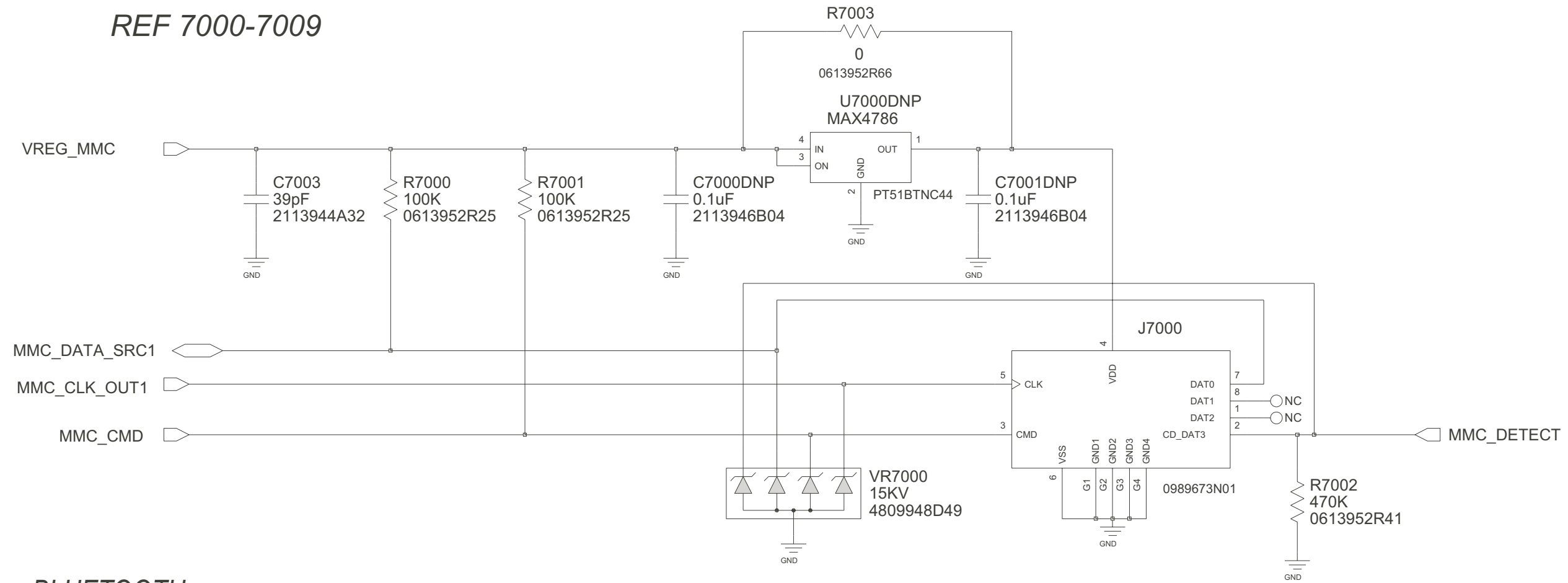


ROUTE THIS VDD_A NODE DIRECTLY BACK TO PIN D13 OF MSM

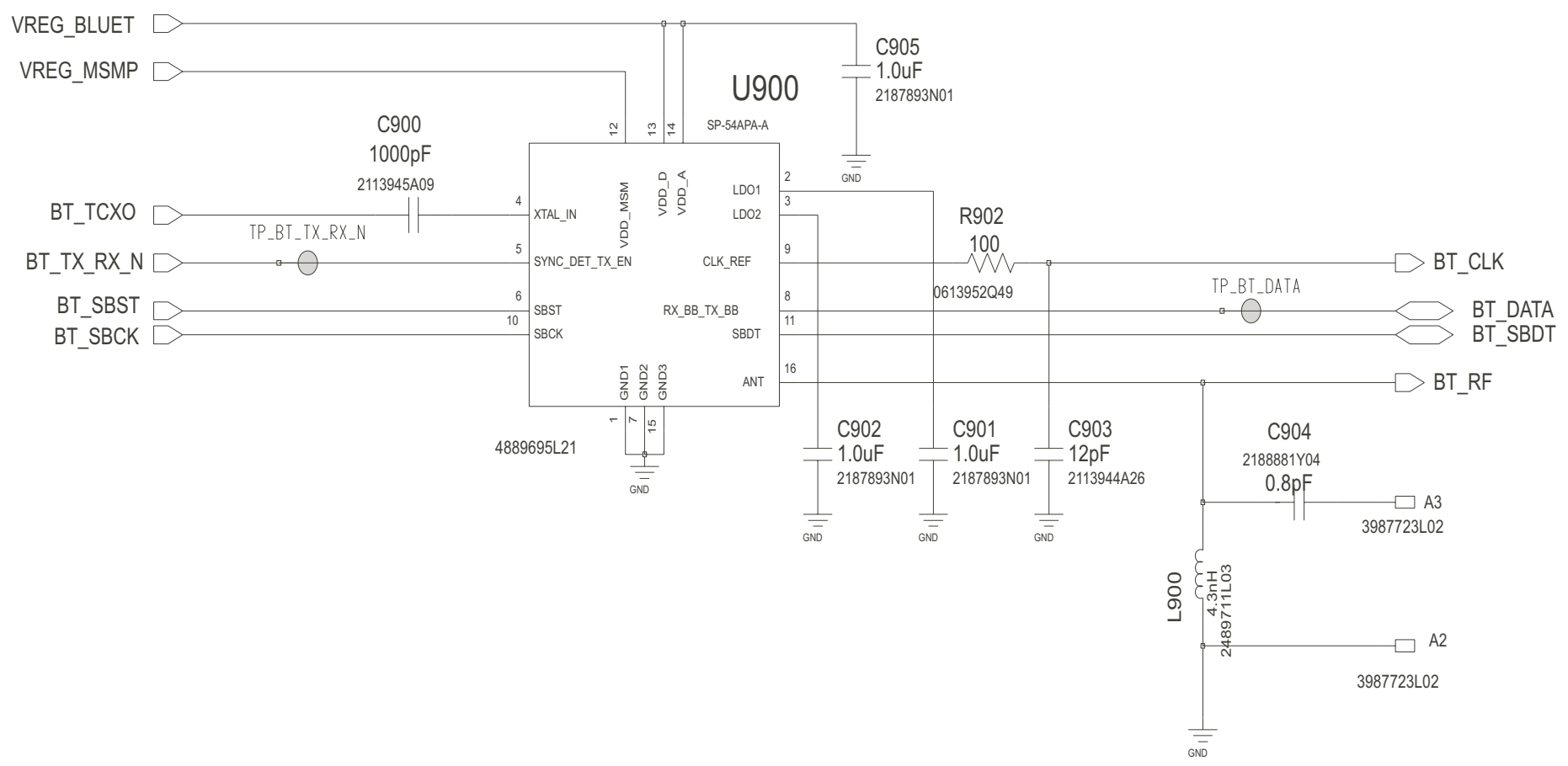
MEMORY
REF 2000-2999



MMC
REF 7000-7009

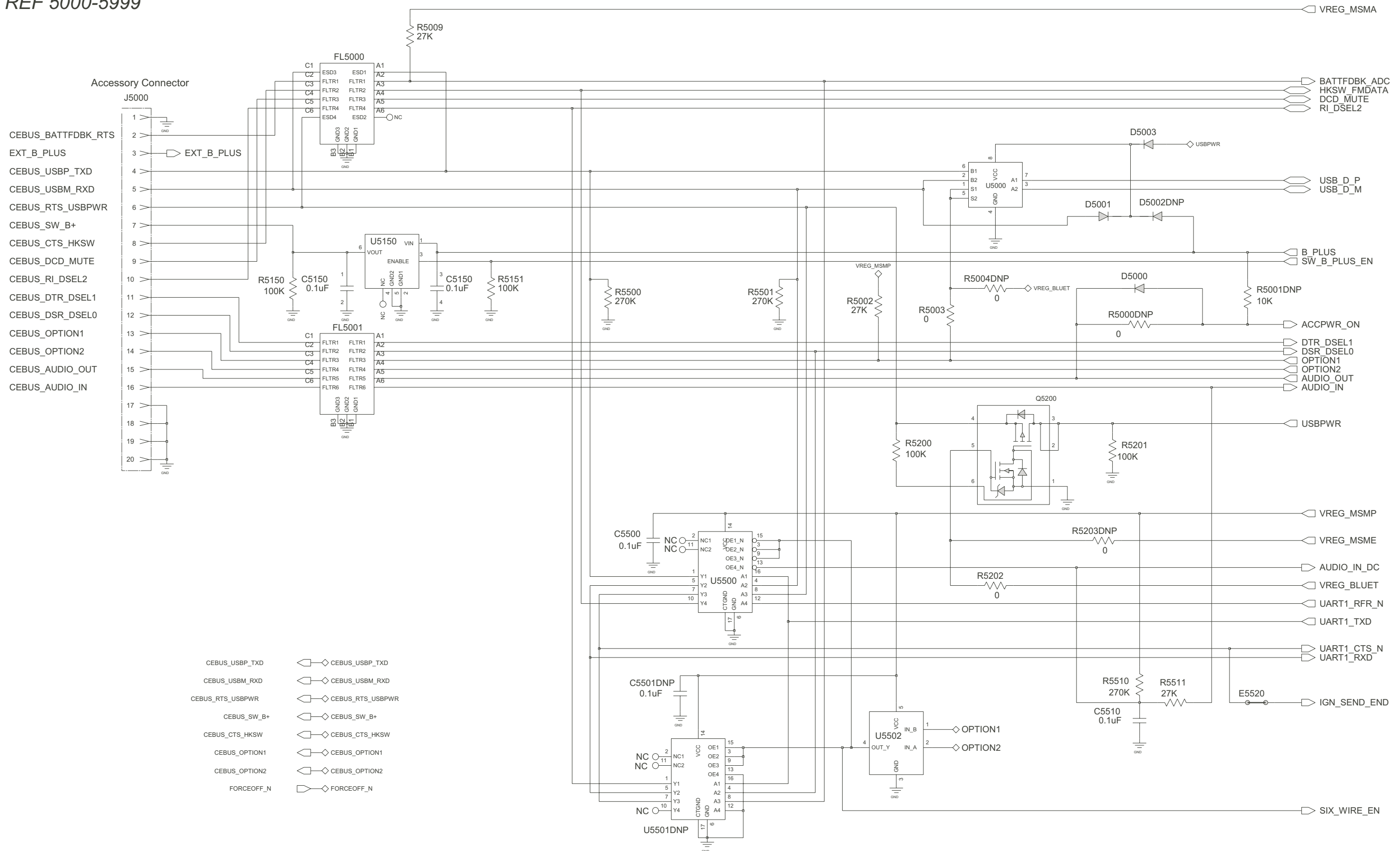


BLUETOOTH
REF 900-999



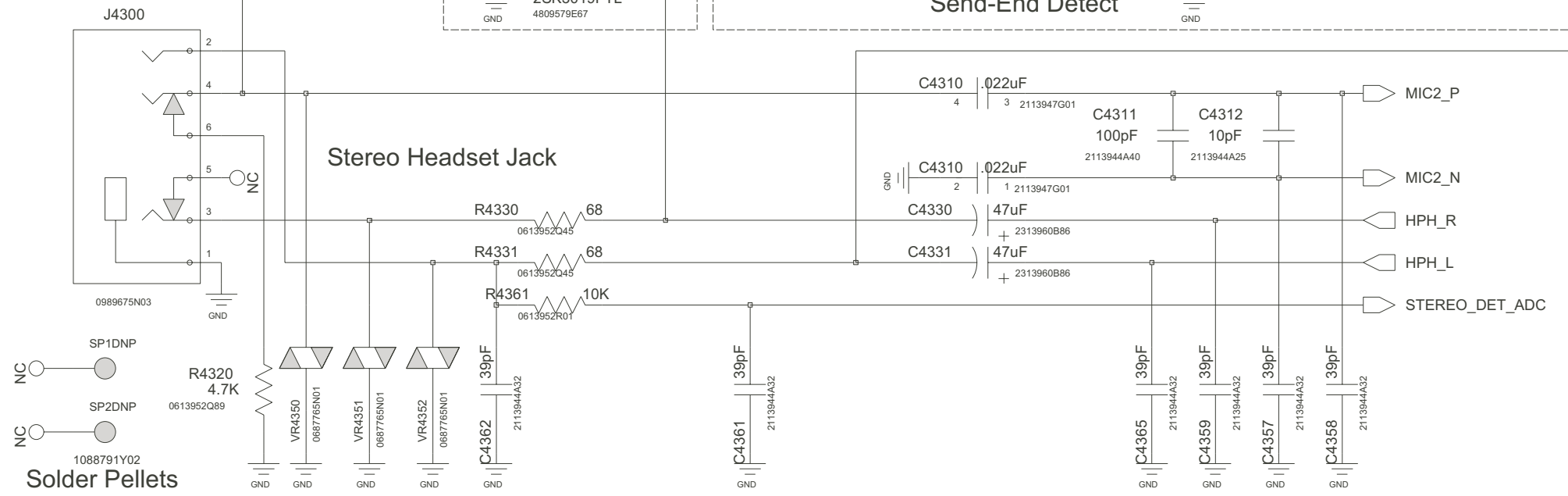
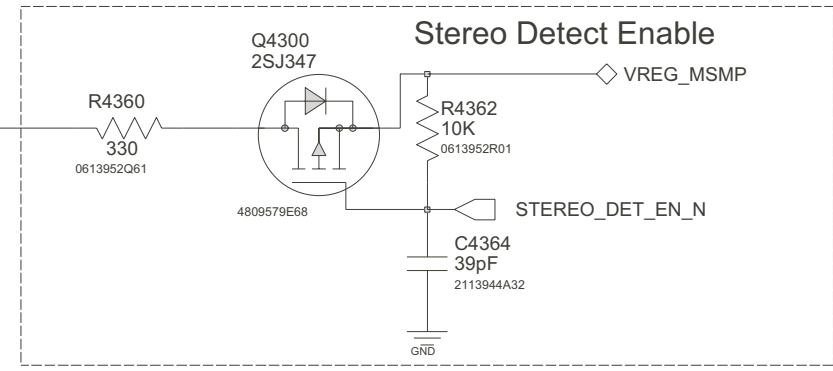
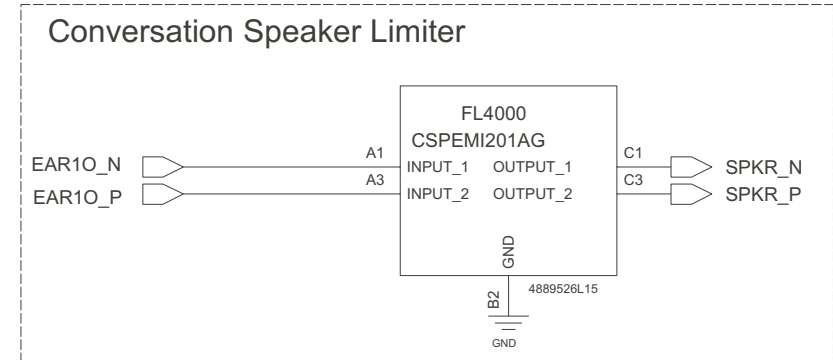
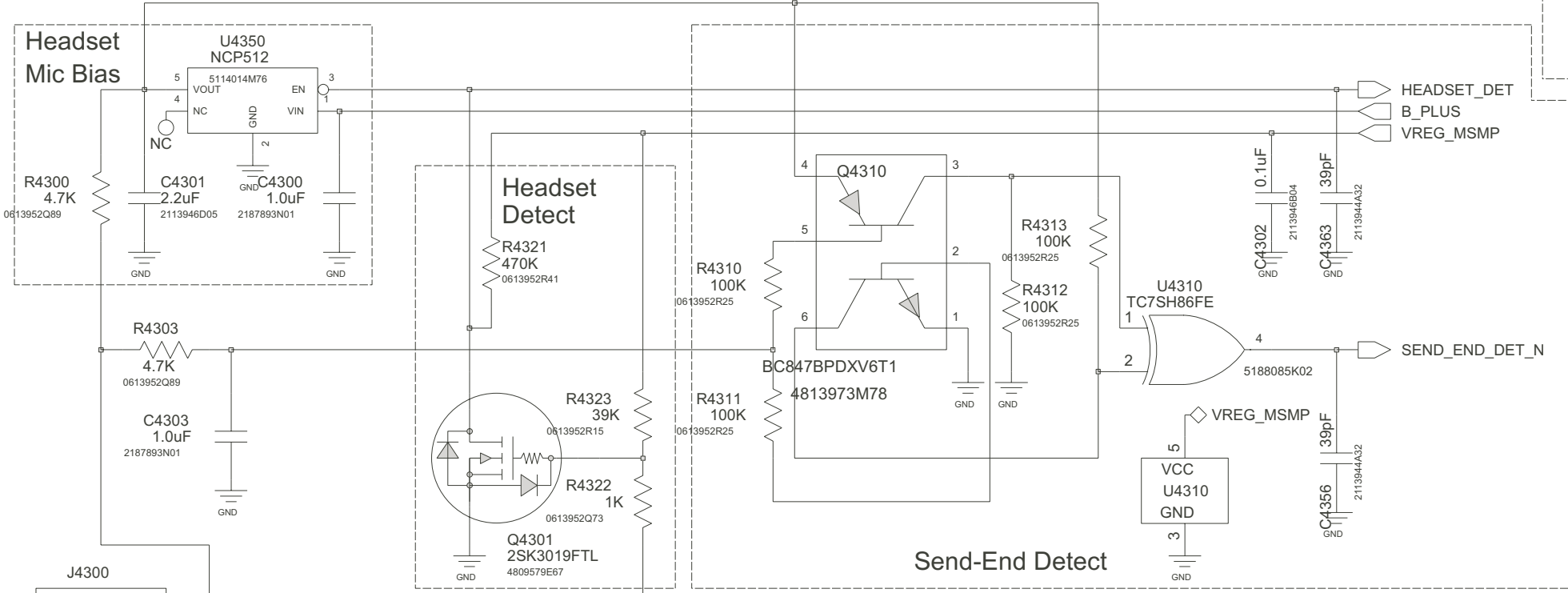
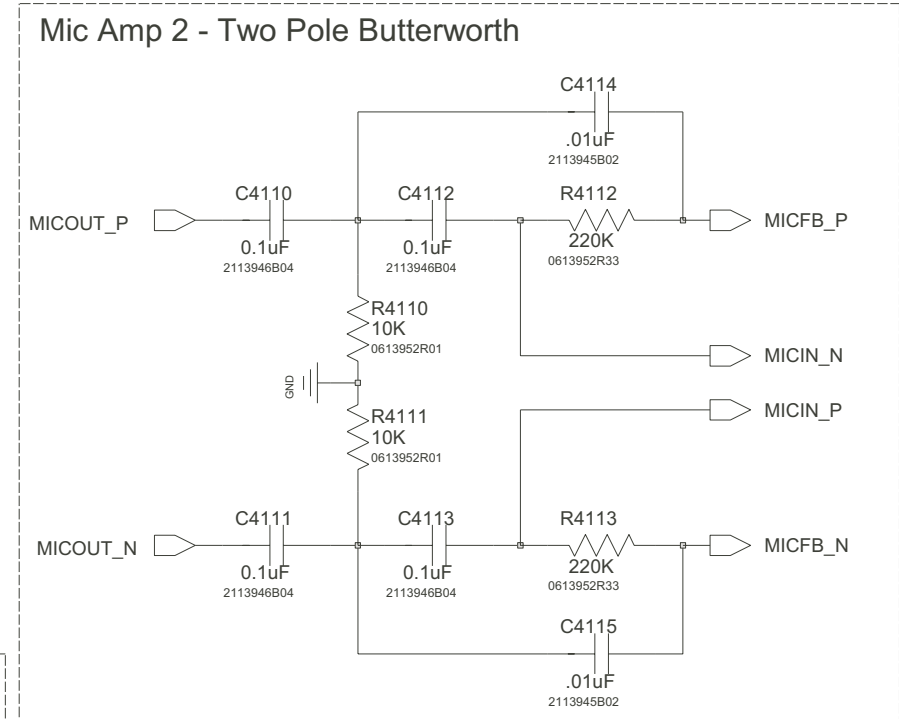
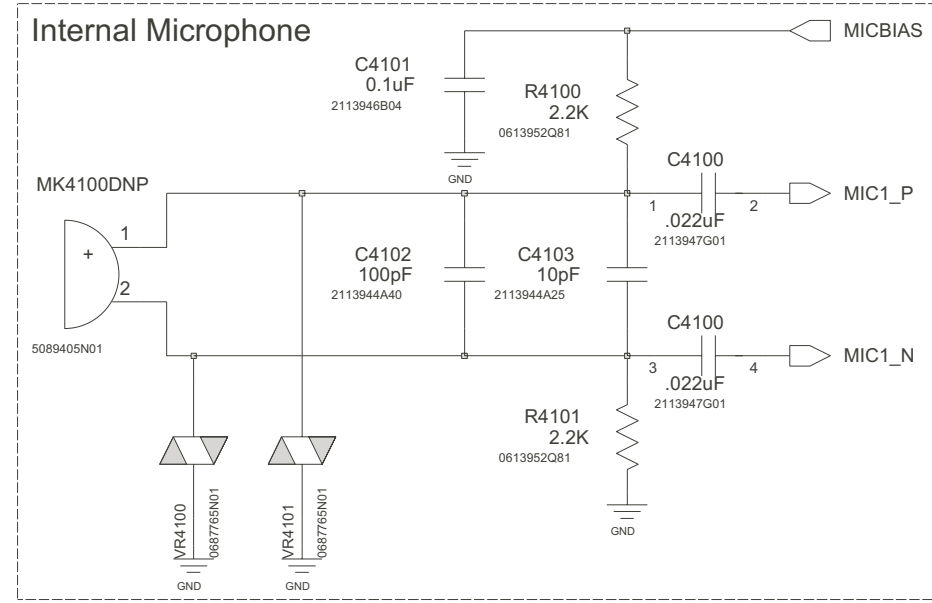
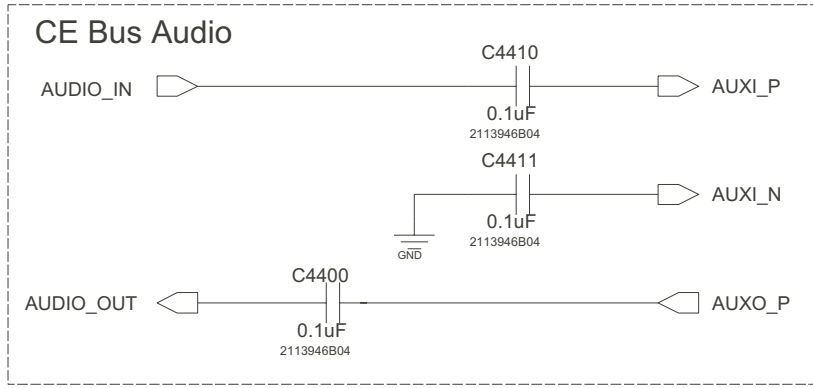
ACCESSORY_INTERFACE

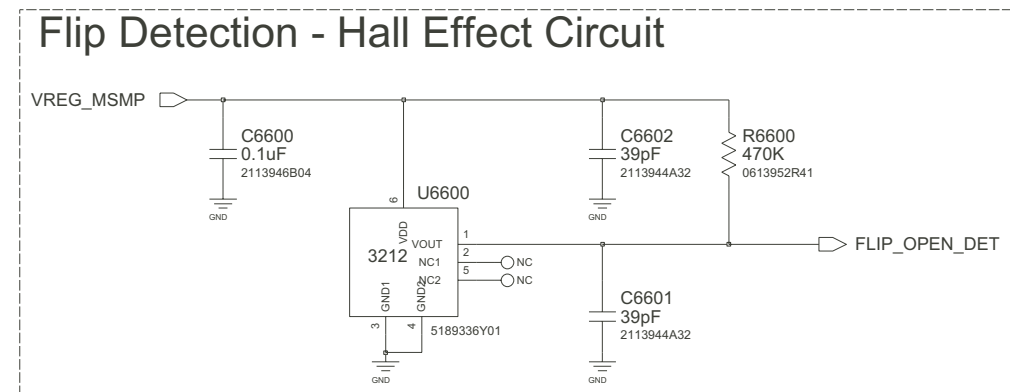
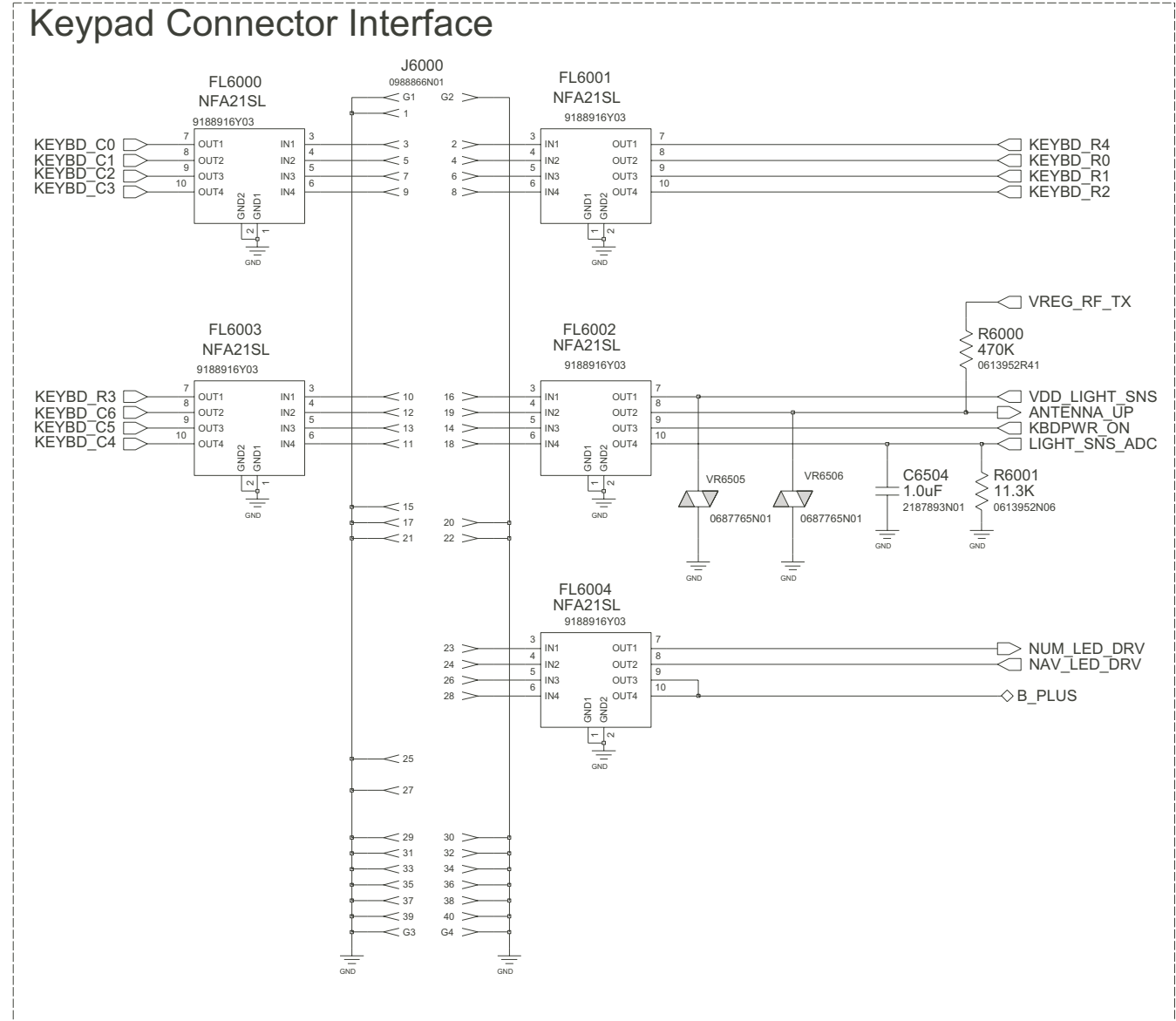
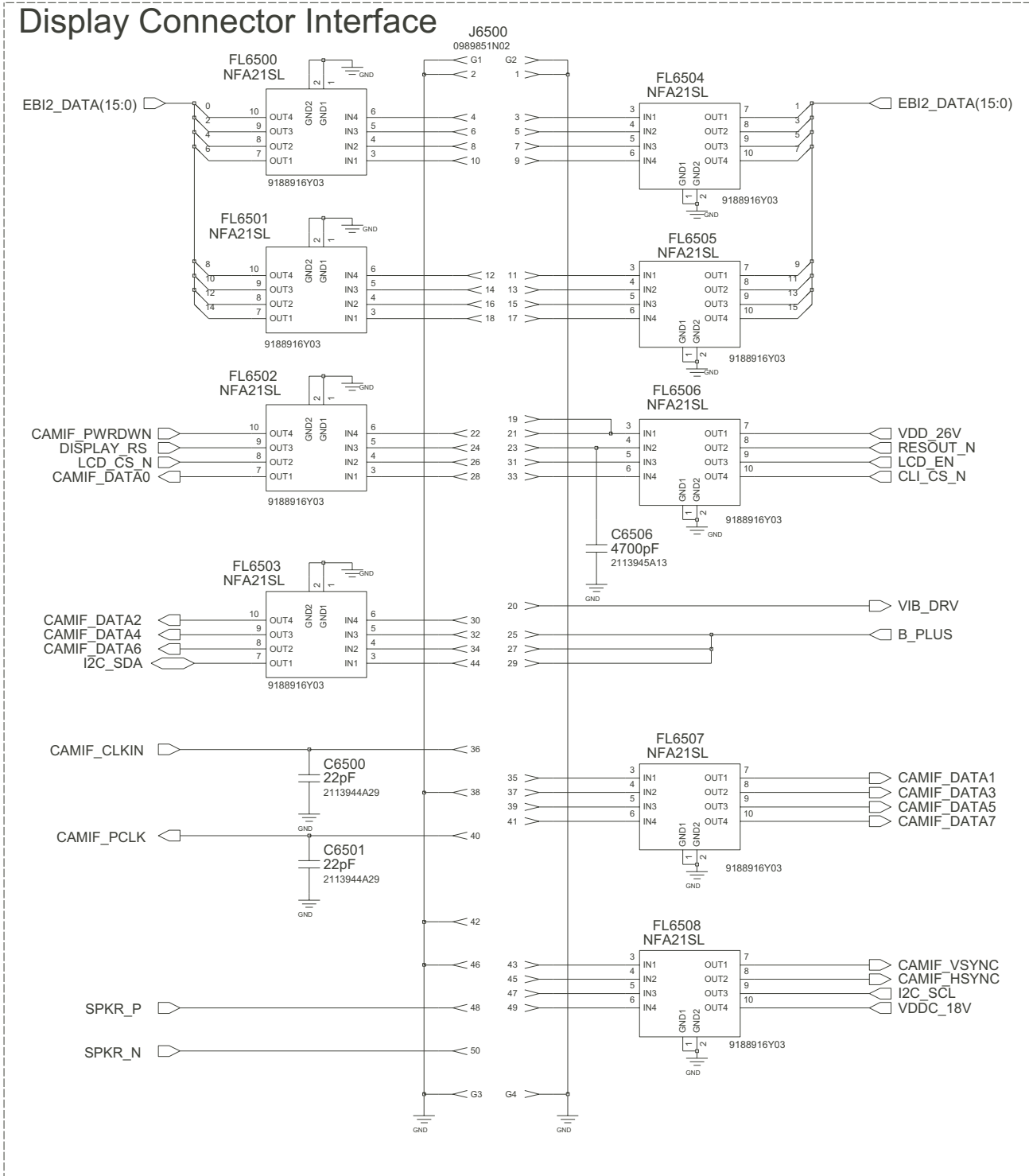
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AUDIO INTERFACE

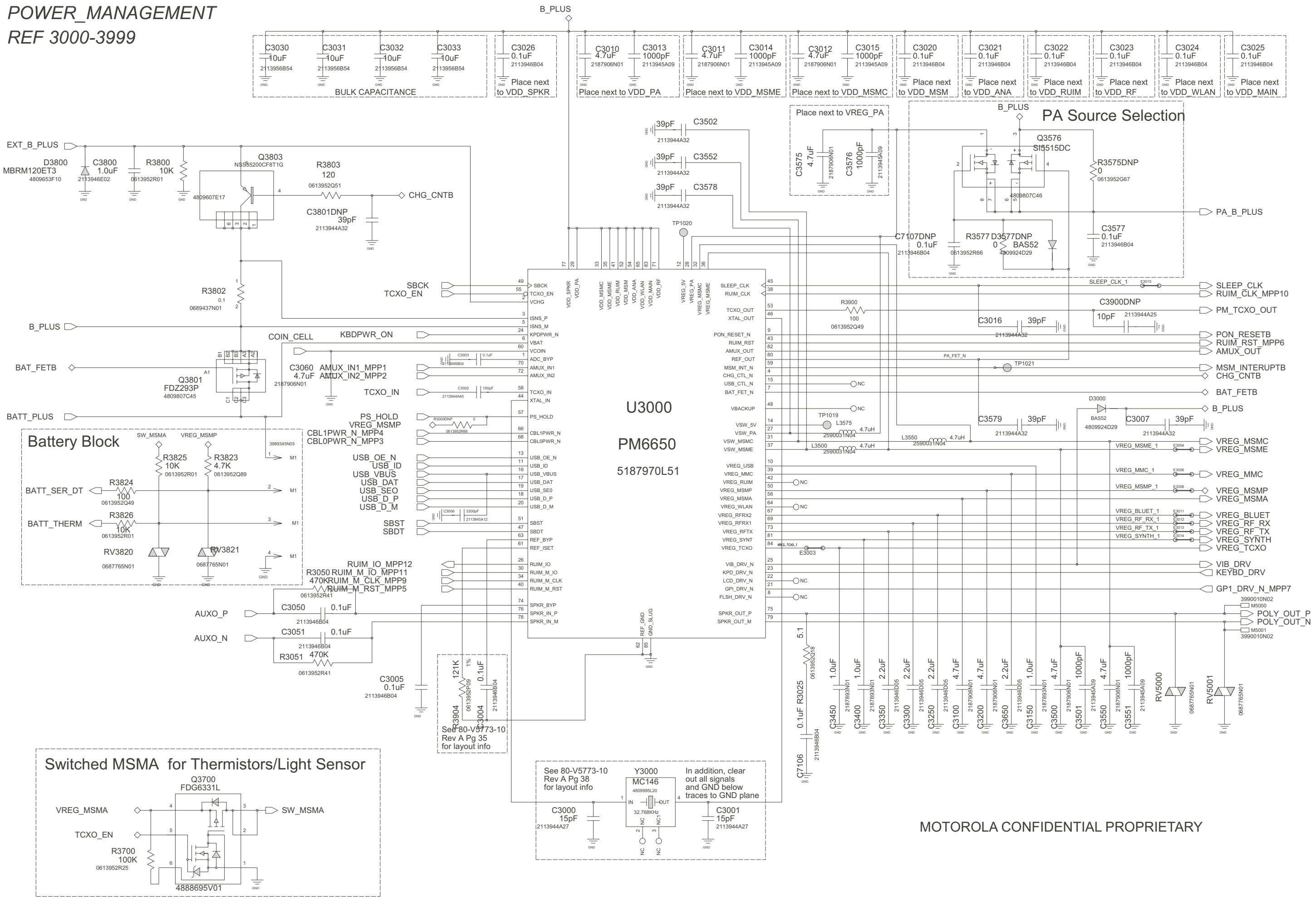
REF 4000-4499





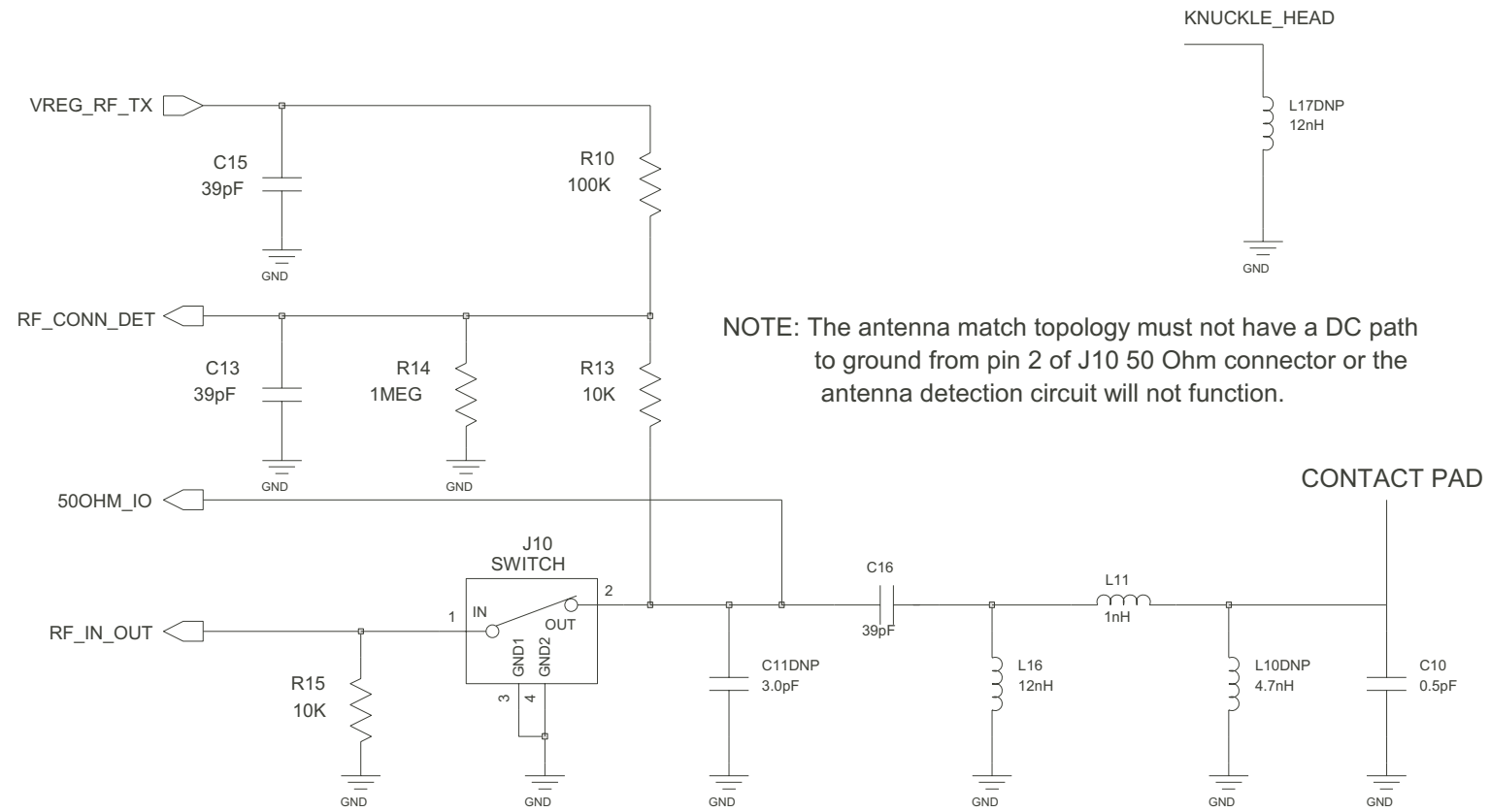
POWER_MANAGEMENT

REF 3000-3999



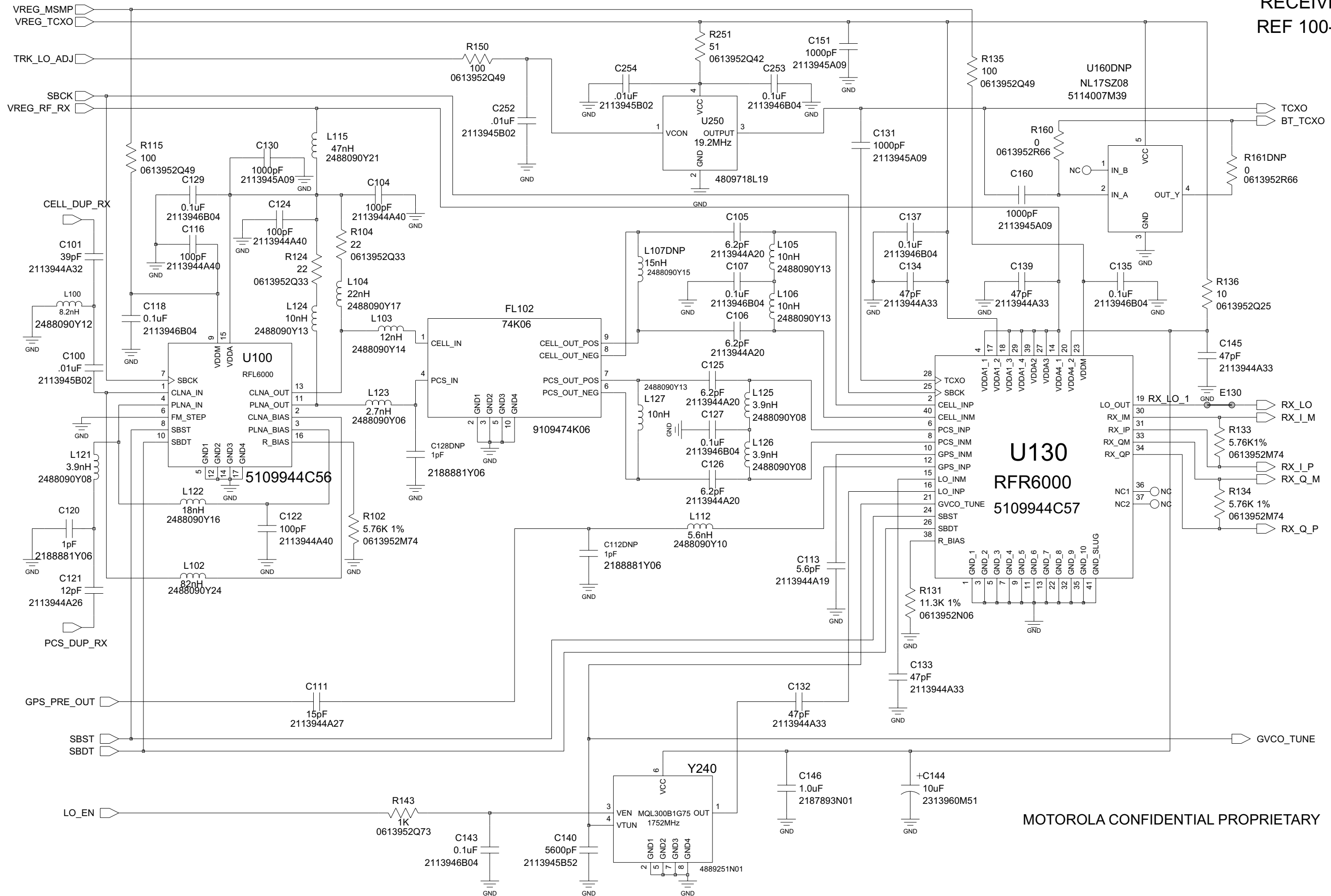
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ANTENNA REF 10-20

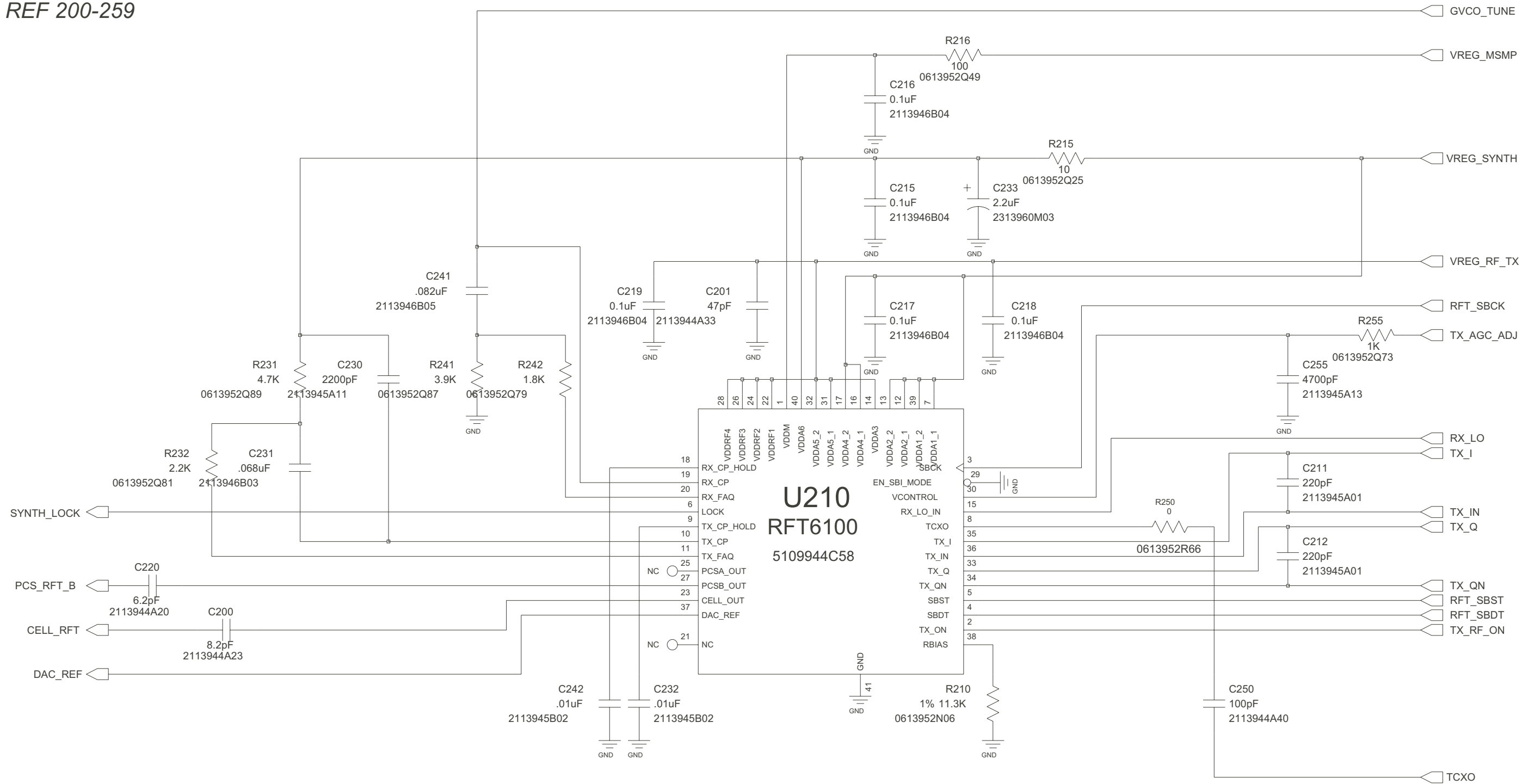


J10 Switch is actually SPDT with throw connecting to 50-ohm coaxial path NOT shown. Antenna sense circuit output is a logic LOW when the internal antenna (A1) is used, with a DC path to ground through R15 existing. When the RF connector path is selected, the output is a logic high, since the DC path to L15 is broken.

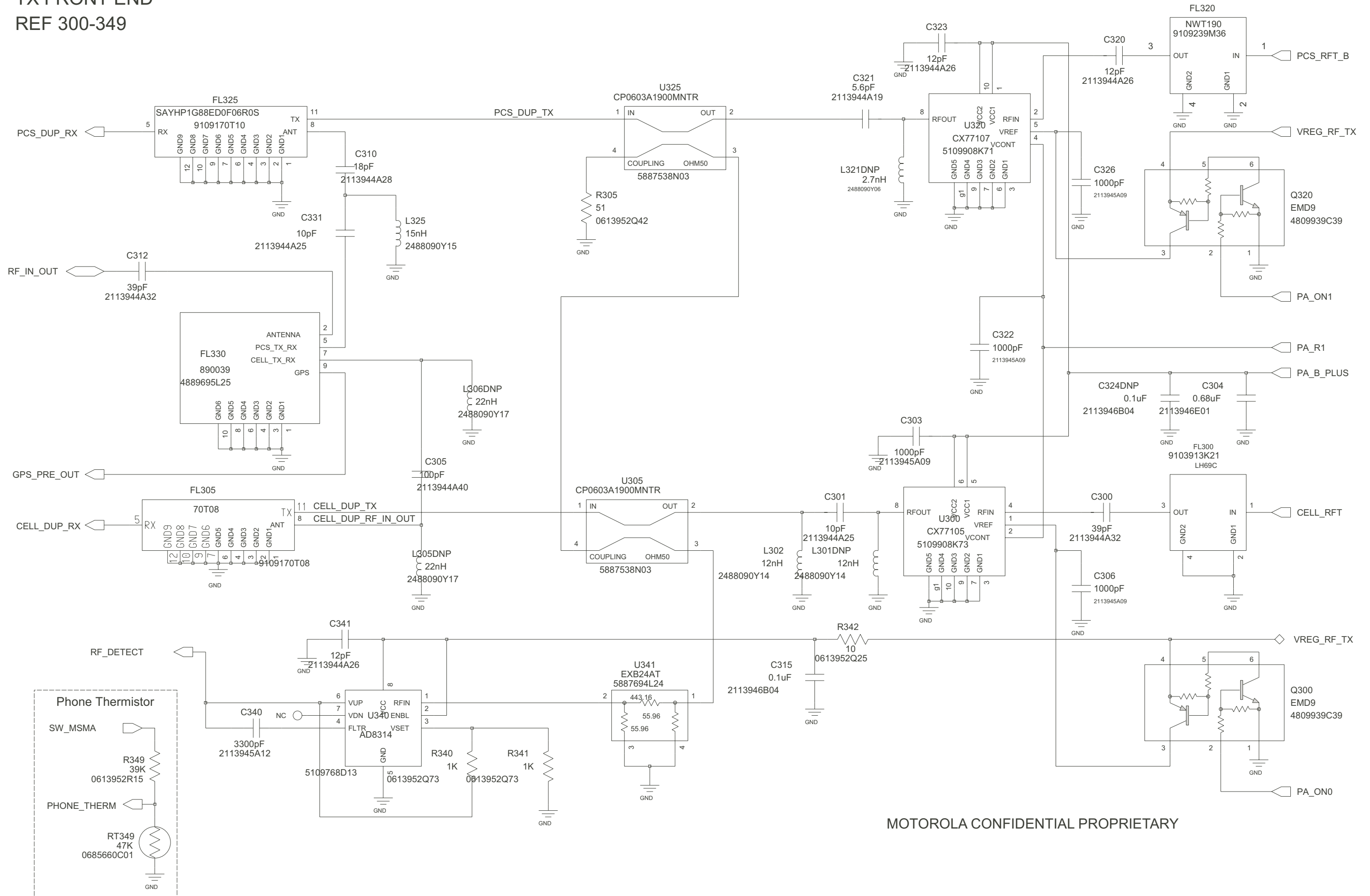
RECEIVER
REF 100-199



TX BACK END
REF 200-259



TX FRONT END
REF 300-349



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