

Engineer:  
Ed Naddeo

Drawn by:  
Ed Naddeo

R&D CHK:

DOC CTRL CHK:

MFG CTRL CHK:

QA CHK:

Ed Naddeo

**MOTOROLA INC.**

TITLE:  
**ELK (E1000)**  
Top Level

Size:  
11x17

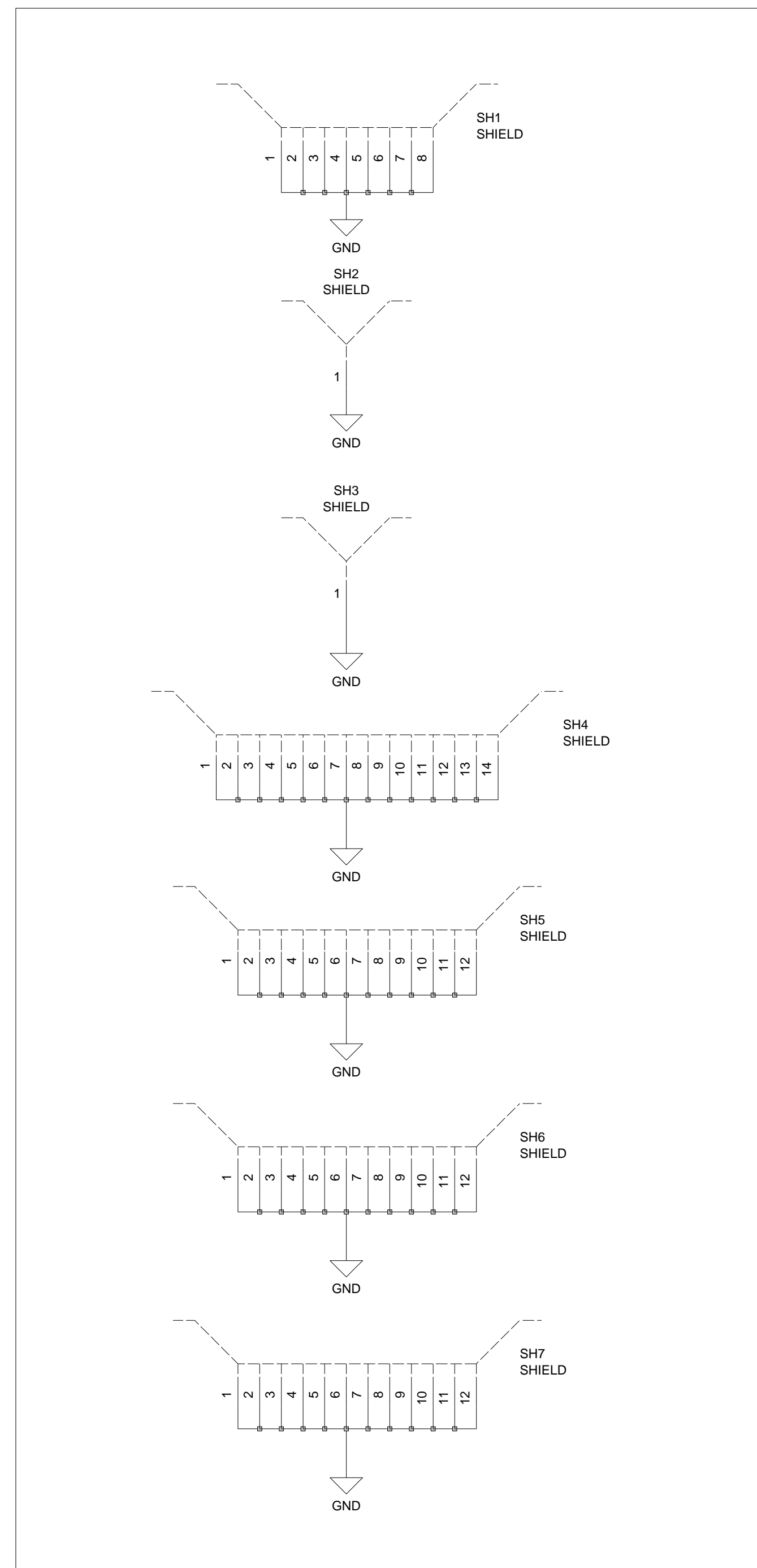
REV:      Drawing Number:  
            8403671B10

Page:      Of:

Date:  
September 17, 2003

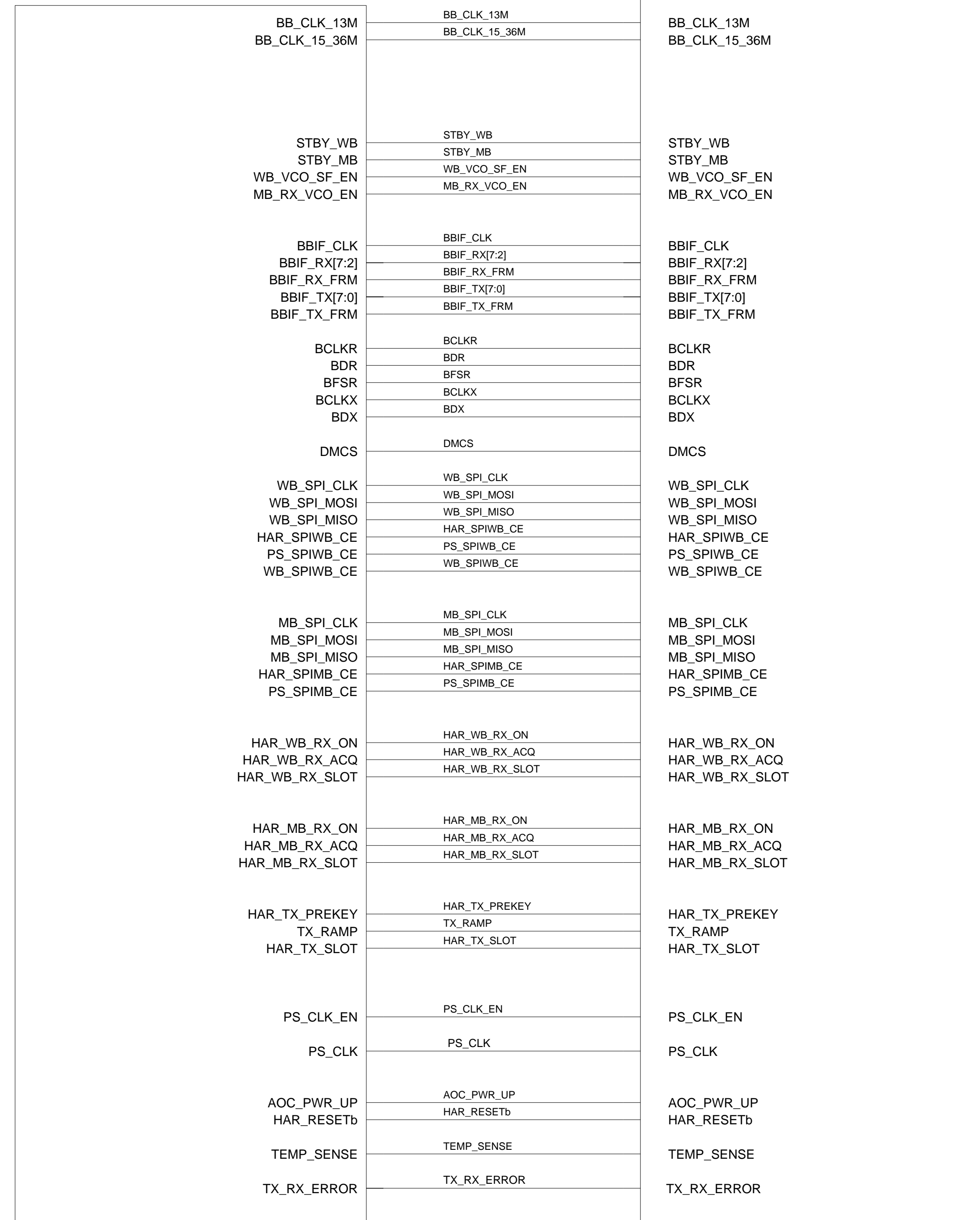
Time:

SHIELDS



Baseband

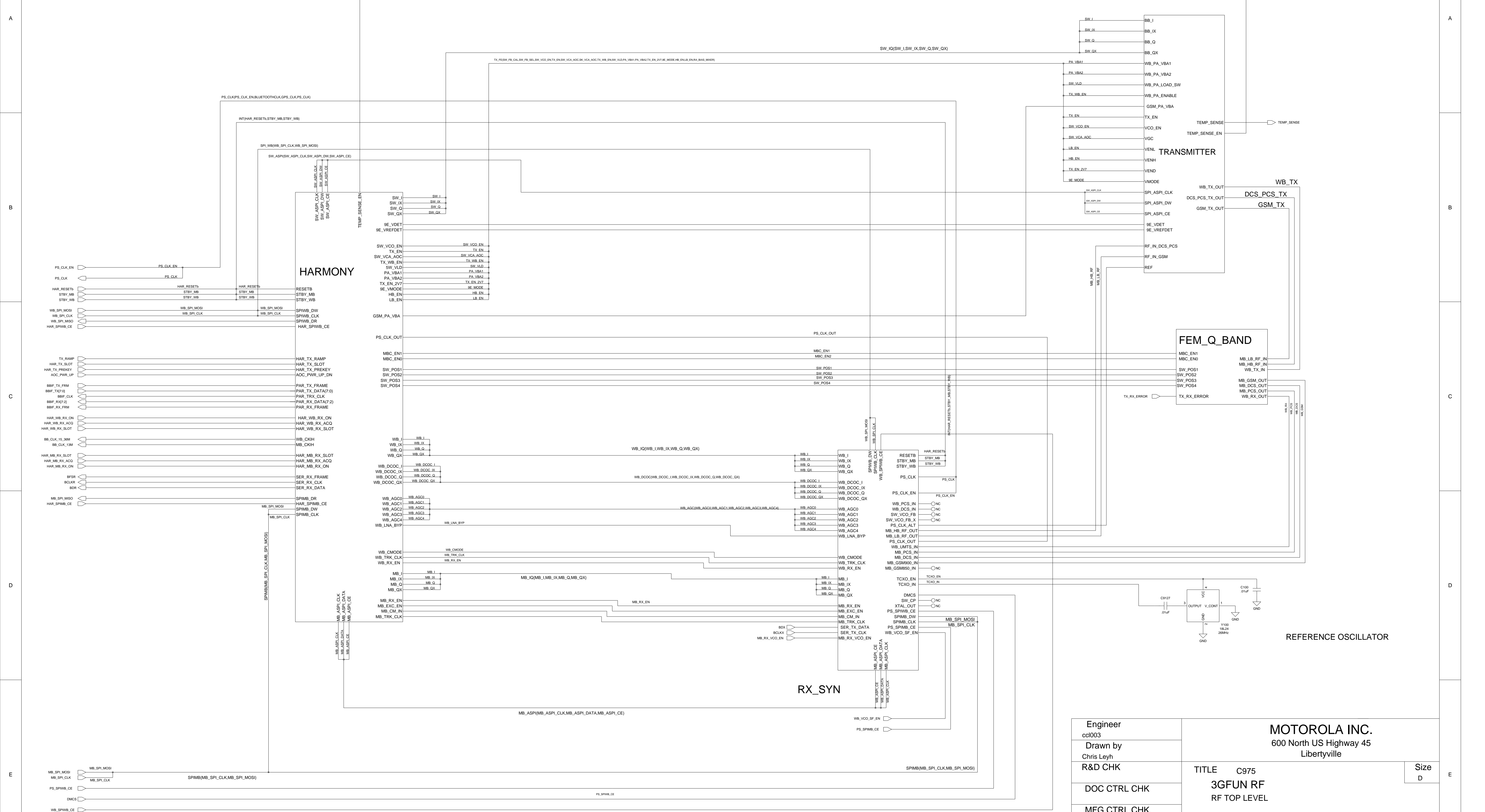
RF



Note:

All changes to the schematic are being recorded and stored in:  
X:\Projects\_Area\Electrical\Projects\C975\PCB & Schematic Changes  
file name:  
Implemented\_schematic\_changes.xls

TEMP\_SENSE\_EN



Engineer ccl003	MOTOROLA INC. 600 North US Highway 45 Libertyville		
Drawn by Chris Layh			
R&D CHK	TITLE	C975	Size D
DOC CTRL CHK	3GFUN RF RF TOP LEVEL		
MFG CTRL CHK	REV	Drawing Number	Sheet of

Changed by ccl003	Date Changed Friday, February 28, 2003	Time
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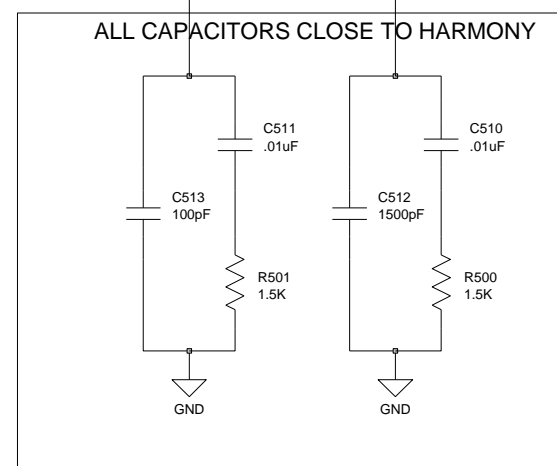
TP placeholders for pins with NC

SYNTHESIS CAPACITORS CLOSE TO HARMONY

### SUPPLIES INTERCONNECTIONS FAR FROM HARMONY

# HARMONY

ALL CAPACITORS CLOSE TO HARMONY



Engineer Chris Leyh	MOTOROLA INC. 600 North US Highway 45 Libertyville		
Drawn by ccl003			
R&D CHK	TITLE C975	Size D	
DOC CTRL CHK	HARMONY		
MFG CTRL CHK			
QA CHK	REV 1	Drawing Number	Sheet of

Changed by ccl003	Date Changed Thursday, April 17, 2003	Time 12:06:46 pm
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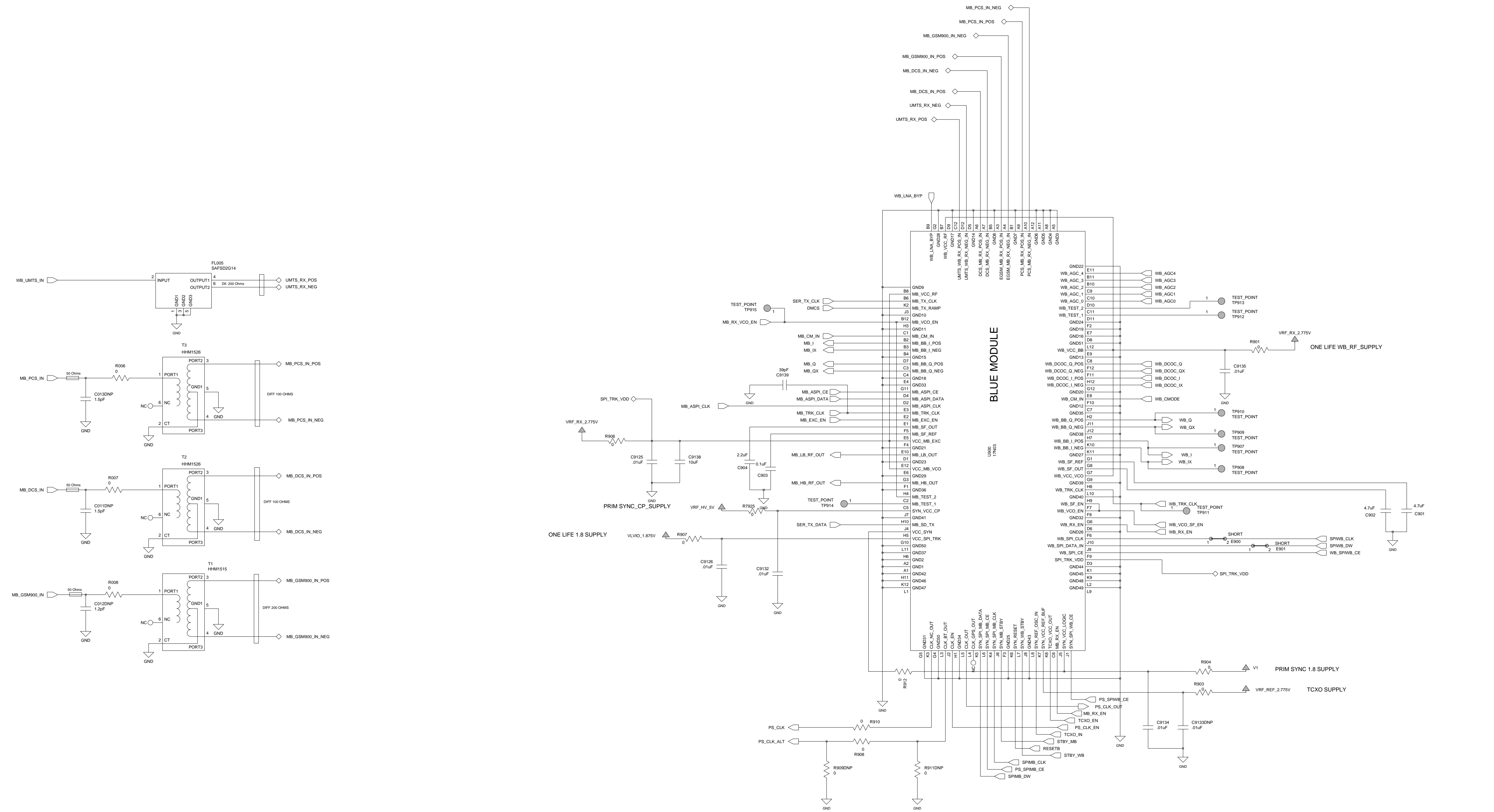
Motorola Confidential Proprietary

# ORG. Blue Module

**DO NOT DELETE**

- WB\_DCS\_IN
- WB\_DCS\_OUT
- WB\_PCS\_IN
- WB\_GSM900\_IN
- WB\_S0S2
- WB\_S0S1
- SW\_CP
- SW\_VCO\_FB
- SW\_VCO\_FB\_X

TPs for test\_1 & test\_2



Engineer Chris Leyh	COMPANY NAME Address City		
Drawn by Chris Leyh			
R&D CHK	TITLE C975	Size D	
DOC CTRL CHK	Rx/Syn		
MFG CTRL CHK			
QA CHK	REV 1.0	Drawing Number	Sheet of

Changed by ccl003	Date Changed Thursday, April 10, 2003	Time 9:50:27 am
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A

B

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E

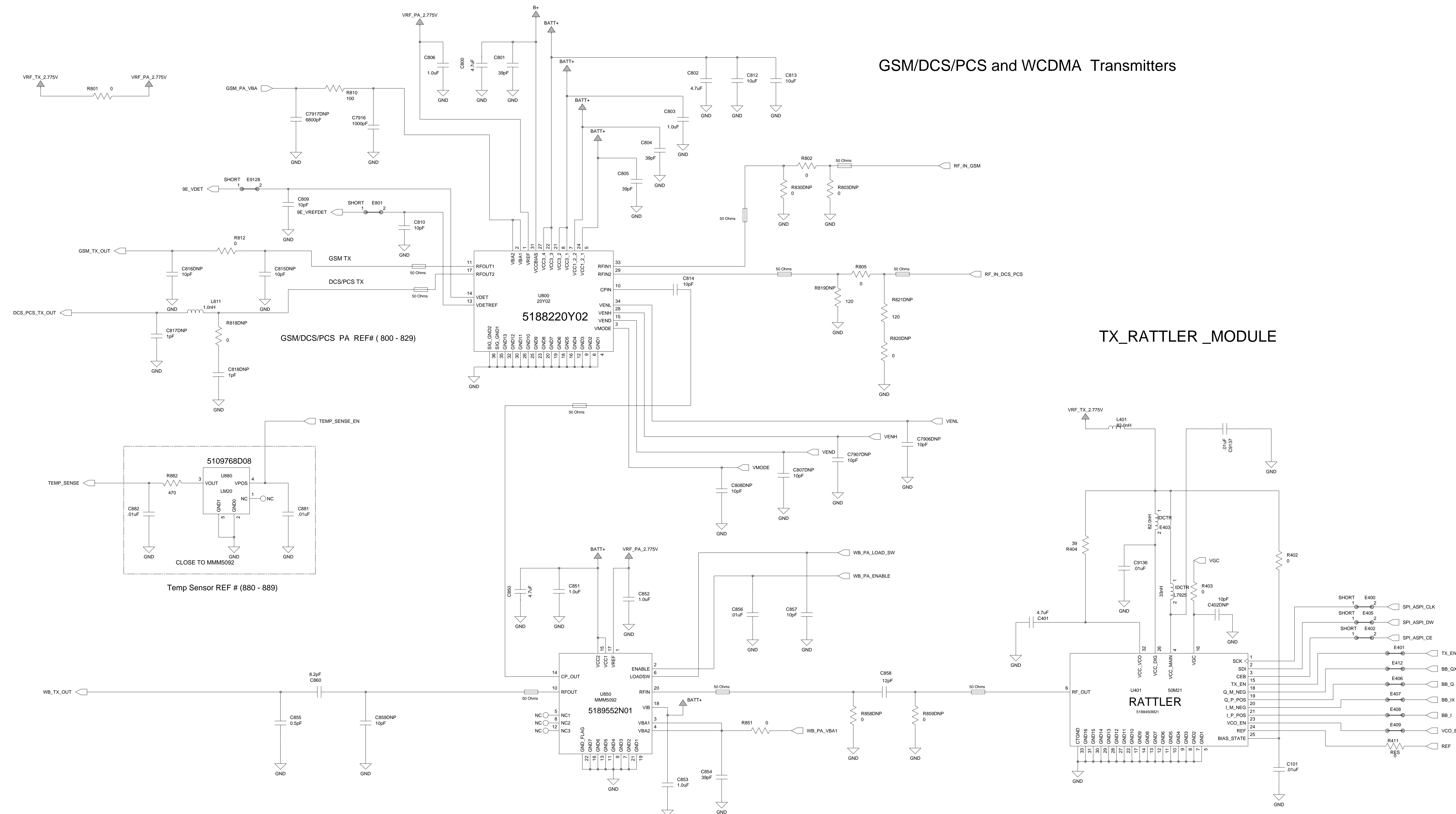
A

B

C

D

E



GSM/DCS/PCS and WCDMA Transmitters

TX\_RATTLER\_MODULE

WCDMA PA REF # (850 - 879)

GSM/DCS/PCS PA REF# ( 800 - 829)

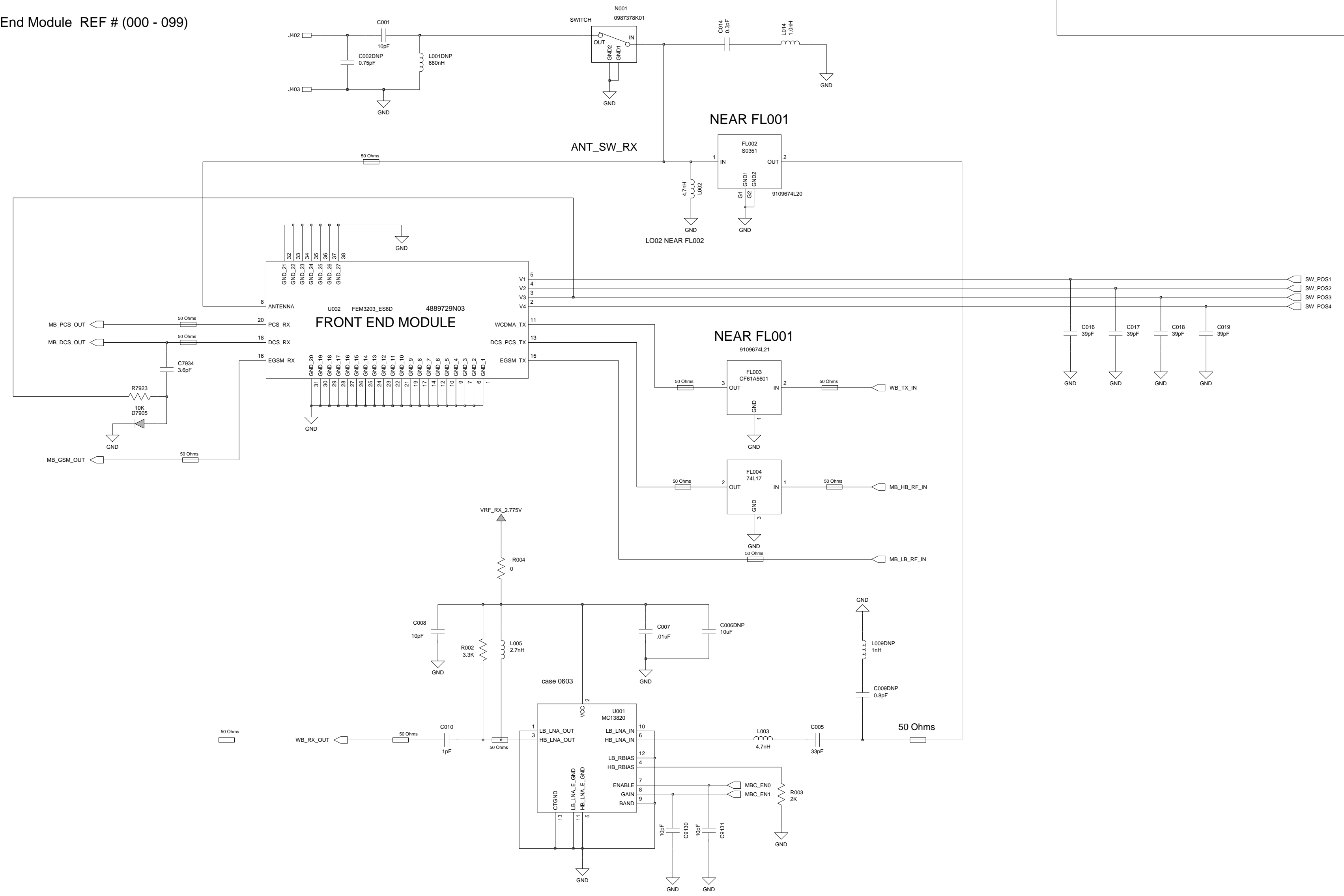
Temp Sensor REF # (880 - 889)

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Engineer	MOTOROLA INC. 600 North US Highway 45 Libertyville		
Drawn by	TITLE		
R&D CHK	C975		
DOC CTRL CHK	TRANSMITTER		
MFG CTRL CHK	REV	Drawing Number	Sheet of
QA CHK			

Changed by	Date Changed	Time
wlpm11	Thursday, April 24, 2003	1:46:09 pm

Front End Module REF # (000 - 099)



Engineer Chris Leyh	MOTOROLA INC. 600 North US Highway 45 Libertyville			Size D
Drawn by ccl003				
R&D CHK	TITLE	C975		
DOC CTRL CHK	Quad Band Front End Module			
MFG CTRL CHK	REV 1	Drawing Number	Sheet	of
QA CHK	Rev. 1			

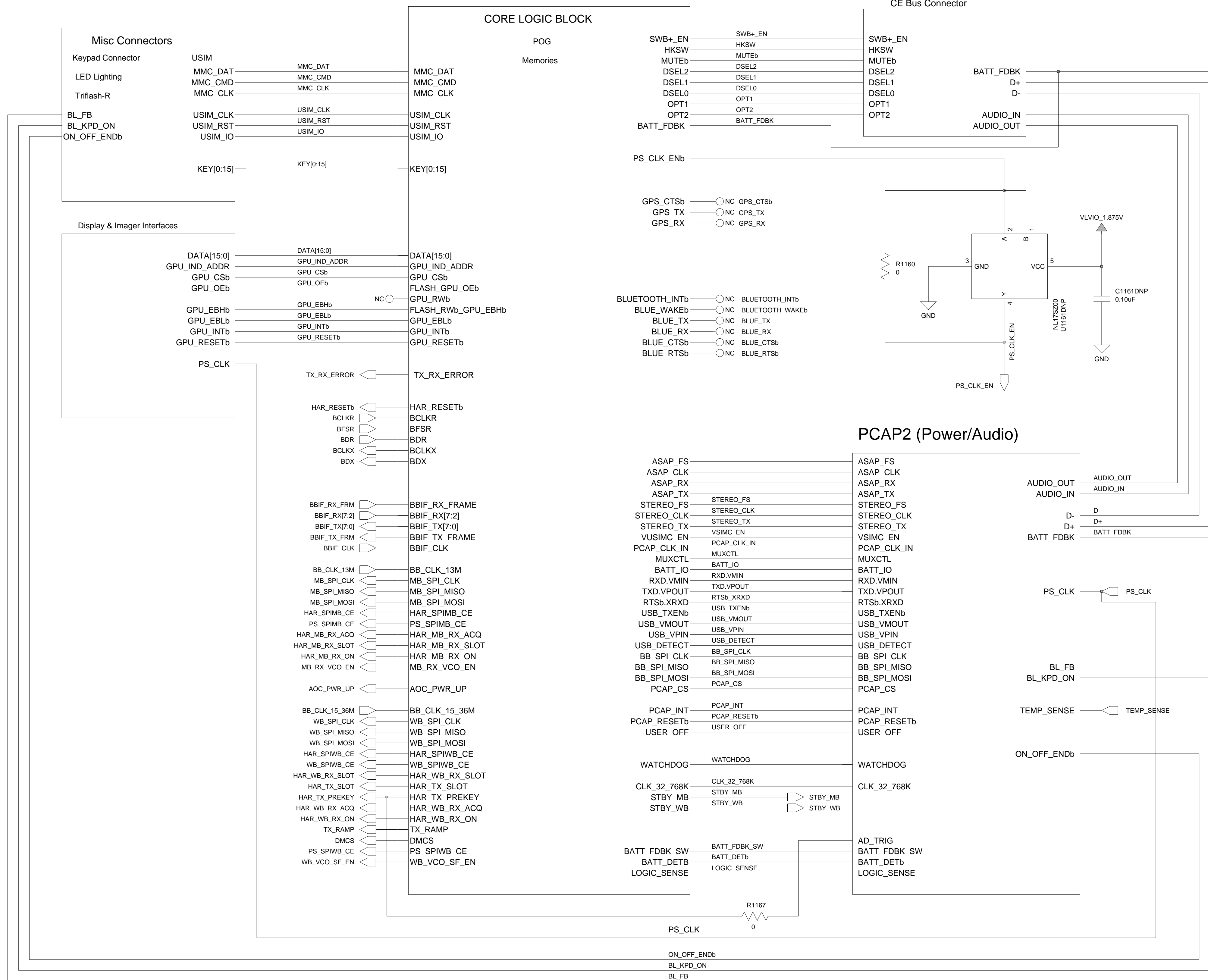
Changed by ccl003	Date Changed Thursday, April 17, 2003	Time 8:25:56 am
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A

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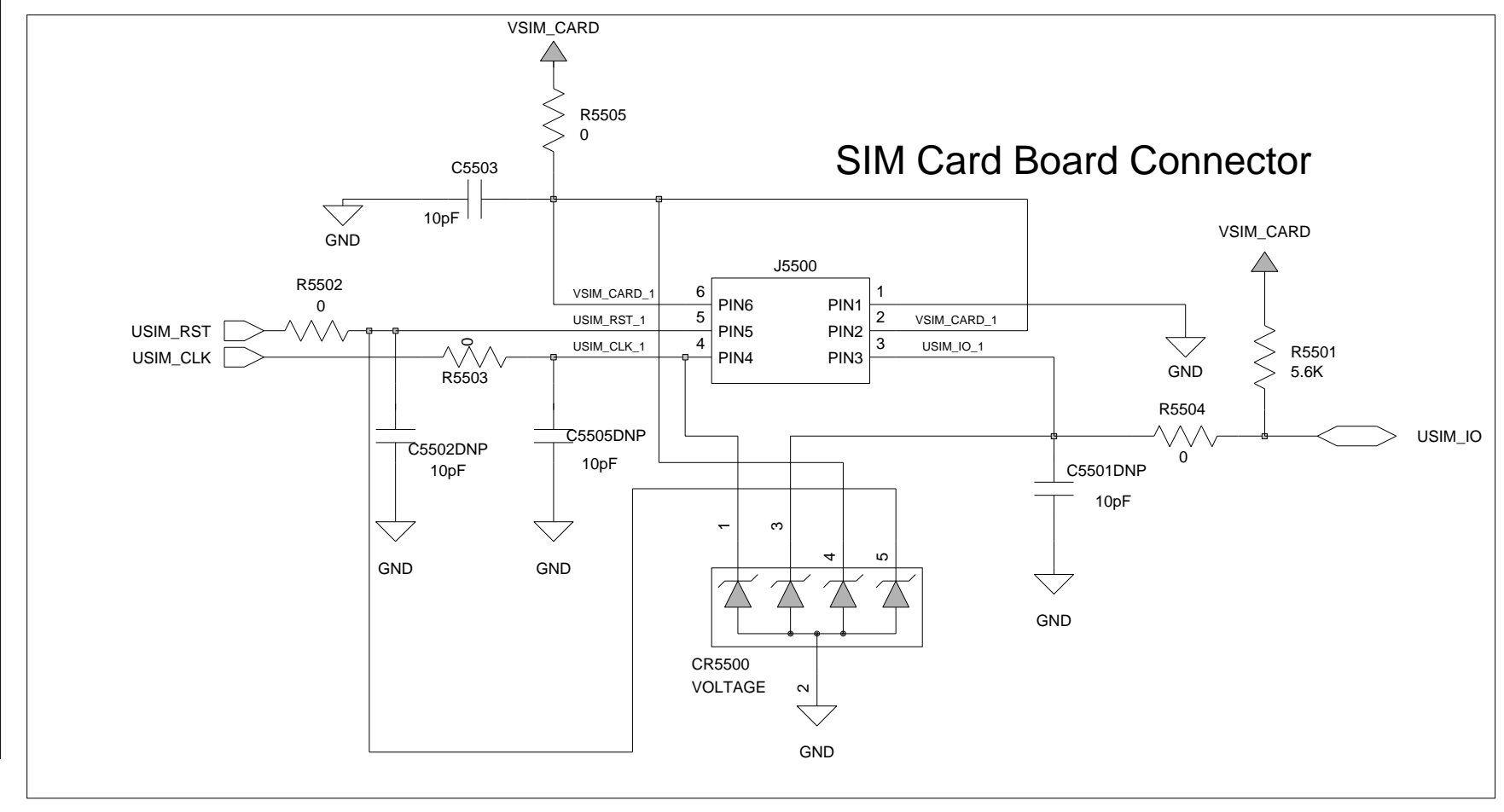
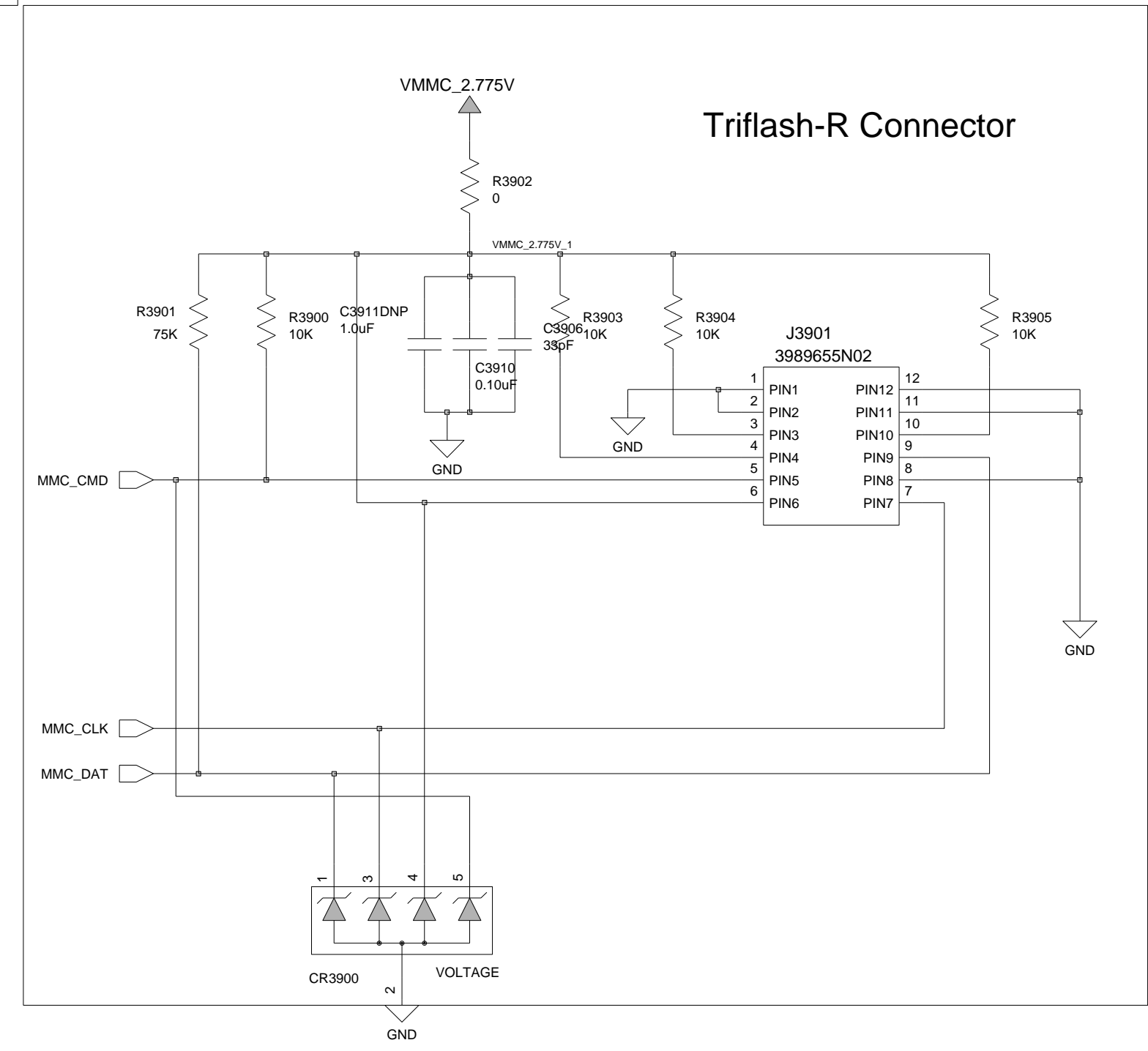
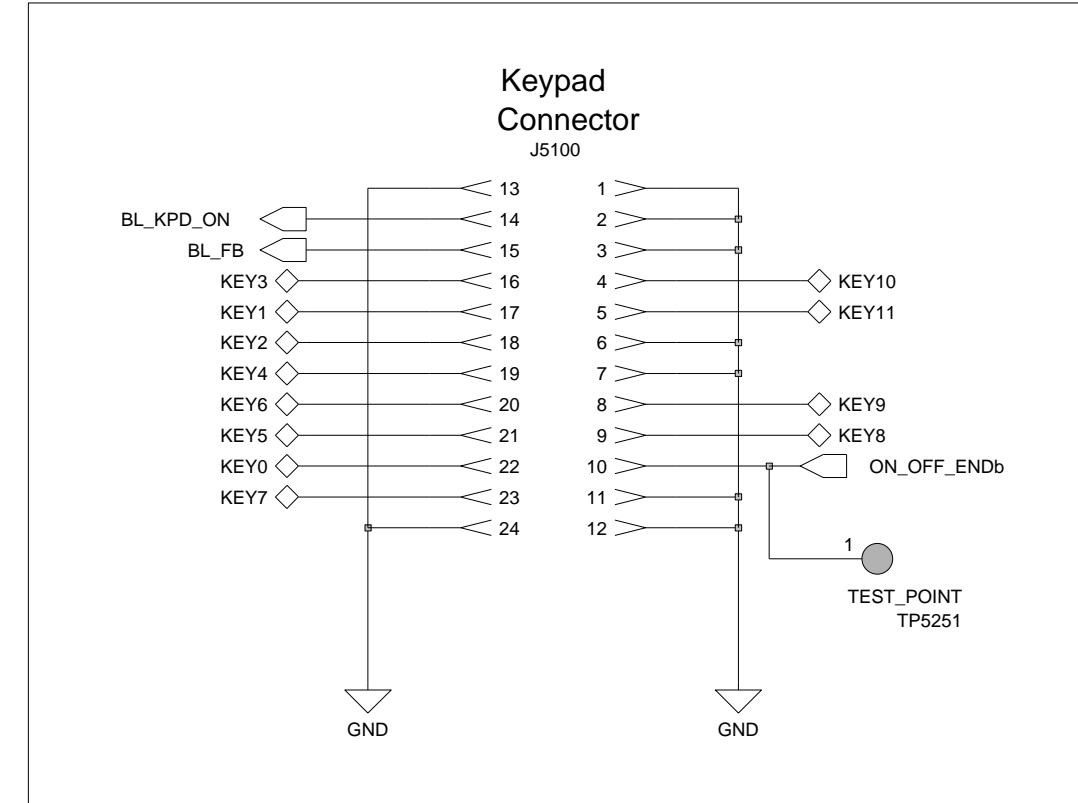
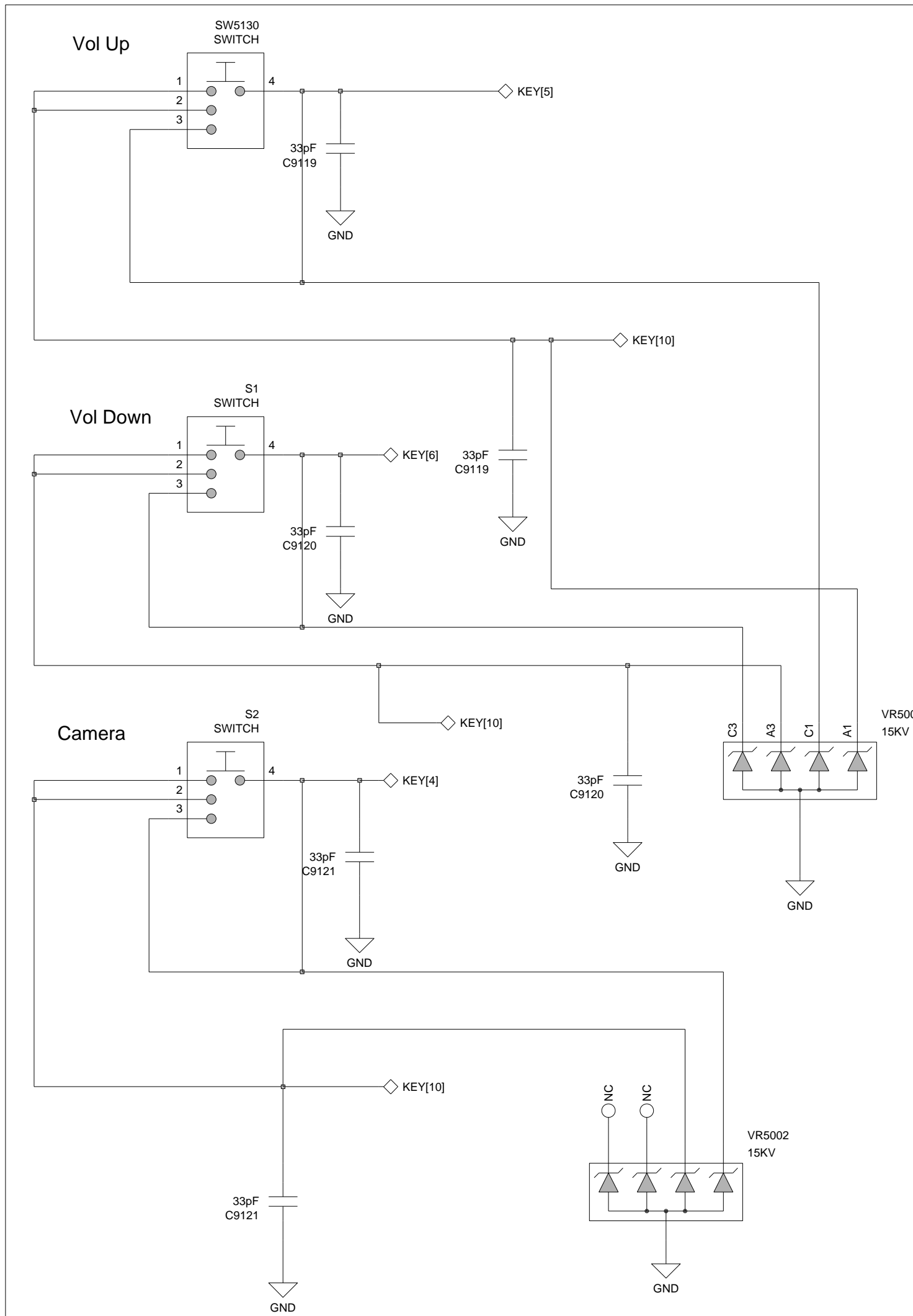
D



Engineer:			
Drawn by: Fabrizio Filizzola			
R&D CHK:	TITLE: C975	Size: 11x17	
DOC CTRL CHK:	Baseband Top Level		
MFG CTRL CHK:			
QA CHK:	REV:	Drawing Number:	Page: Of:
Changed by: Fabrizio Filizzola	Date: January 26, 2004	Time:	

Engineer: Fabrizio Filizzola	TITLE: C975			Size: 11x21
Drawn by: Fabrizio Filizzola	REV: 1			Page: 1
R&D CHK:	DOC CTRL CHK:			Of: 1
MFG CTRL CHK:	Date: January 27, 2004			Time:
QA CHK:	Drawing Number:			
Changed by: Fabrizio Filizzola				

### Side Keys



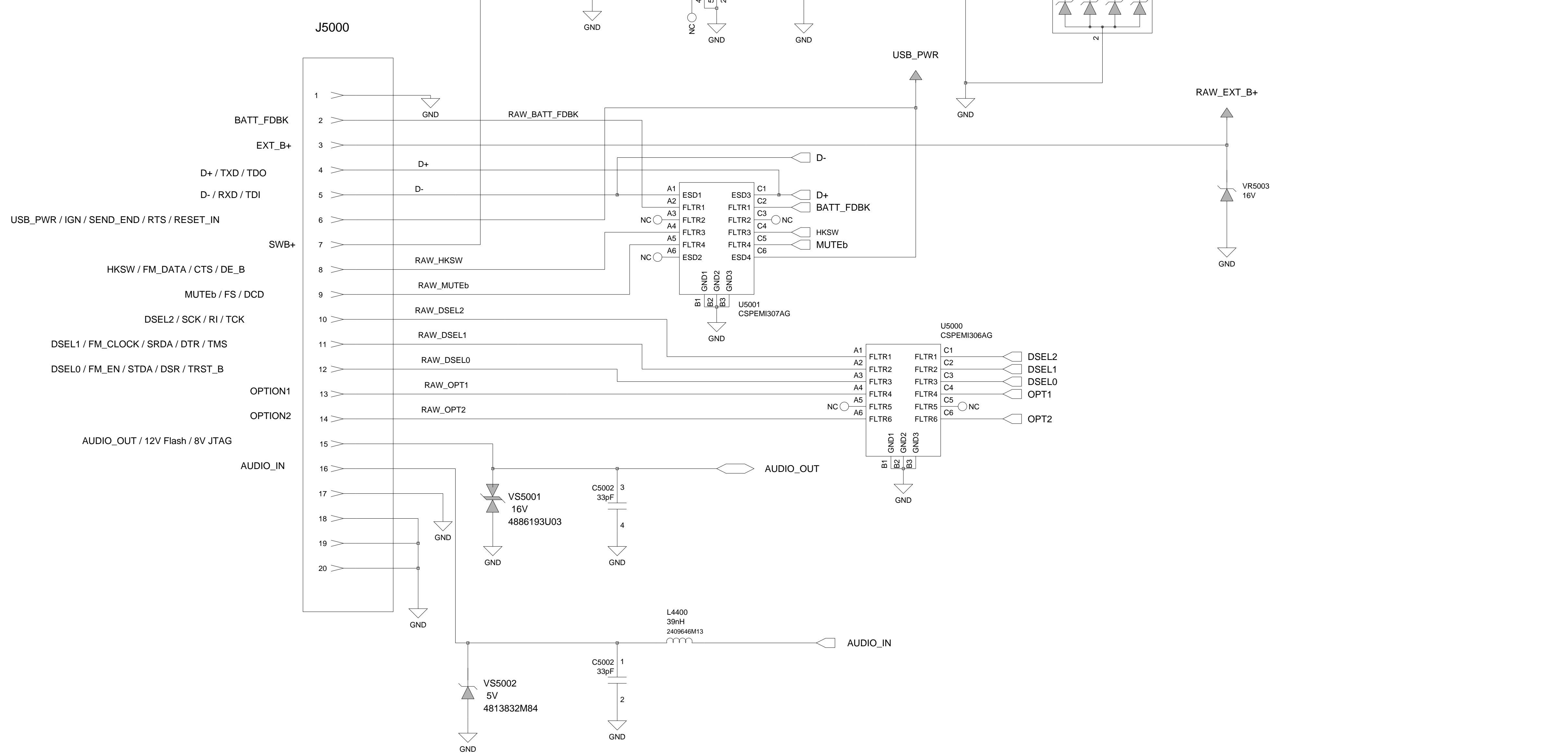
A  
B  
C  
D

KEY[0]  
KEY[1]  
KEY[2]  
KEY[3]  
KEY[4]  
KEY[5]  
KEY[6]  
KEY[7]  
KEY[8]  
KEY[9]  
KEY[10]  
KEY[11]

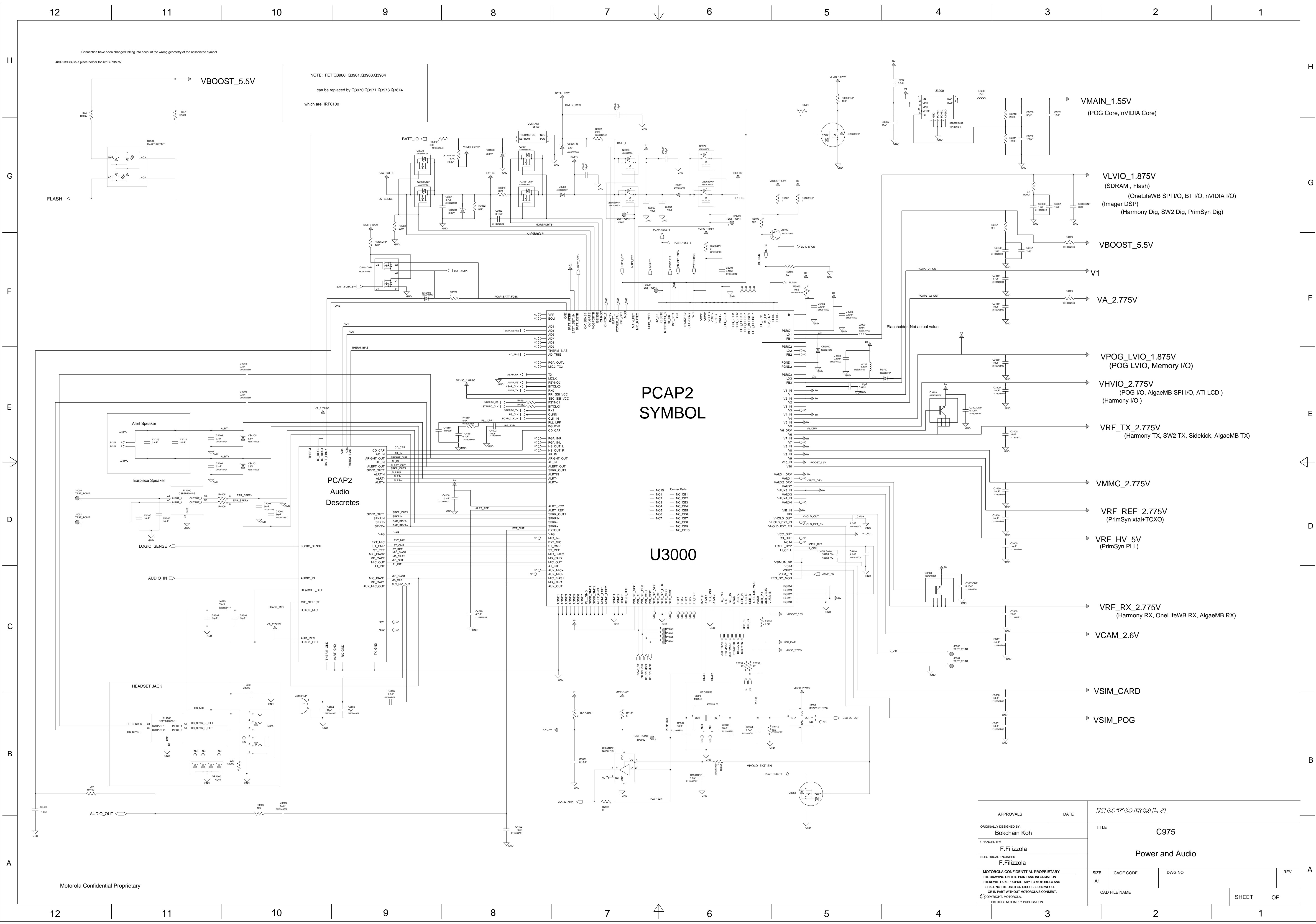


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### Accessory Connector CE Connector (0987636K06)



Engineer: Mark Melone			
Drawn by:			
R&D CHK:	TITLE: C975	Size:	
DOC CTRL CHK:	CE Accessory Connector		
MFG CTRL CHK:	REV:	Drawing Number:	Page: Of:
QA CHK:	Date: January 30th, 2004		Time:
Changed by: F.Filizola			



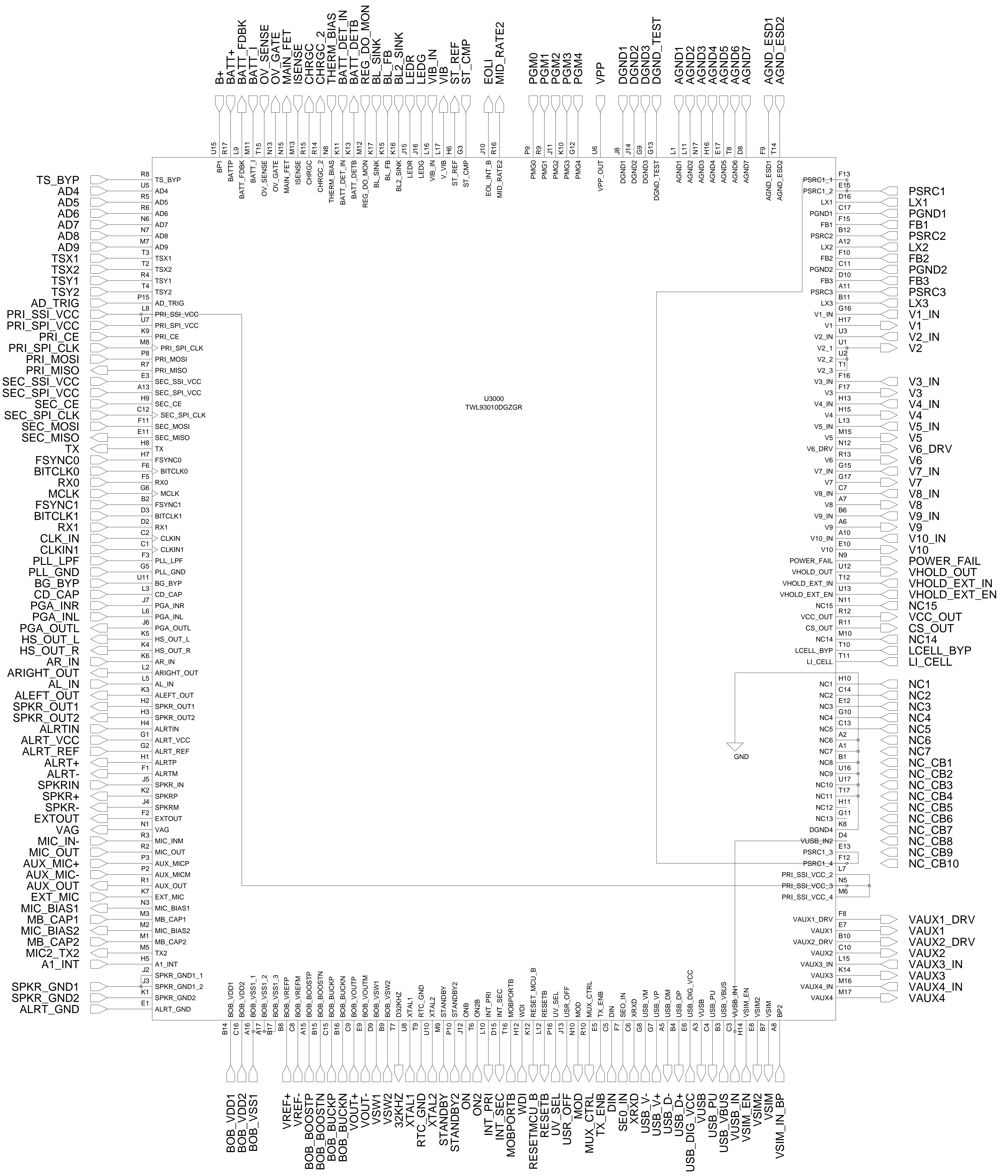
NOTE: FET Q3960, Q3961, Q3963, Q3964  
can be replaced by Q3970, Q3971, Q3973, Q3974  
which are IRF6100

PCAP2  
SYMBOL

U3000

- NC15 Control Balls
- NC1 NC1 CB1
- NC2 NC2 CB1
- NC3 NC3 CB1
- NC4 NC4 CB1
- NC5 NC5 CB1
- NC6 NC6 CB1
- NC7 NC7 CB1

APPROVALS	DATE	<b>MOTOROLA</b>	
ORIGINALLY DESIGNED BY: <b>Bokchain Koh</b>		TITLE <b>C975</b>	
CHANGED BY: <b>F.Filizzola</b>		ELECTRICAL ENGINEER <b>F.Filizzola</b>	
<small>MOTOROLA CONFIDENTIAL PROPRIETARY THE DRAWING ON THIS PRINT AND INFORMATION THEREON ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCLOSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © PROPRIETARY MOTOROLA THIS DOES NOT IMPLY PUBLICATION</small>		SIZE A1	CAGE CODE
		DWG NO	REV
		SHEET	OF



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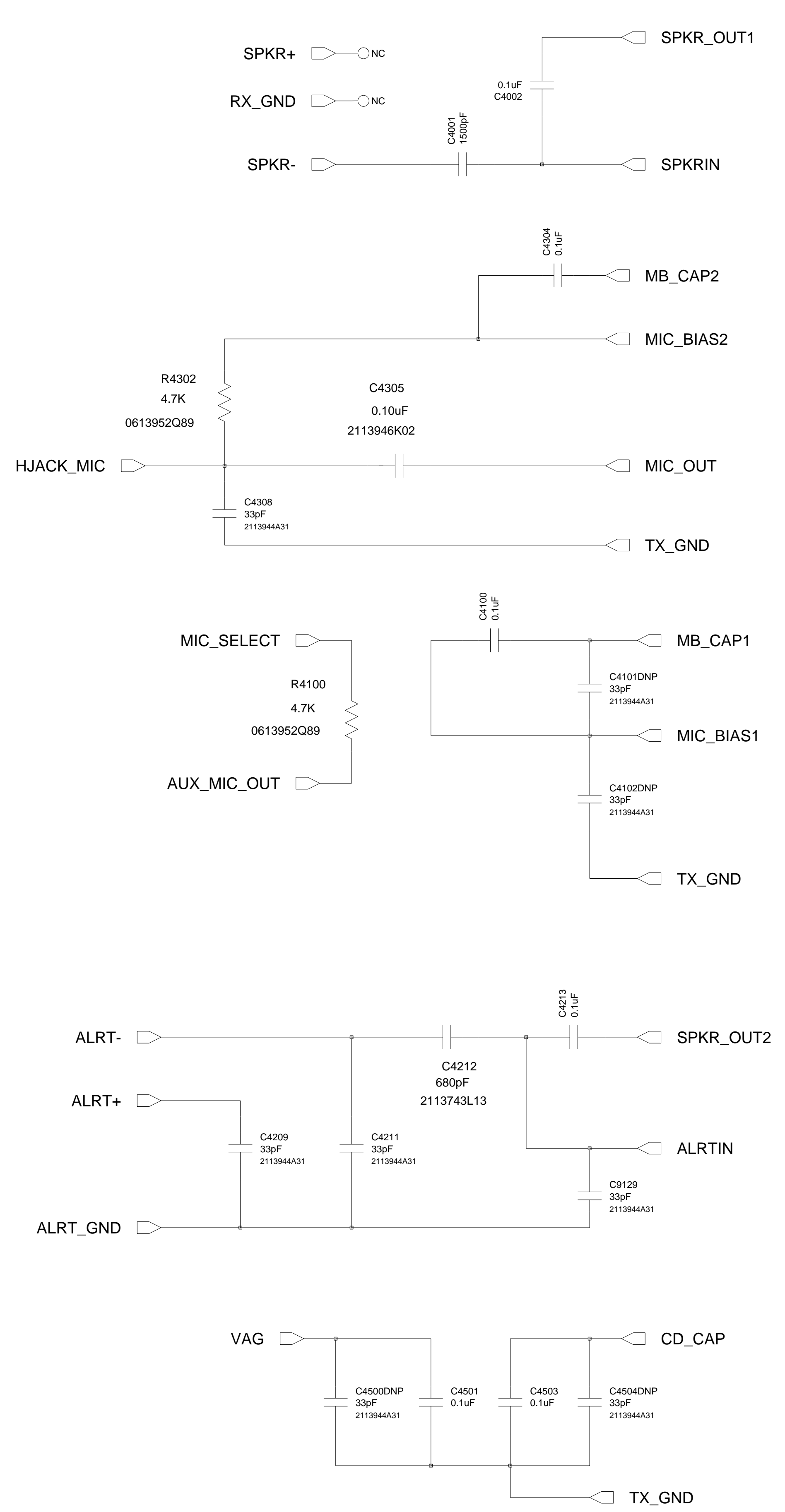
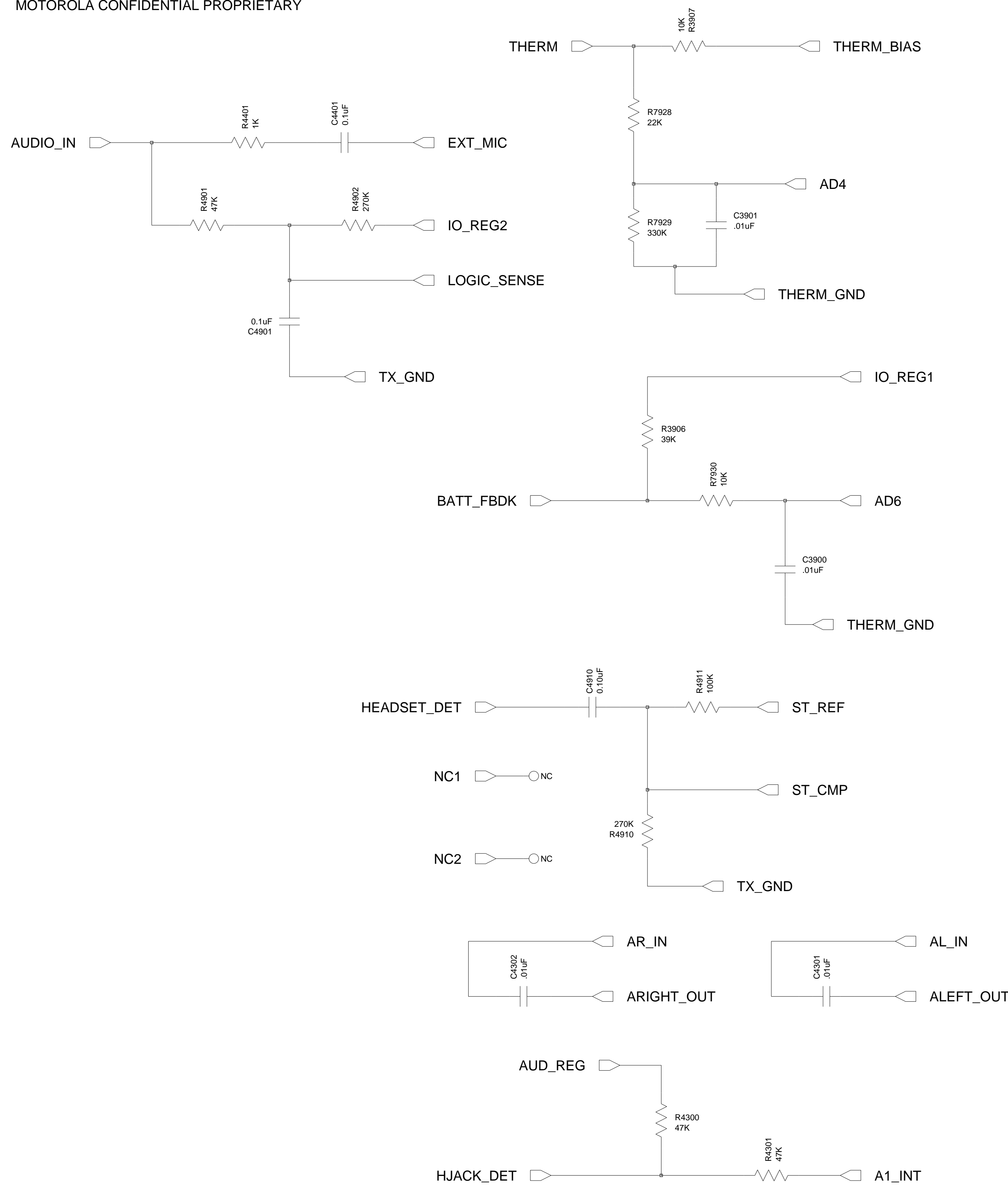
4

A

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Engineer:			
Drawn by:			
R&D CHK:	TITLE:	Size:	
DOC CTRL CHK:			
MFG CTRL CHK:			
QA CHK:	REV:	Drawing Number:	Page: Of:
Changed by:	Date:	Time:	

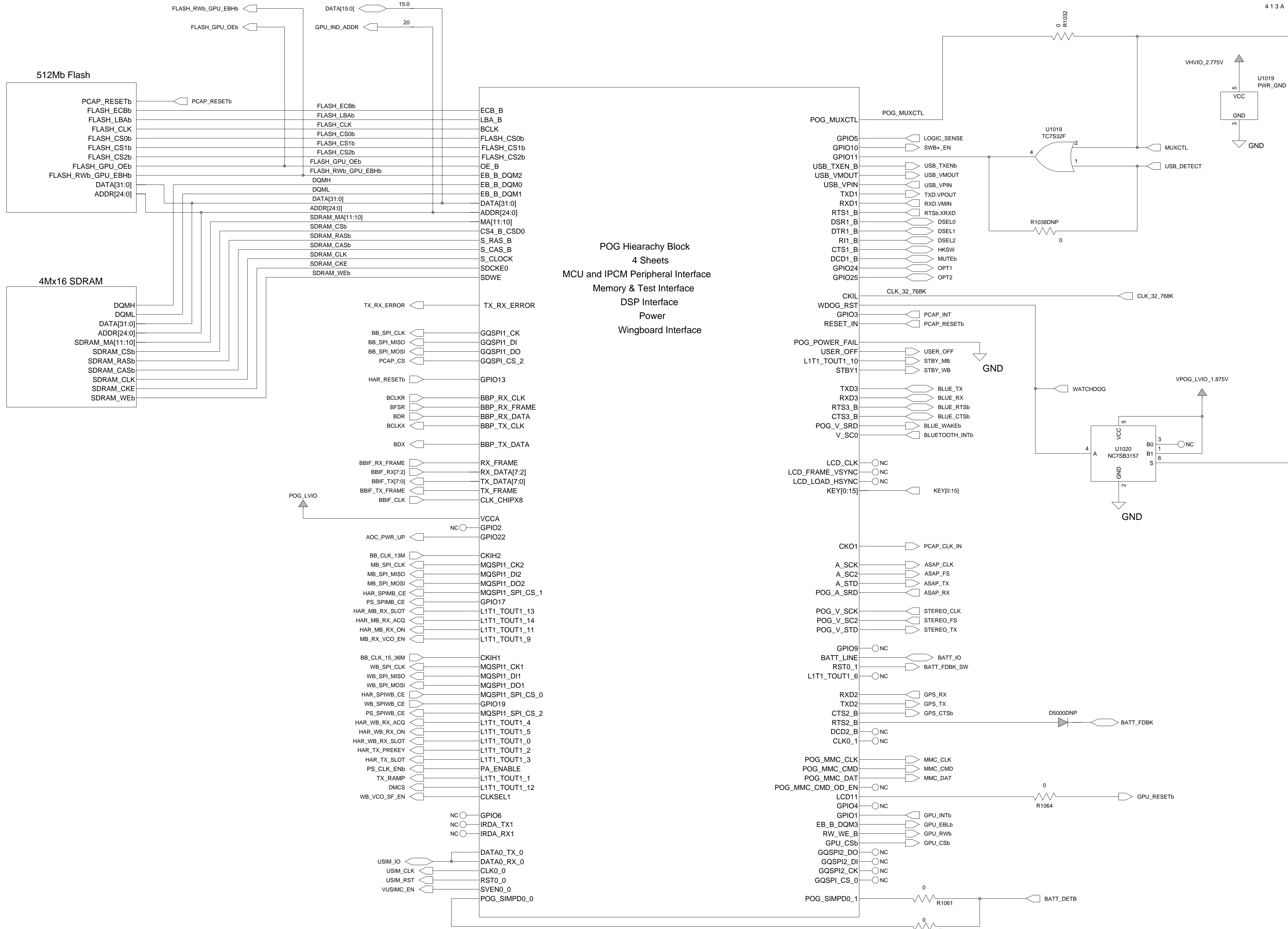


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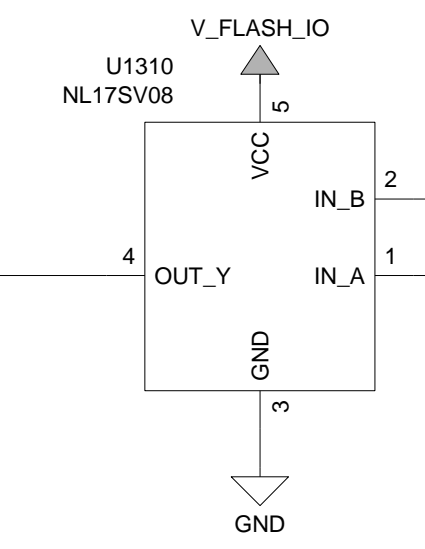
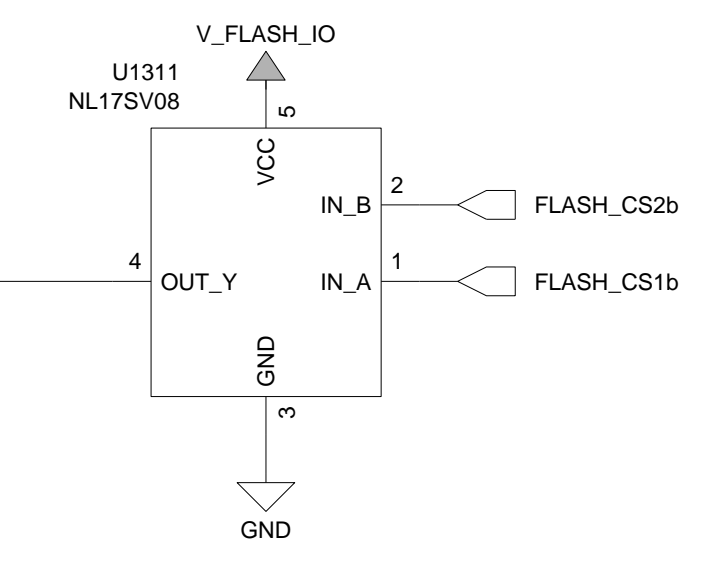
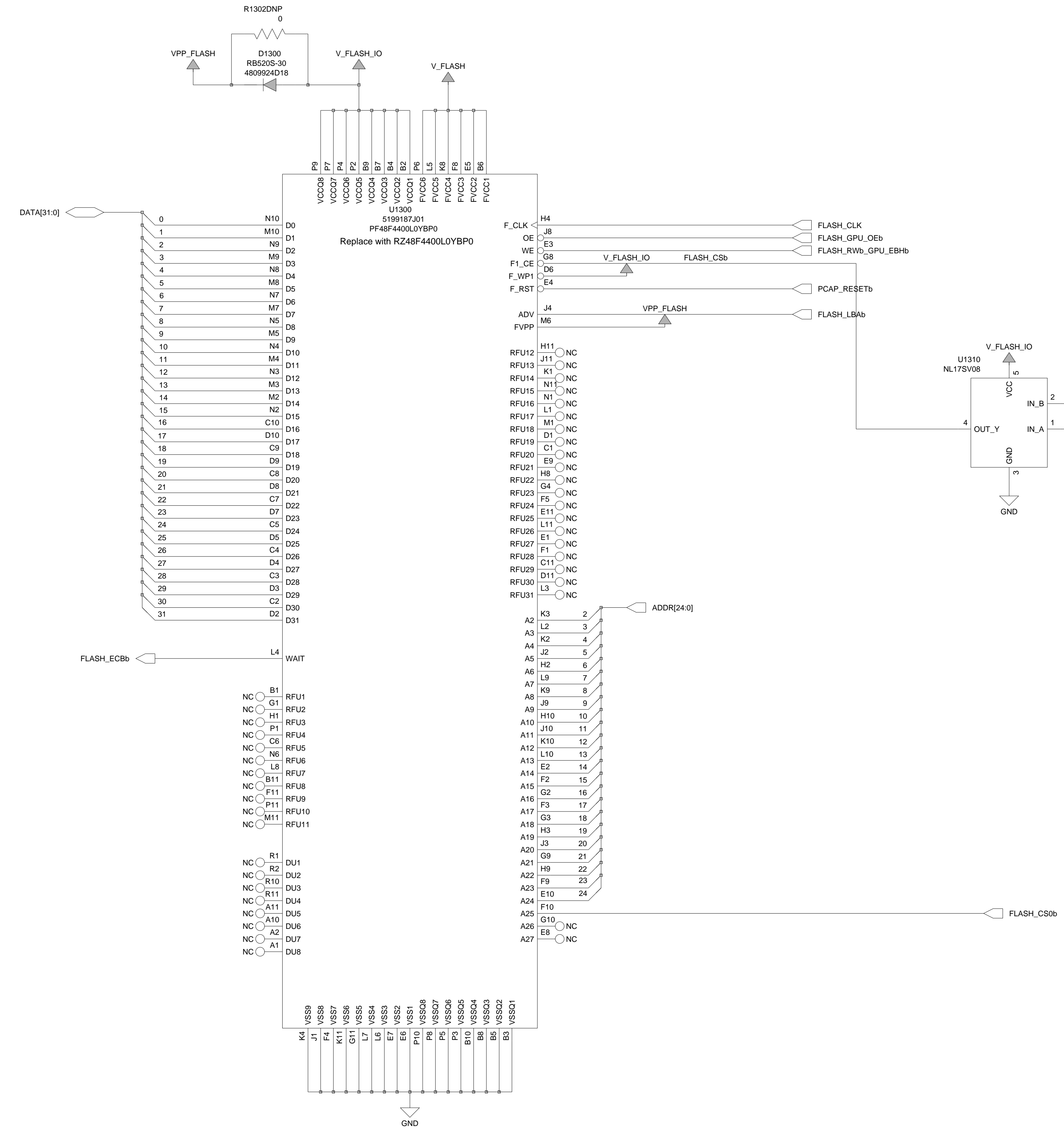
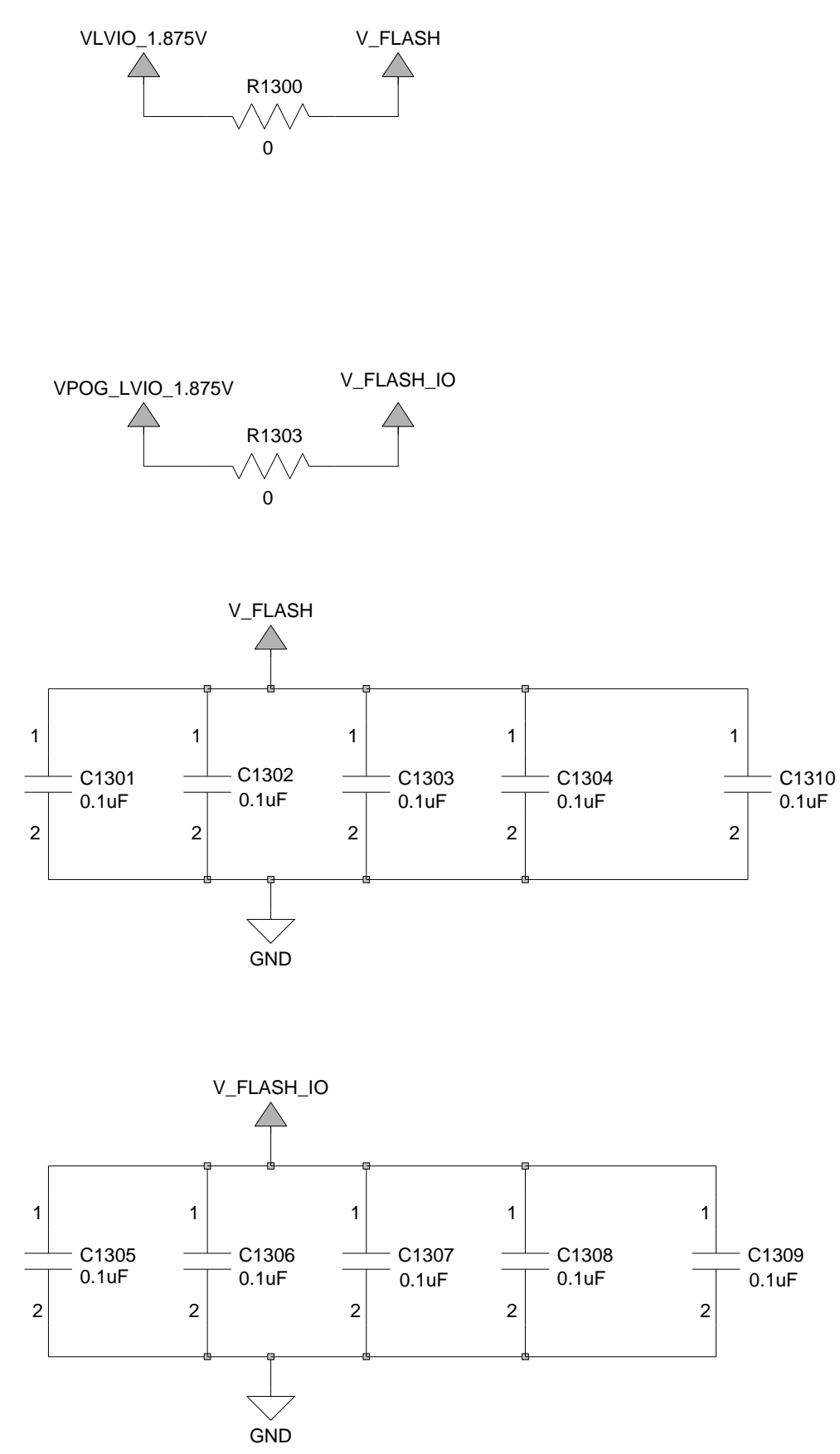
POG Hierarchy Block  
4 Sheets  
MCU and IPCM Peripheral Interface  
Memory & Test Interface  
DSP Interface  
Power  
Wingboard Interface

Engineer: Ed Naddo	TITLE: E1000 Core Logic			Size: 11x21
Drawn by: Ed Naddo				
R&D CHK:				
DOC CTRL CHK:				
MFG CTRL CHK:				
QA CHK:	REV: 1.0	Drawing Number: 848888x88	Page: 1	Of: 1
Changed by: wlen01	Date: Friday, March 1, 2002	Time: 1:16:44 pm		



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Engineer:	MOTOROLA INC.			
Drawn by:				
R&D CHK:	TITLE: V975	Size:		
DOC CTRL CHK:	Flash Memory			
MFG CTRL CHK:	REV:	Drawing Number:	Page:	Of:
QA CHK:	Date:			Time:
Changed by:				



MOTOROLA CONFIDENTIAL PROPRIETARY

4 1 3 A

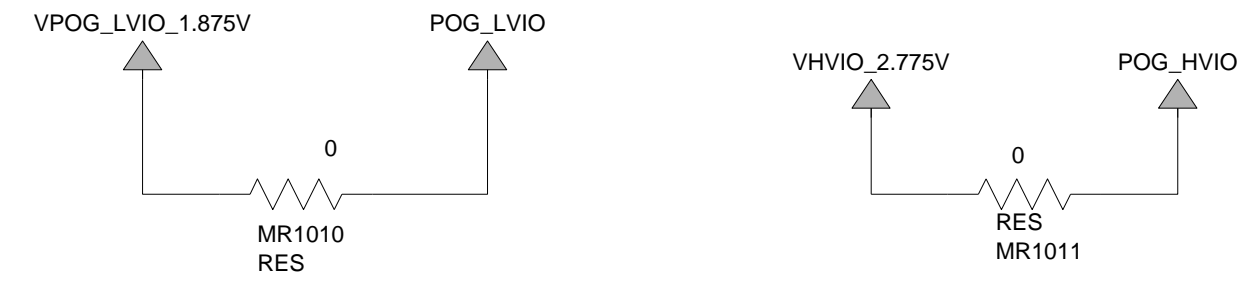
Engineer:  
Ed Nadeo  
Drawn by:  
Ed Nadeo  
R&D CHK:  
DOC CTRL CHK:  
MFG CTRL CHK:  
QA CHK:  
wien01

**MOTOROLA INC.**

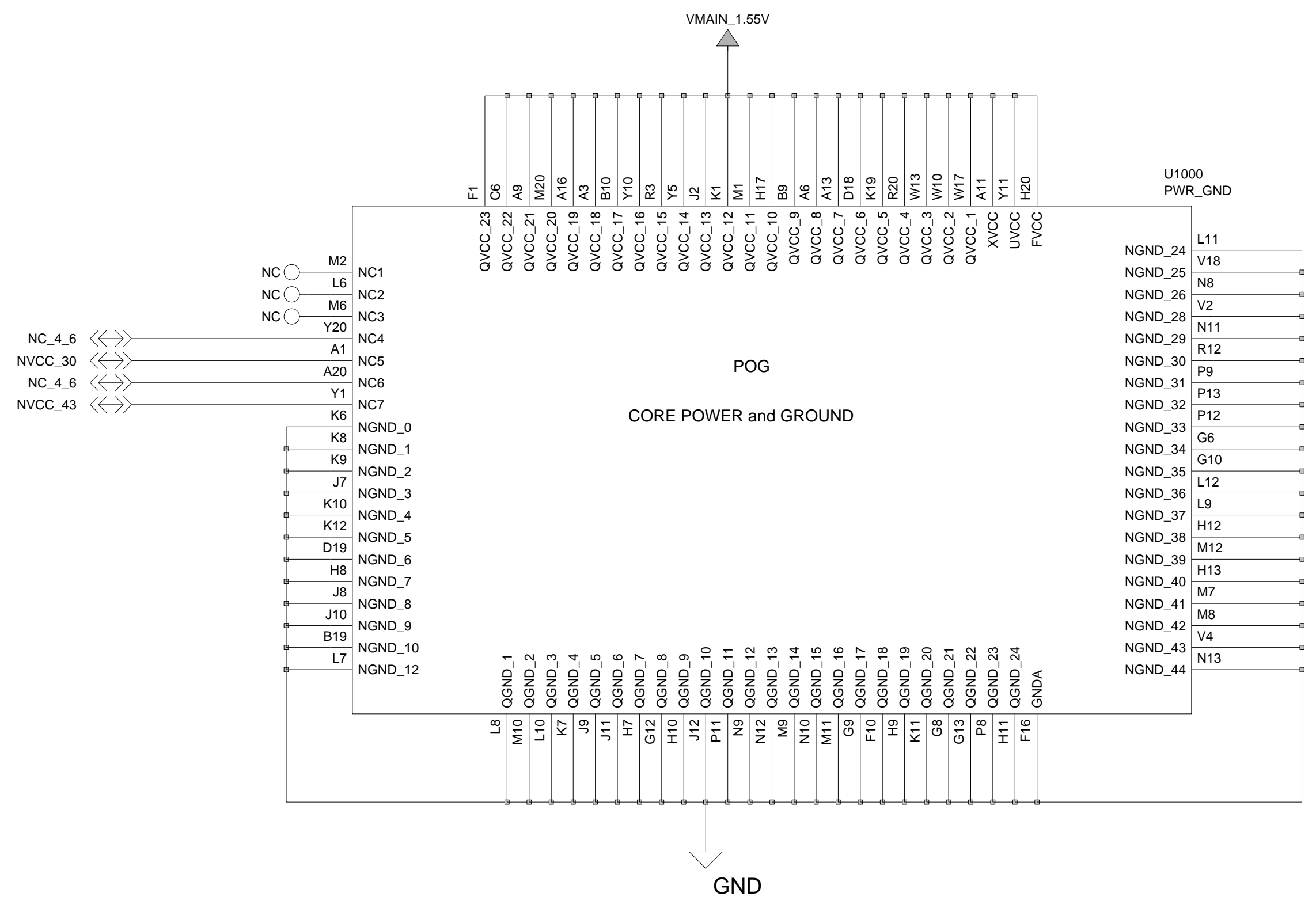
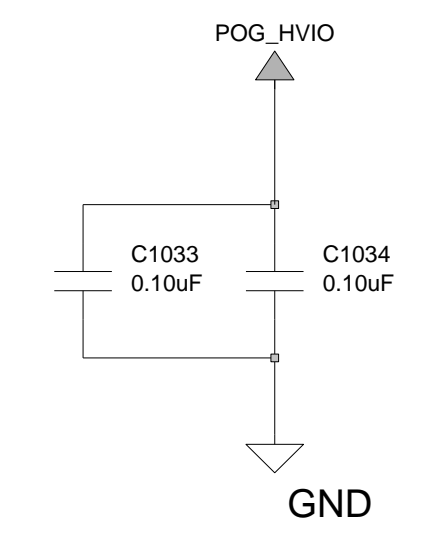
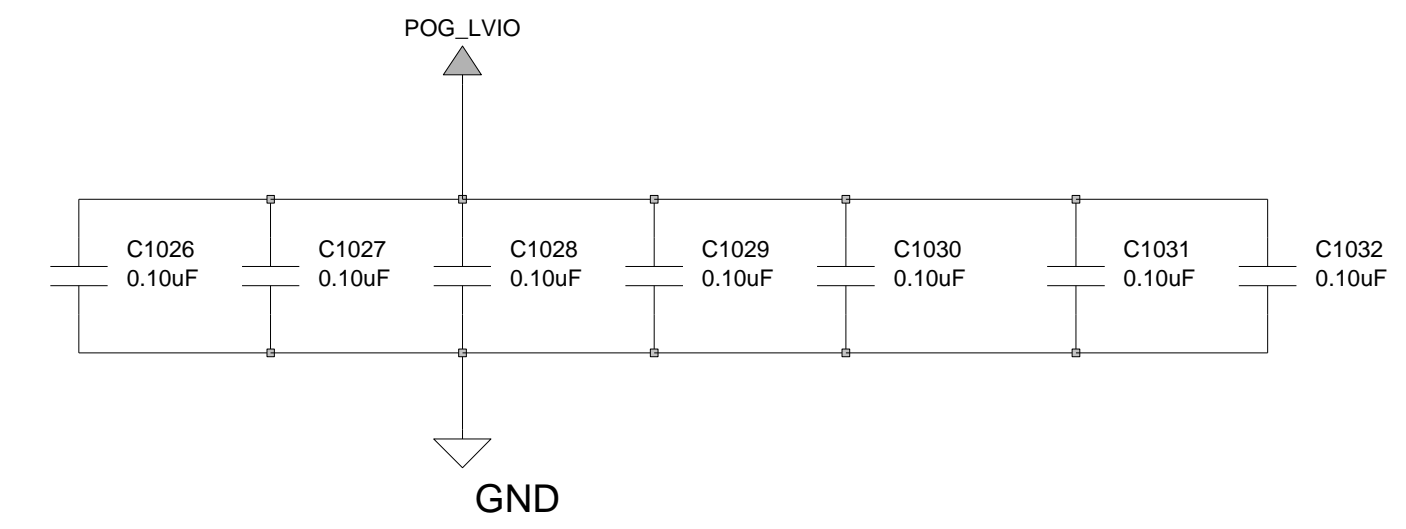
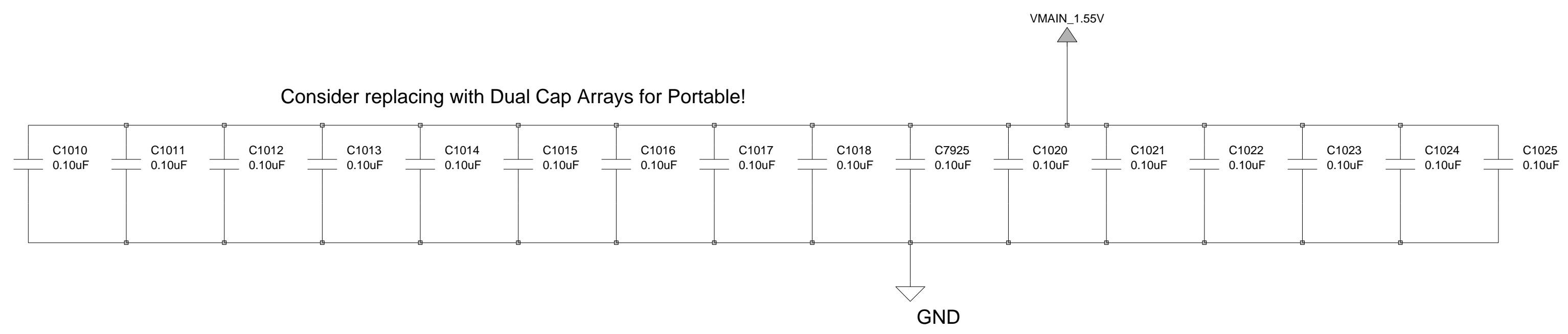
TITLE: Talon Integrated  
POG  
Power

REV: 1    Drawing Number: 8488888x88    Page: 1    Of: 4

Date: Tuesday, February 26, 2002    Time: 2:56:17 pm



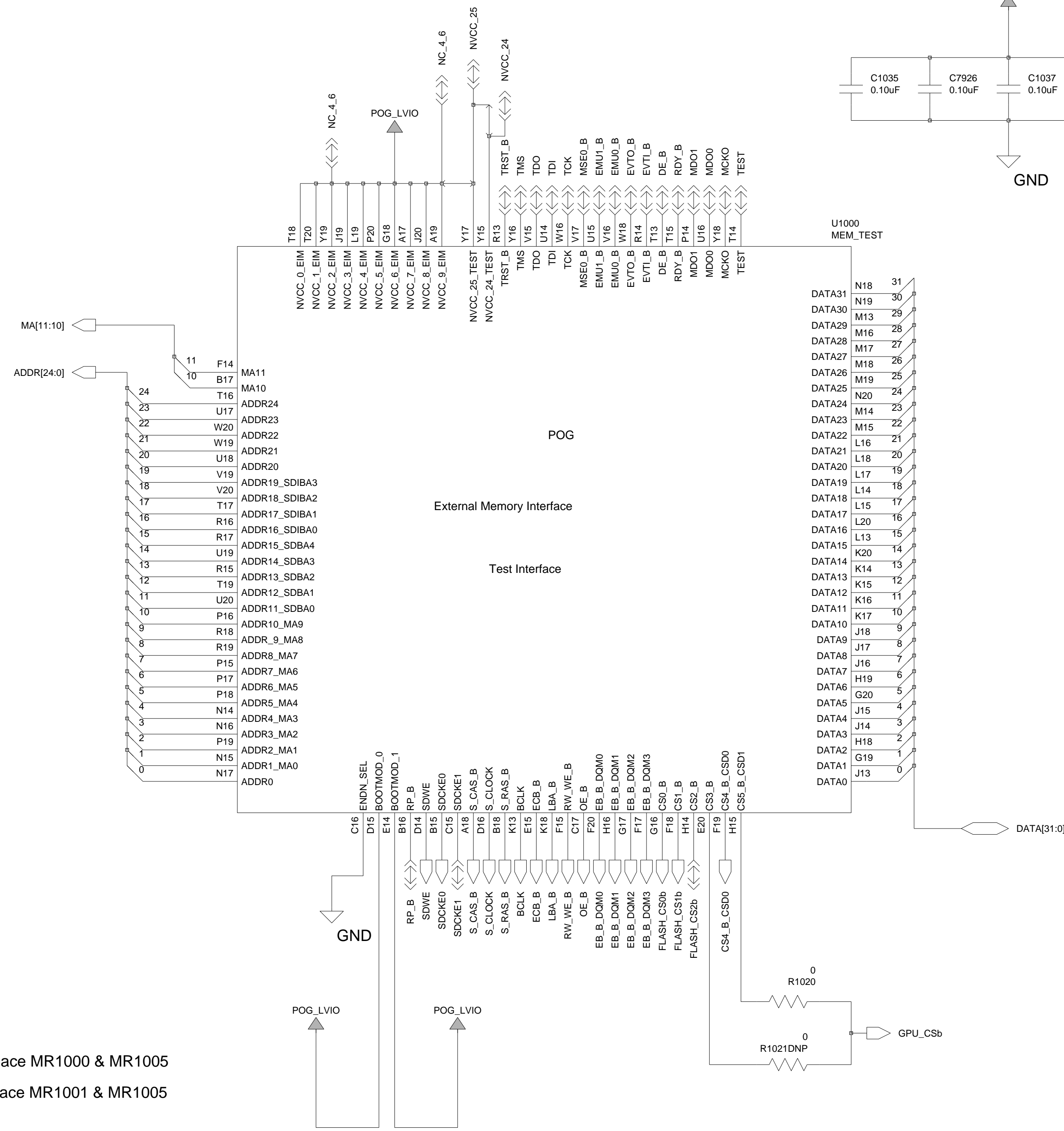
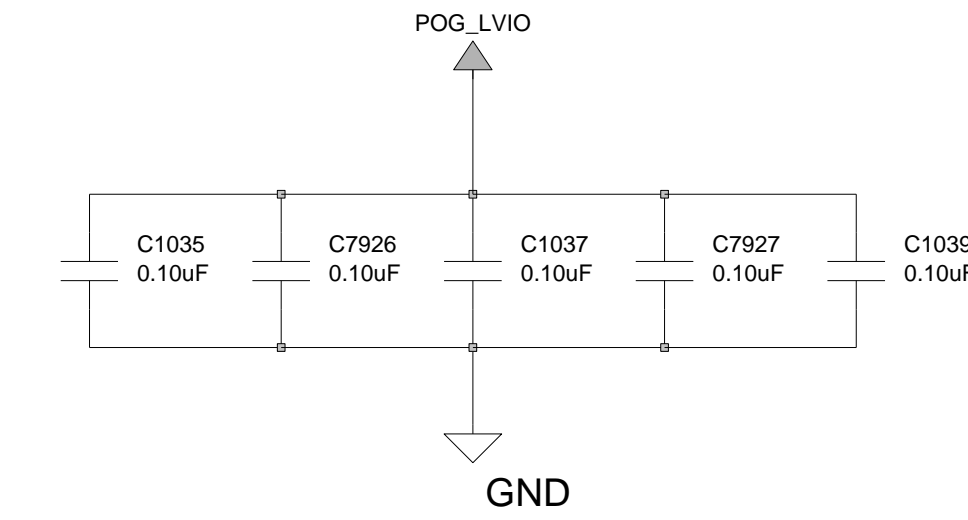
Consider replacing with Dual Cap Arrays for Portable!





Engineer: Ed Naddo	TITLE: Talon Integrated POG Memory and Test Interface			Size: 11x17
Drawn by: Ed Naddo				
R&D CHK:				
DOC CTRL CHK:				
MFG CTRL CHK:				
QA CHK:	REV: 1.0	Drawing Number: 8488888x88	Page: 3	Of: 4
Changed by: wien01	Date: Tuesday, February 26, 2002			Time: 6:15:09 pm

Replace with dual cap arrays for portable!



Internal Boot: Place MR1000 & MR1005  
 External Boot: Place MR1001 & MR1005

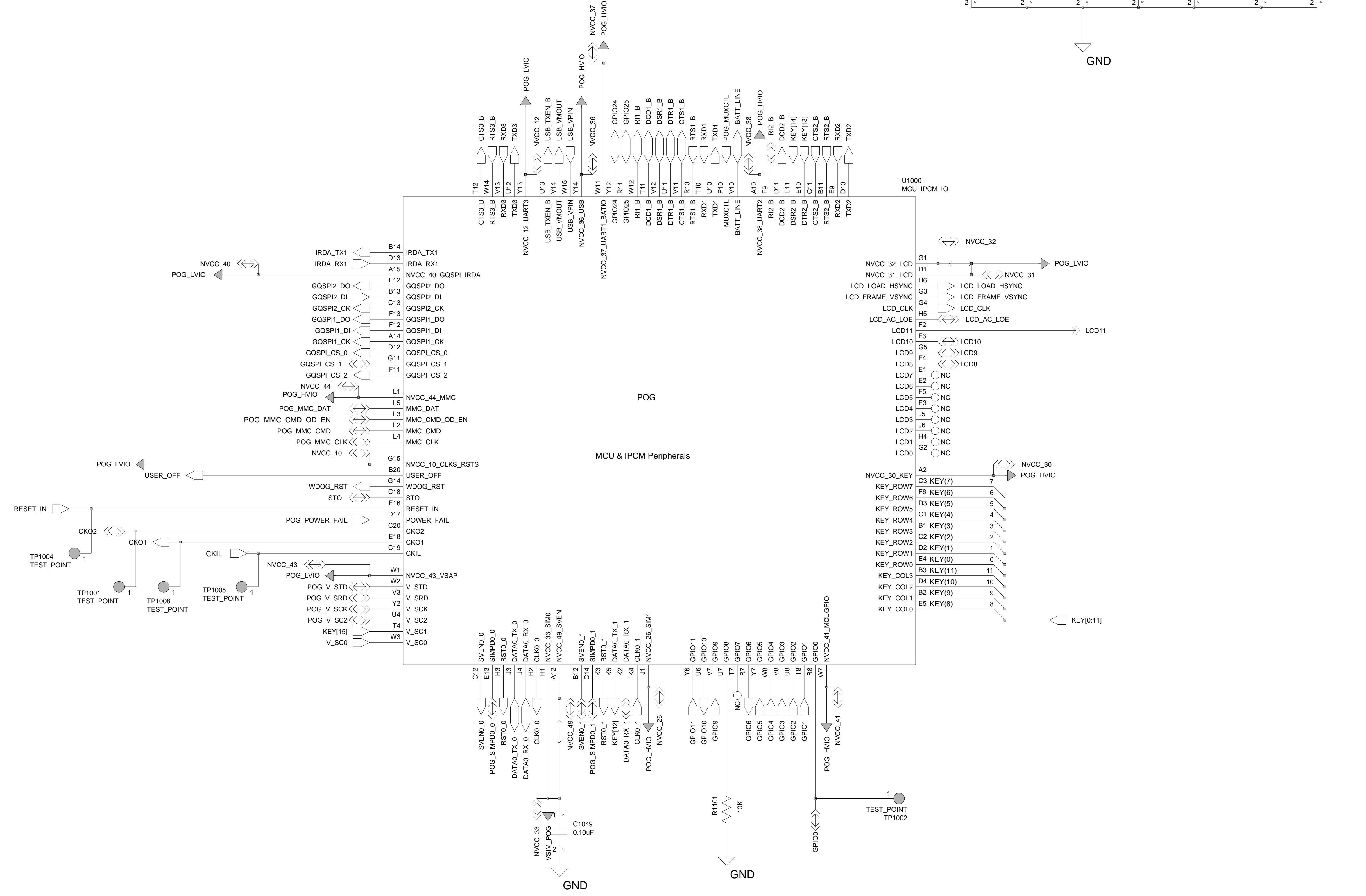
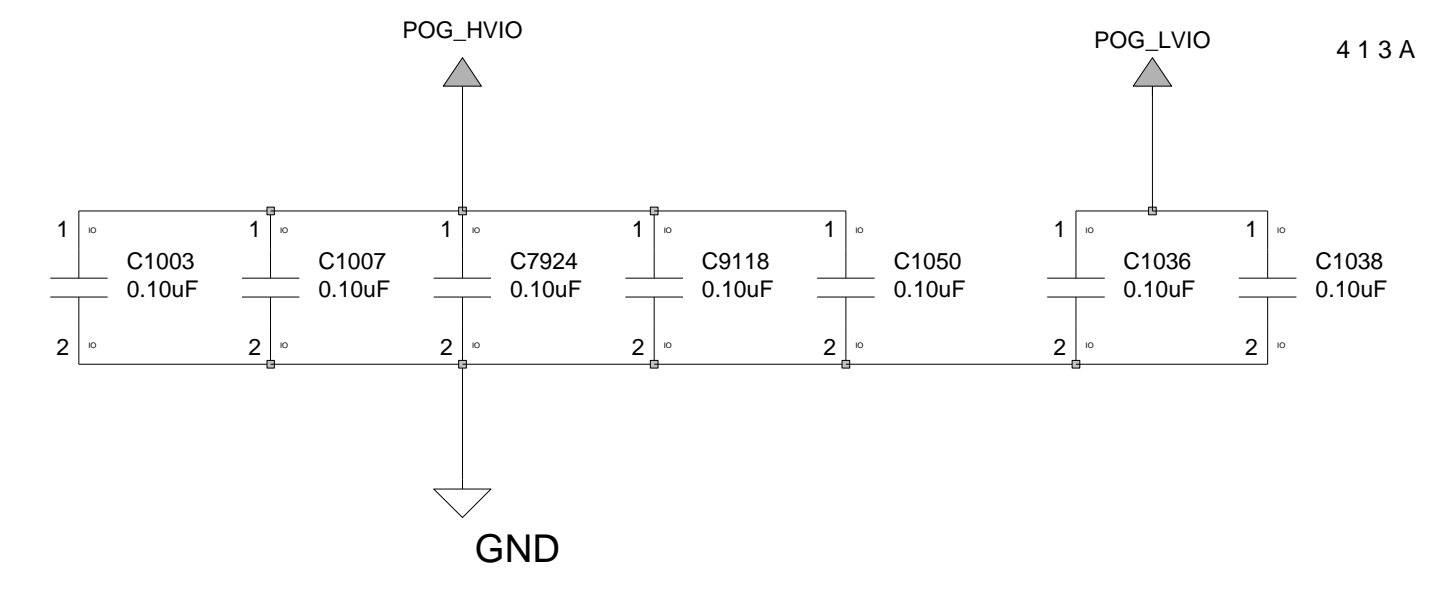
POG & Memories (Flash and SDRAM) should be STACKED.

A  
B  
C  
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Engineer: Ed Naddeo	TITLE: TALON Integrated			Size: 11x17
Drawn by: Ed Naddeo	REV: 1.0			Page: 2
R&D CHK:	Drawing Number: 848888x88			Of: 4
DOC CTRL CHK:	Date: Tuesday, February 26, 2002			Time: 4:39:04 pm
MFG CTRL CHK:	Accessories Interface			
QA CHK:	wien01			

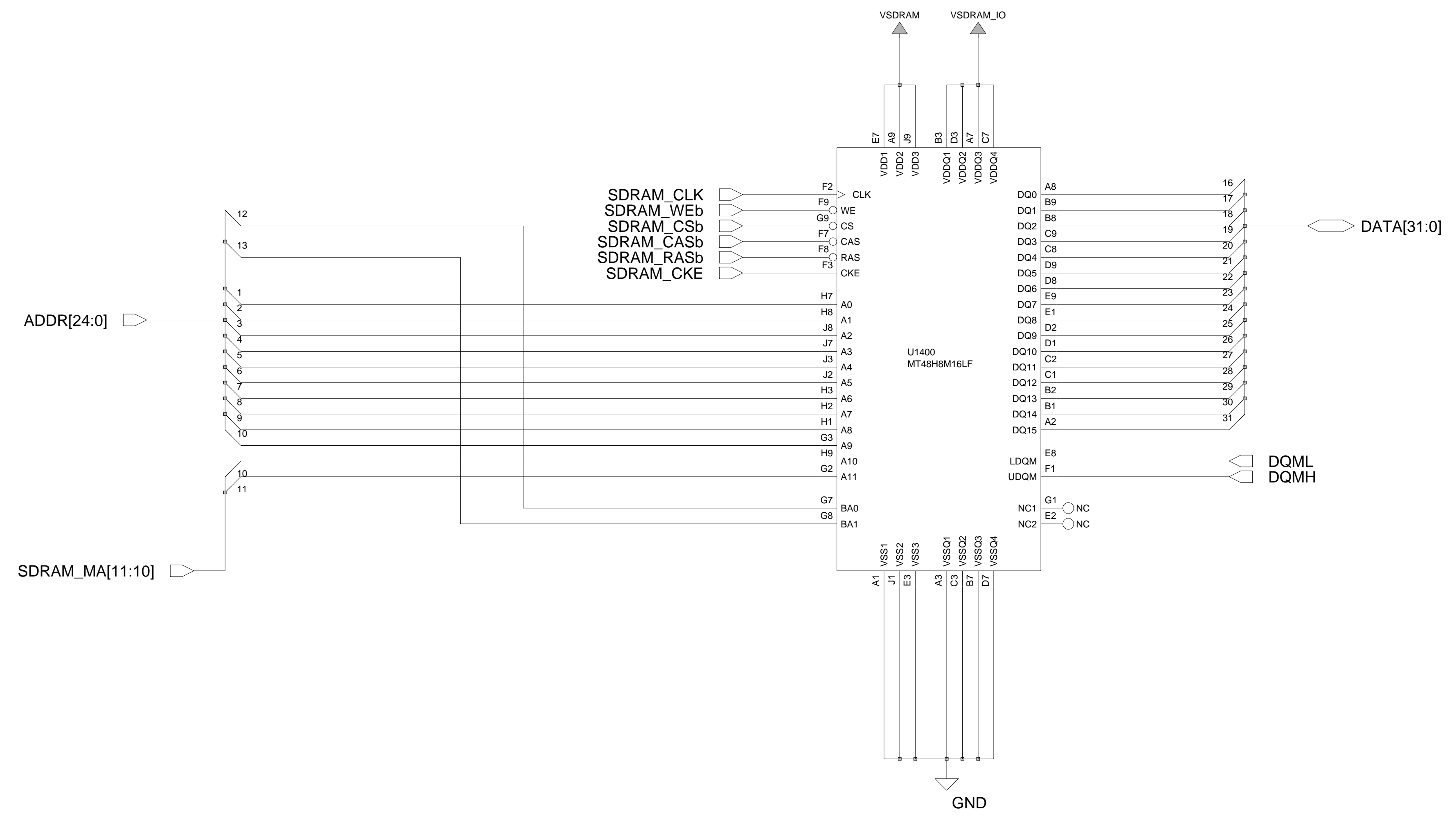
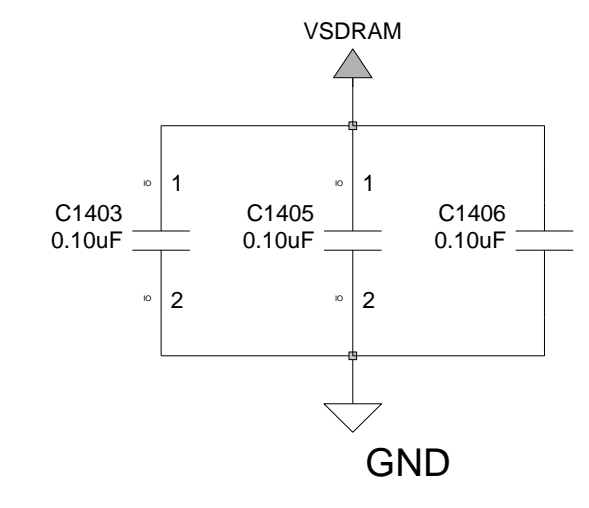
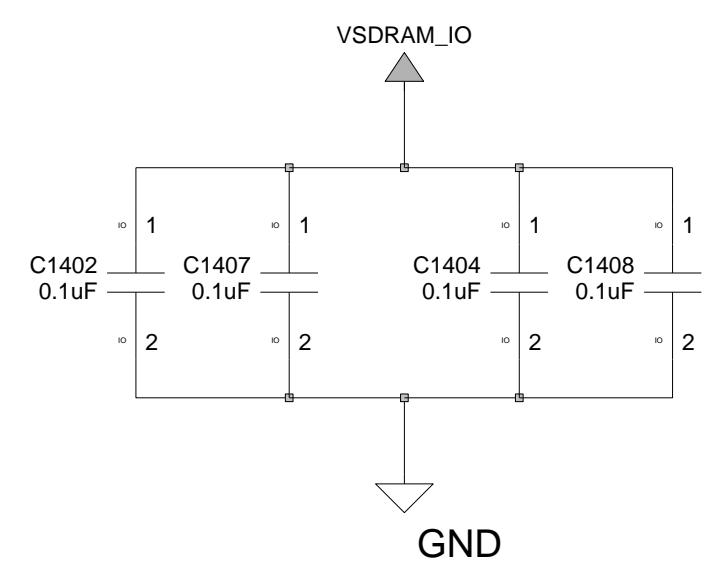
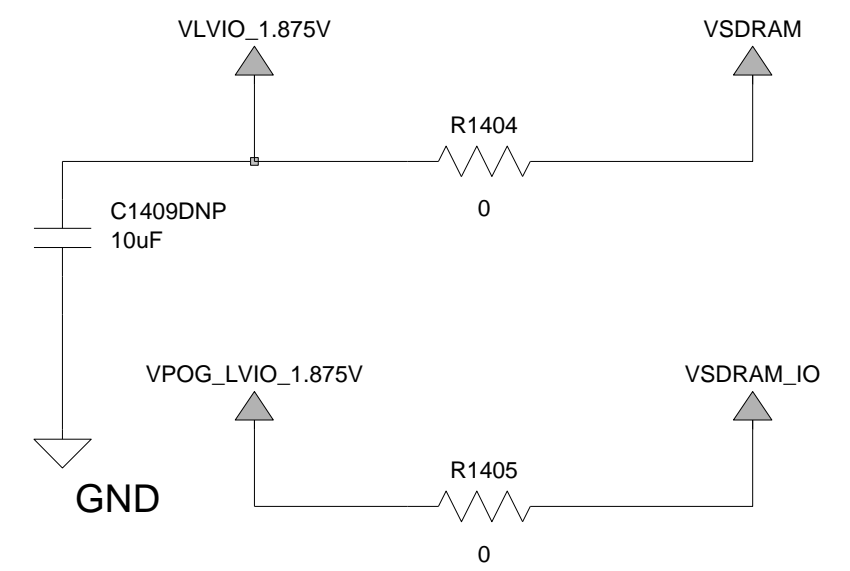


F.Filizola  
January 28, 2004

MOTOROLA CONFIDENTIAL PROPRIETARY

4 1 3 A

Engineer: Ed Naddeo				
Drawn by: Ed Naddeo				
R&D CHK:	TITLE: Talon Integrated 4Mx16 SDRAM	Size: 11x17		
DOC CTRL CHK:				
MFG CTRL CHK:				
QA CHK:	REV: 1.0	Drawing Number: 8488888x88	Page: 1	Of: 1
Changed by: wien01	Date: Friday, March 1, 2002	Time: 3:37:06 pm		



REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED

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8

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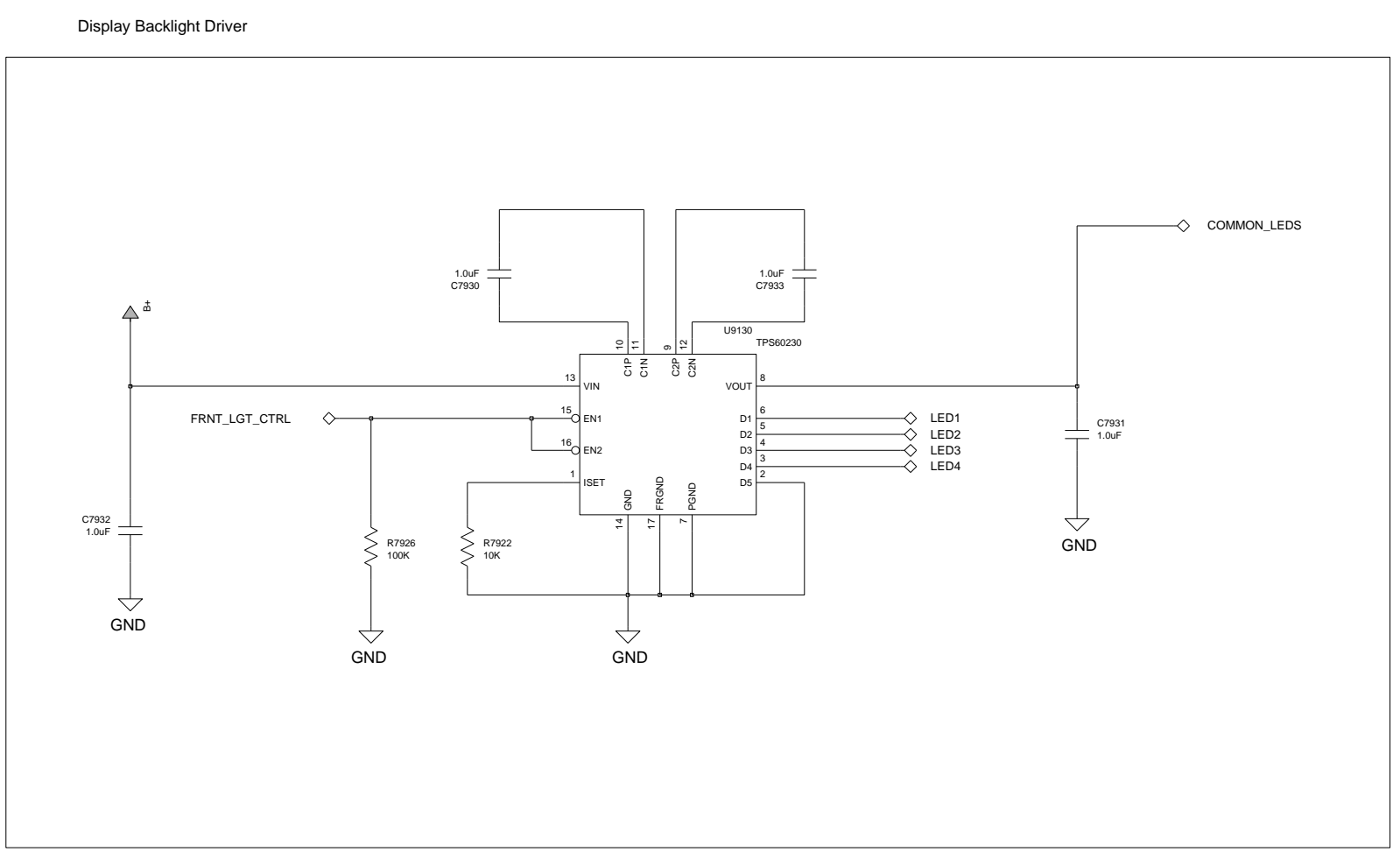
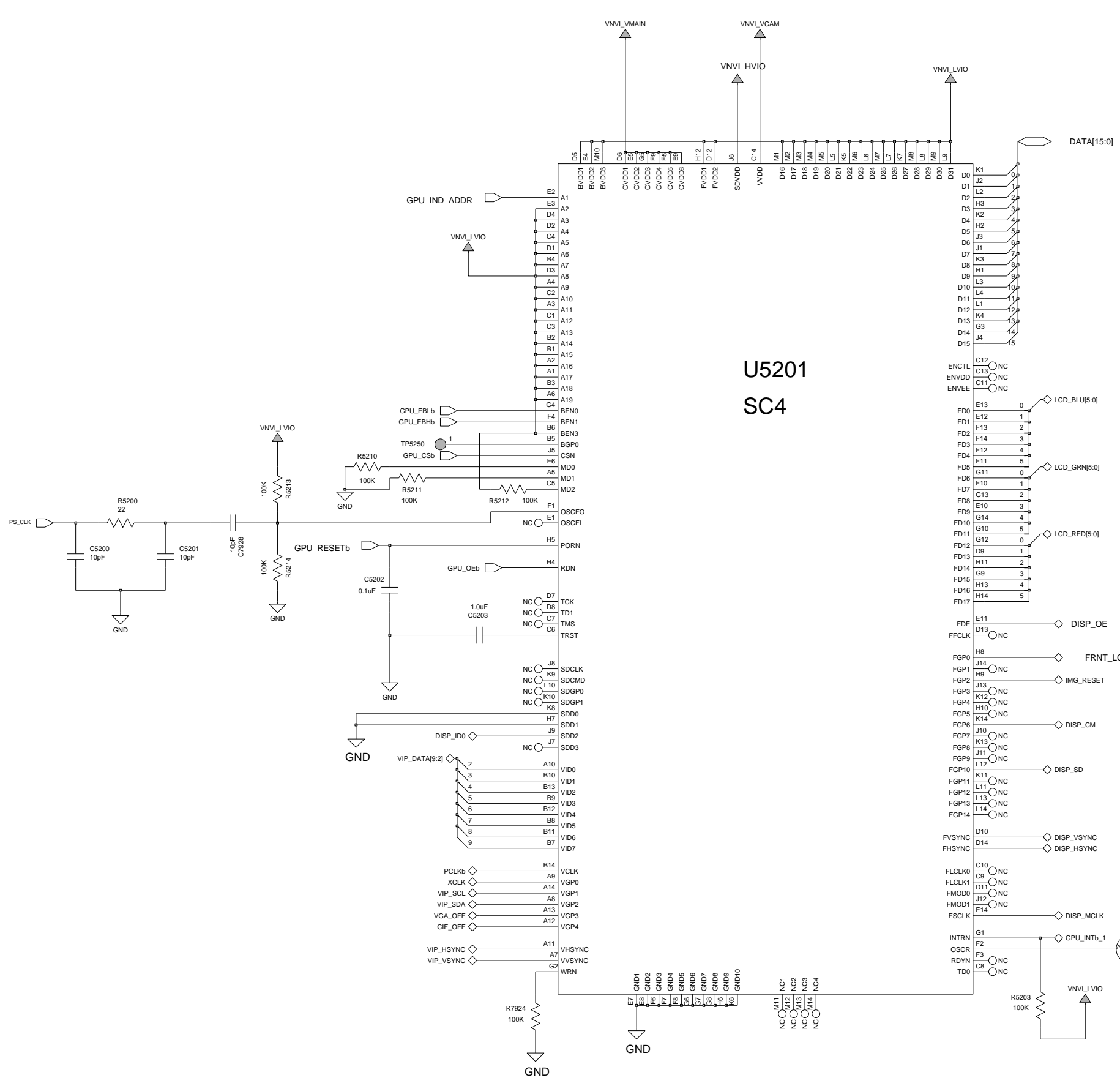
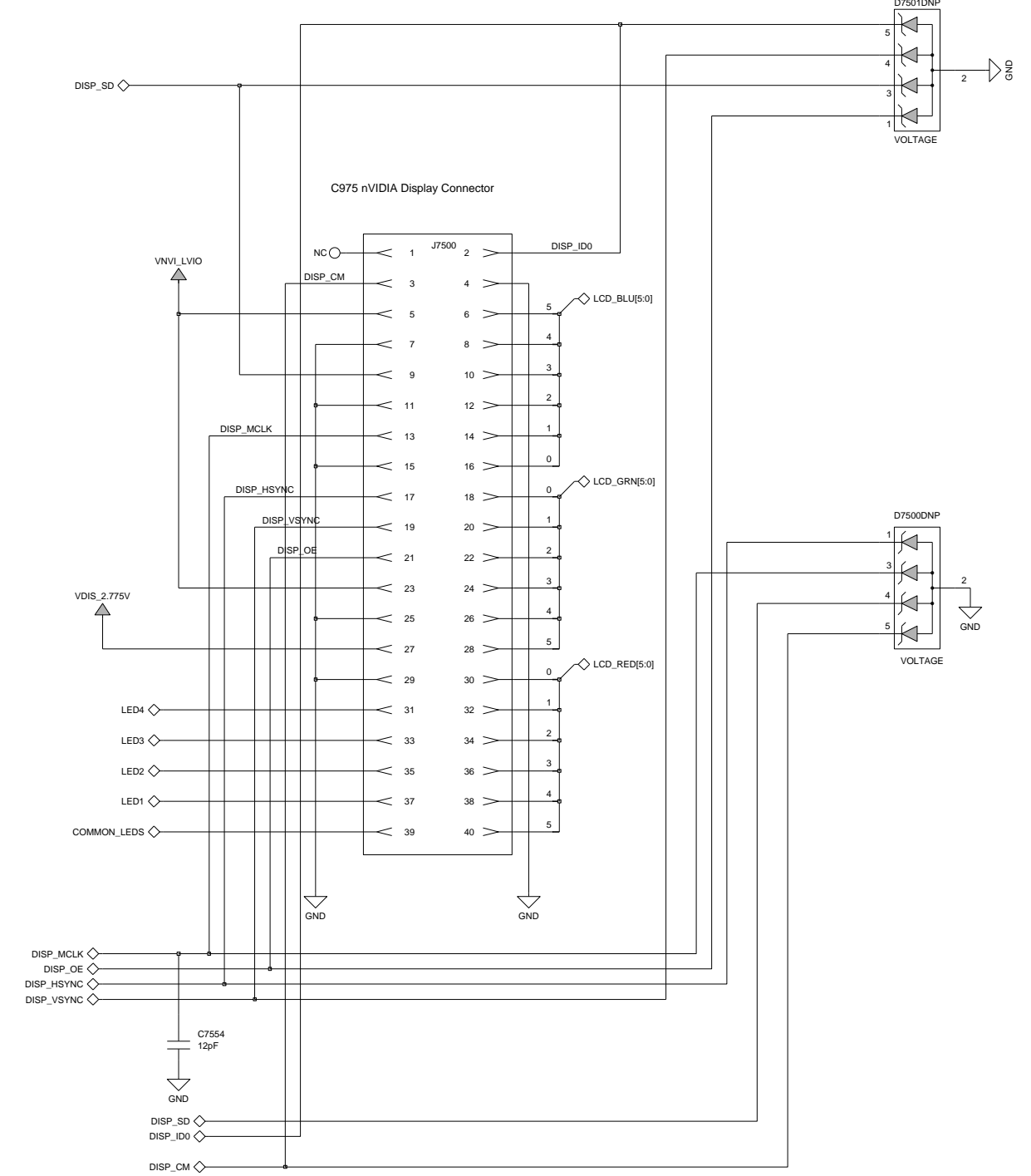
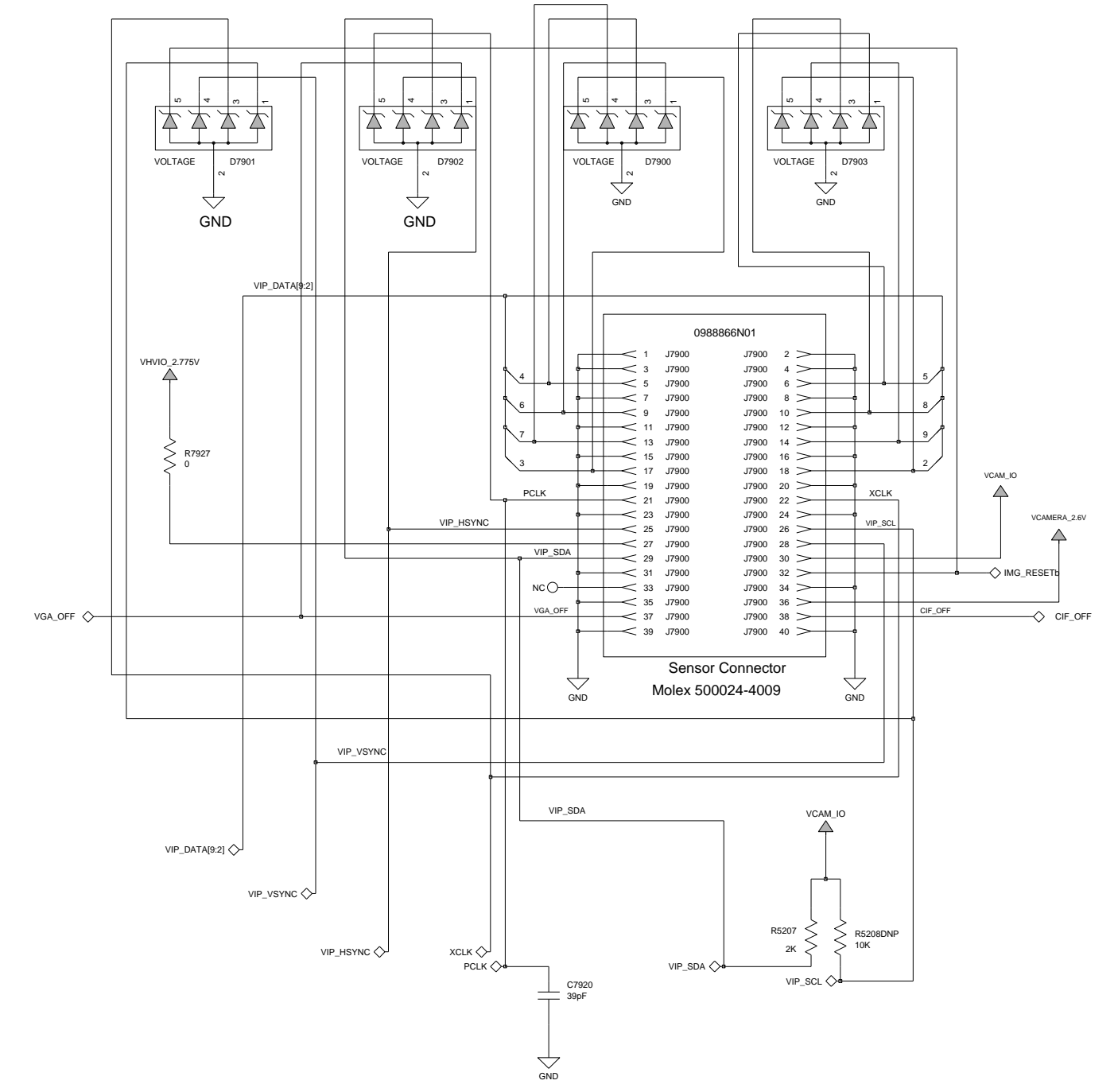
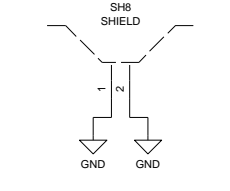
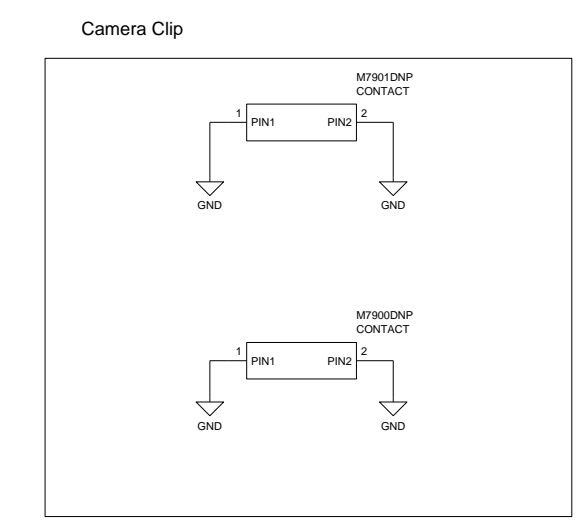
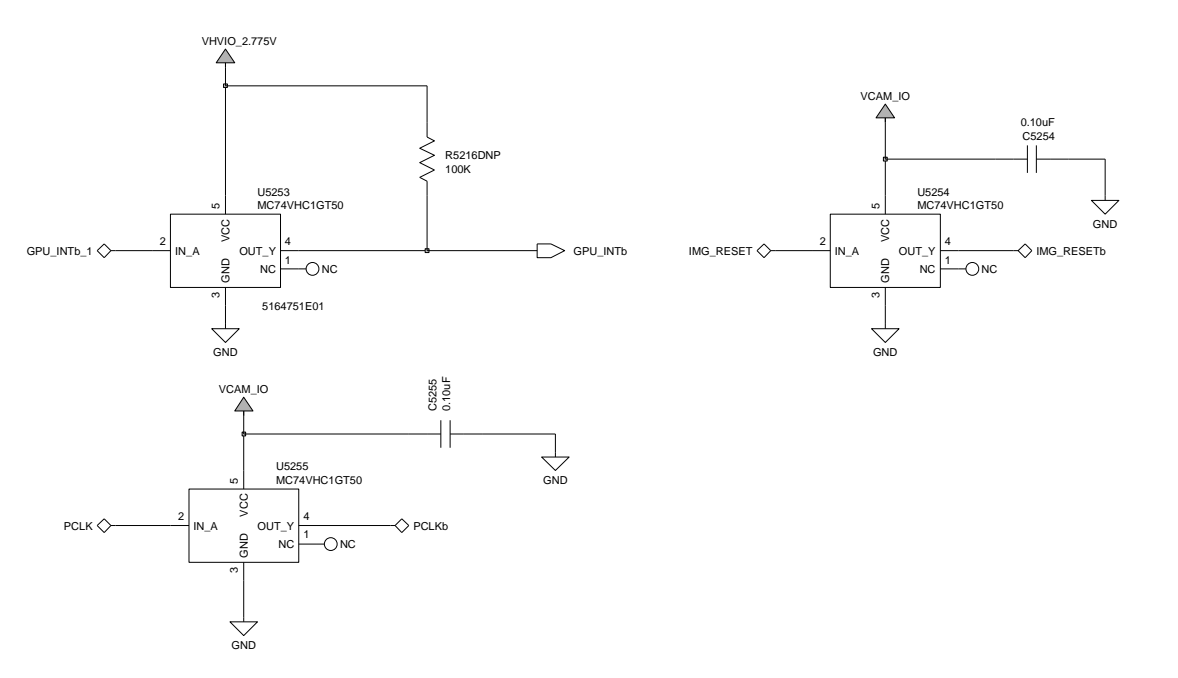
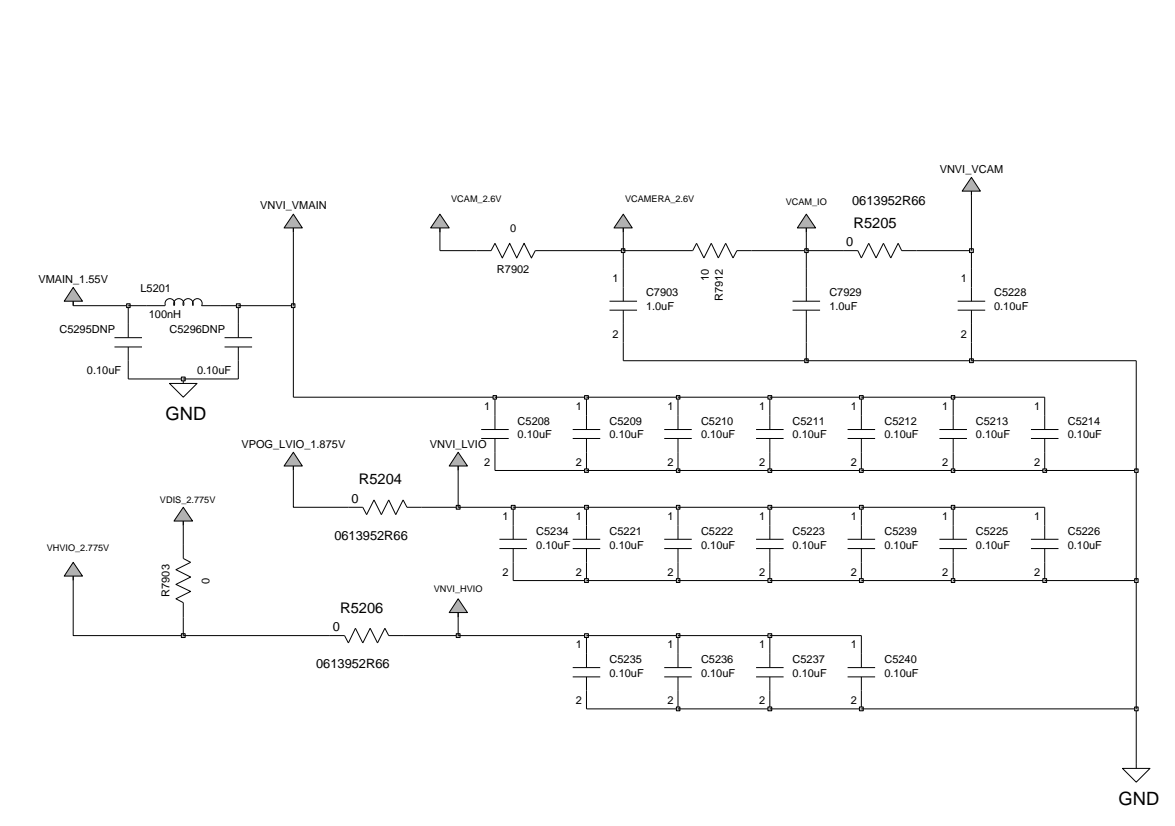
C

B

B

A

A



Plot Scale = 0.7

Engineer:				
Drawn by:				
R&D CHK:	TITLE: E1000	Size:		
DOC CTRL CHK:	nVIDIA - Video & Graphics			
MFG CTRL CHK:	REV:	Drawing Number:	Page:	Of:
QA CHK:	Date:			Time:
Changed by:				1:36:56 pm