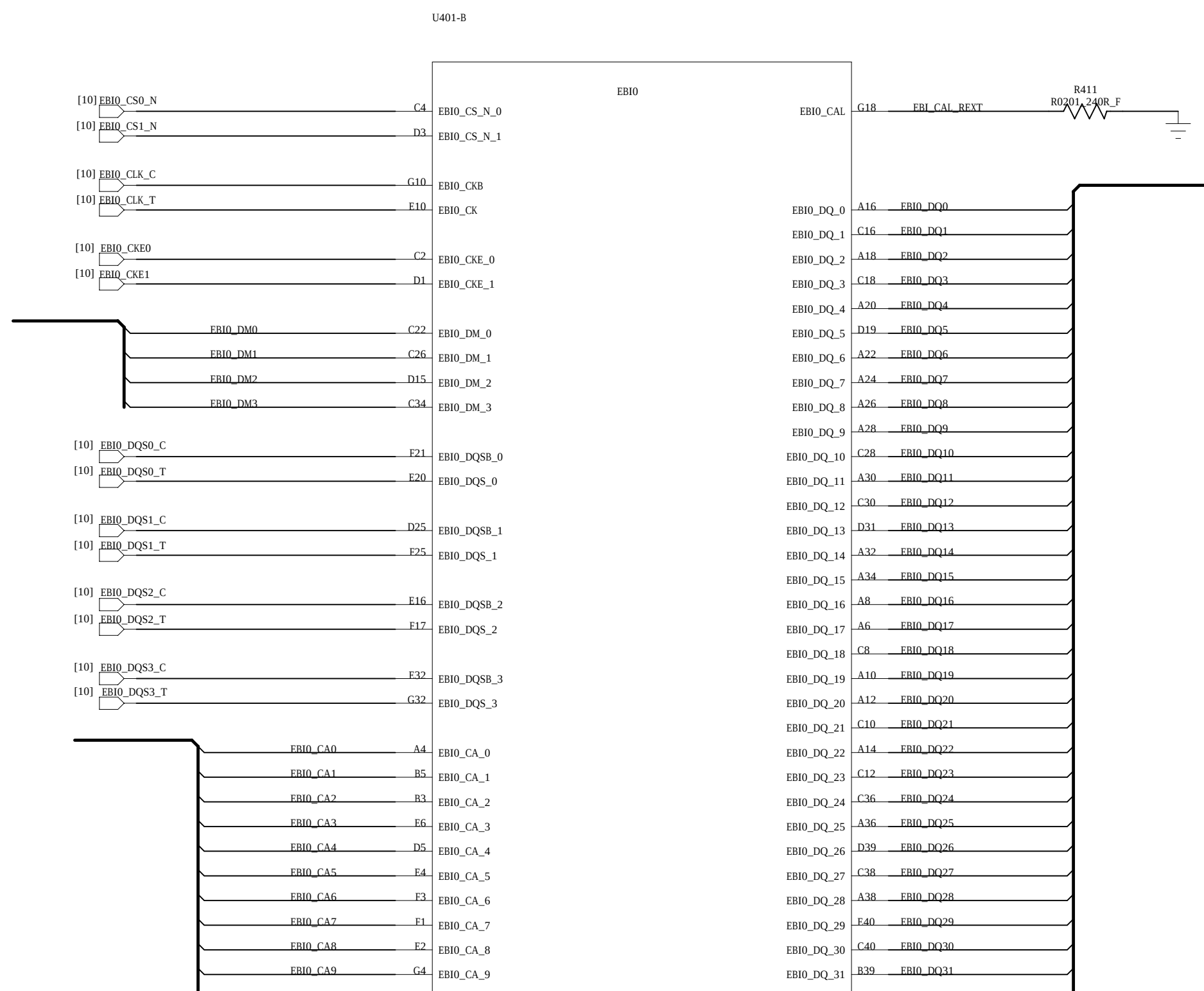
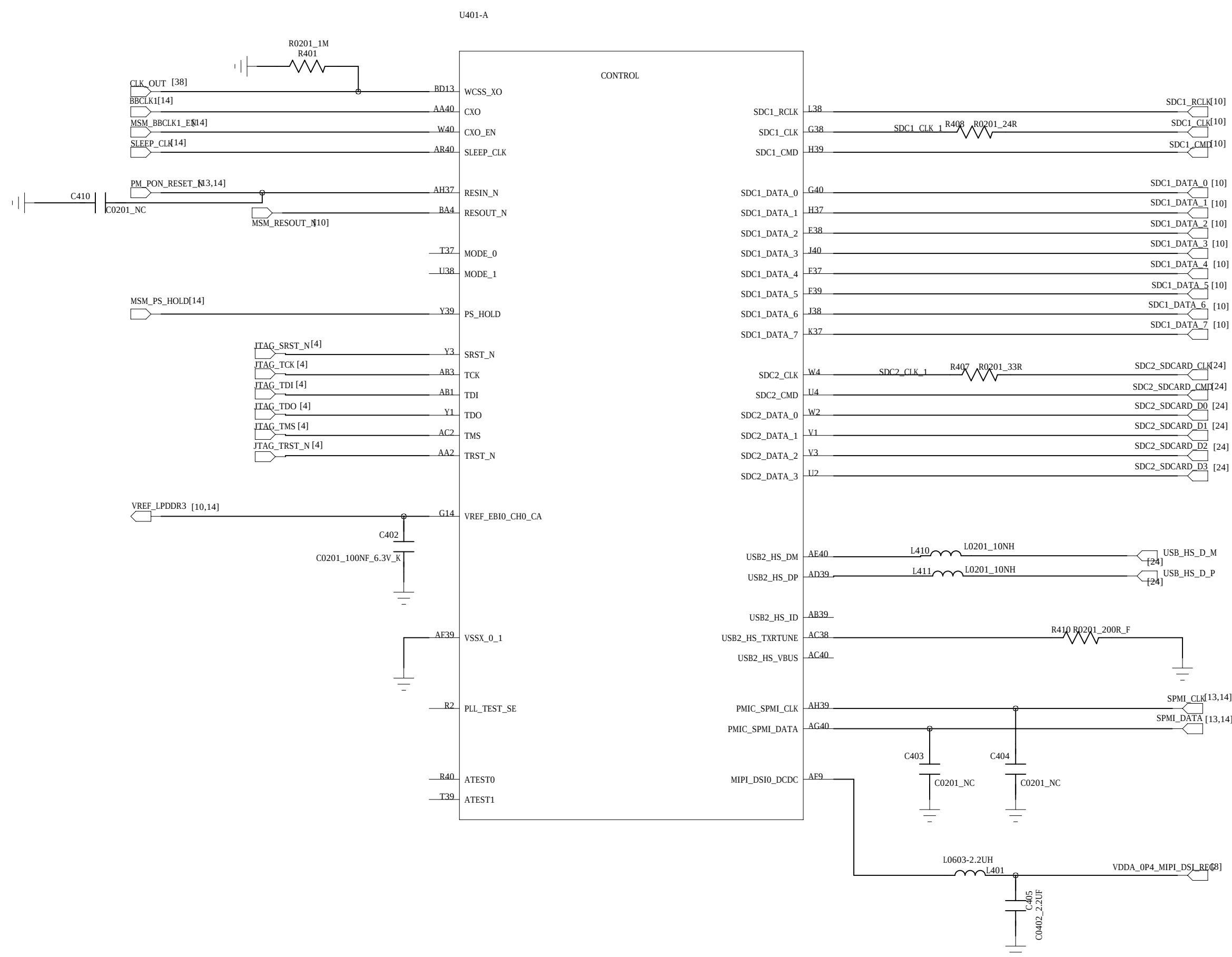
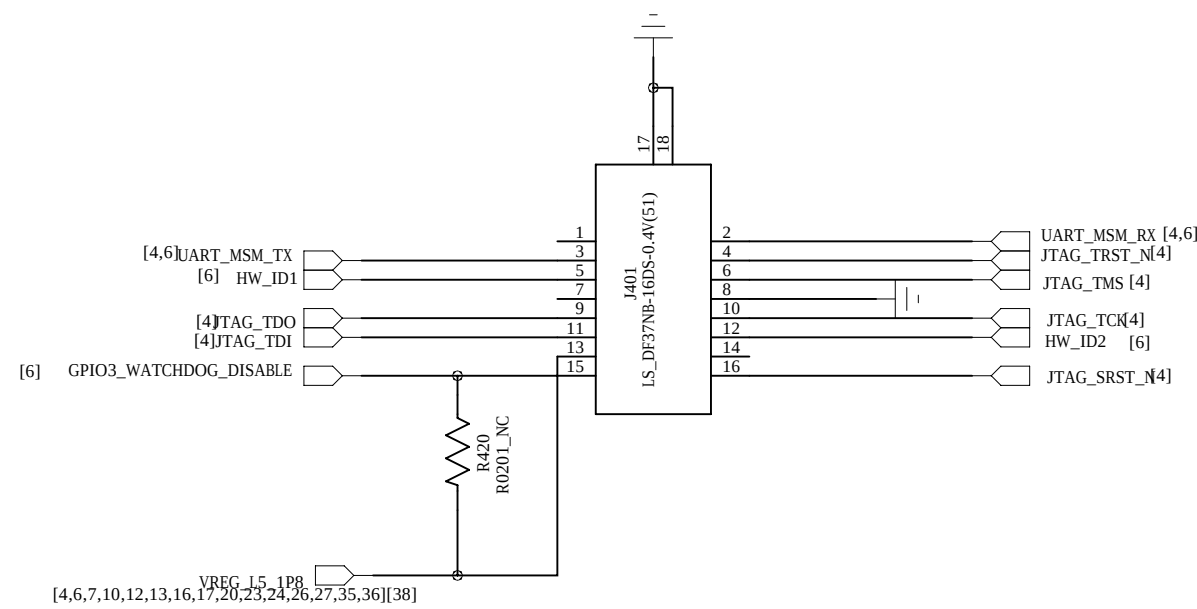
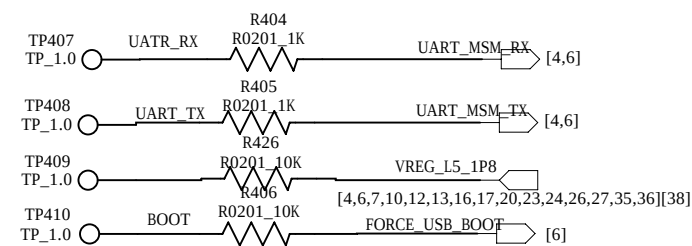


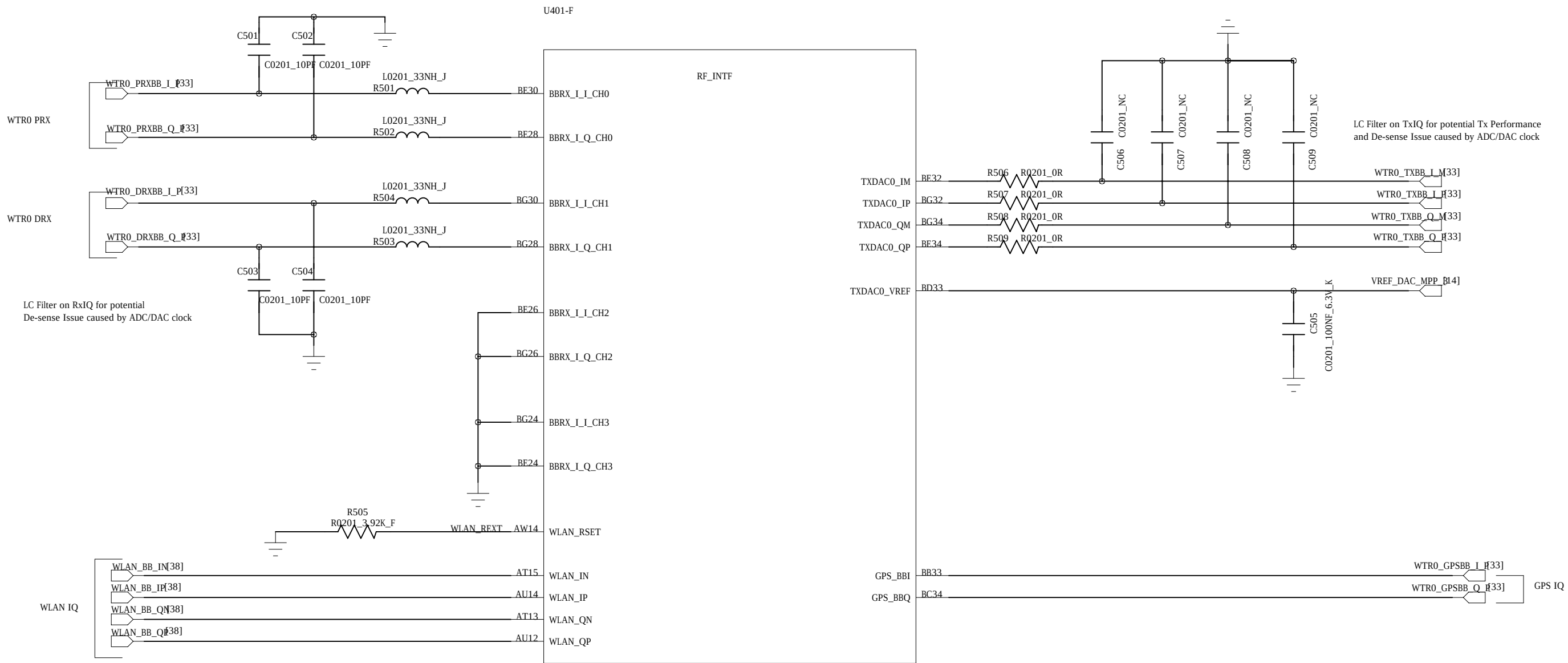
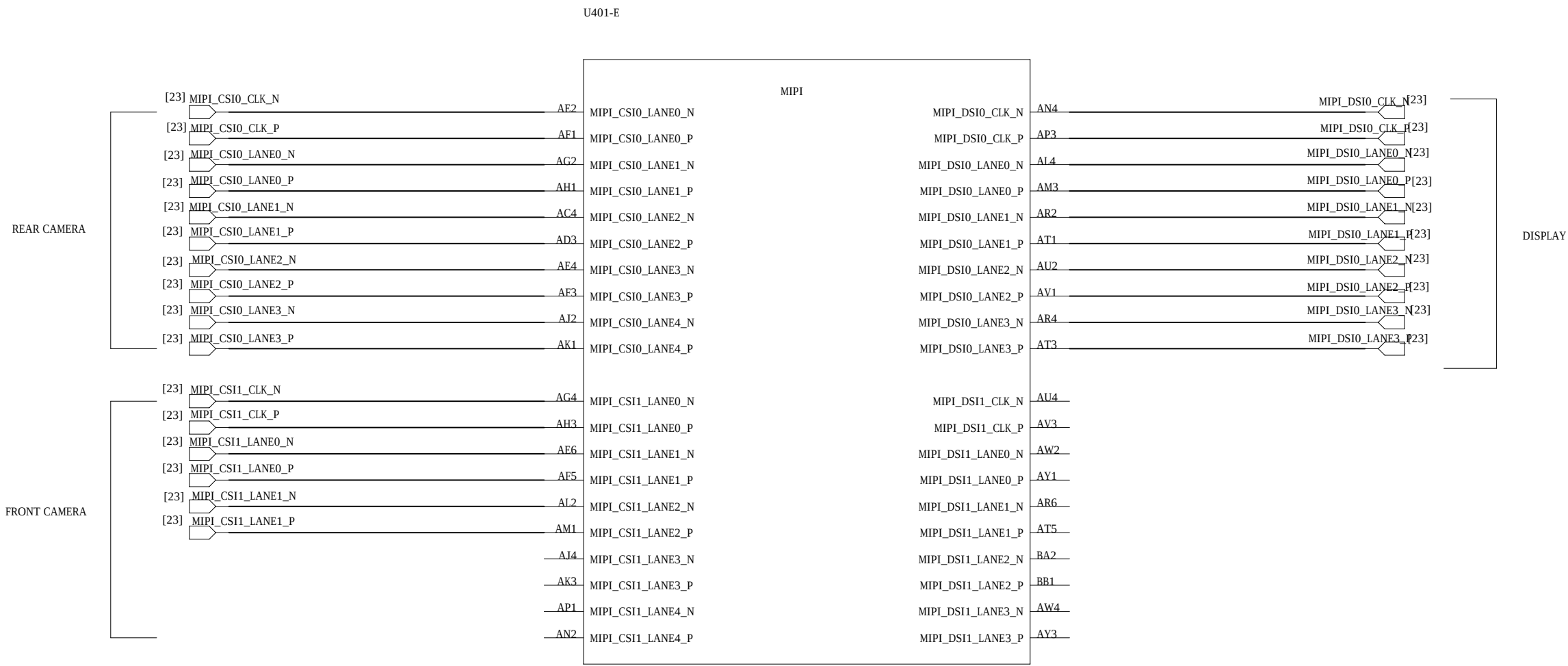
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

MSM8937 Control & EBI



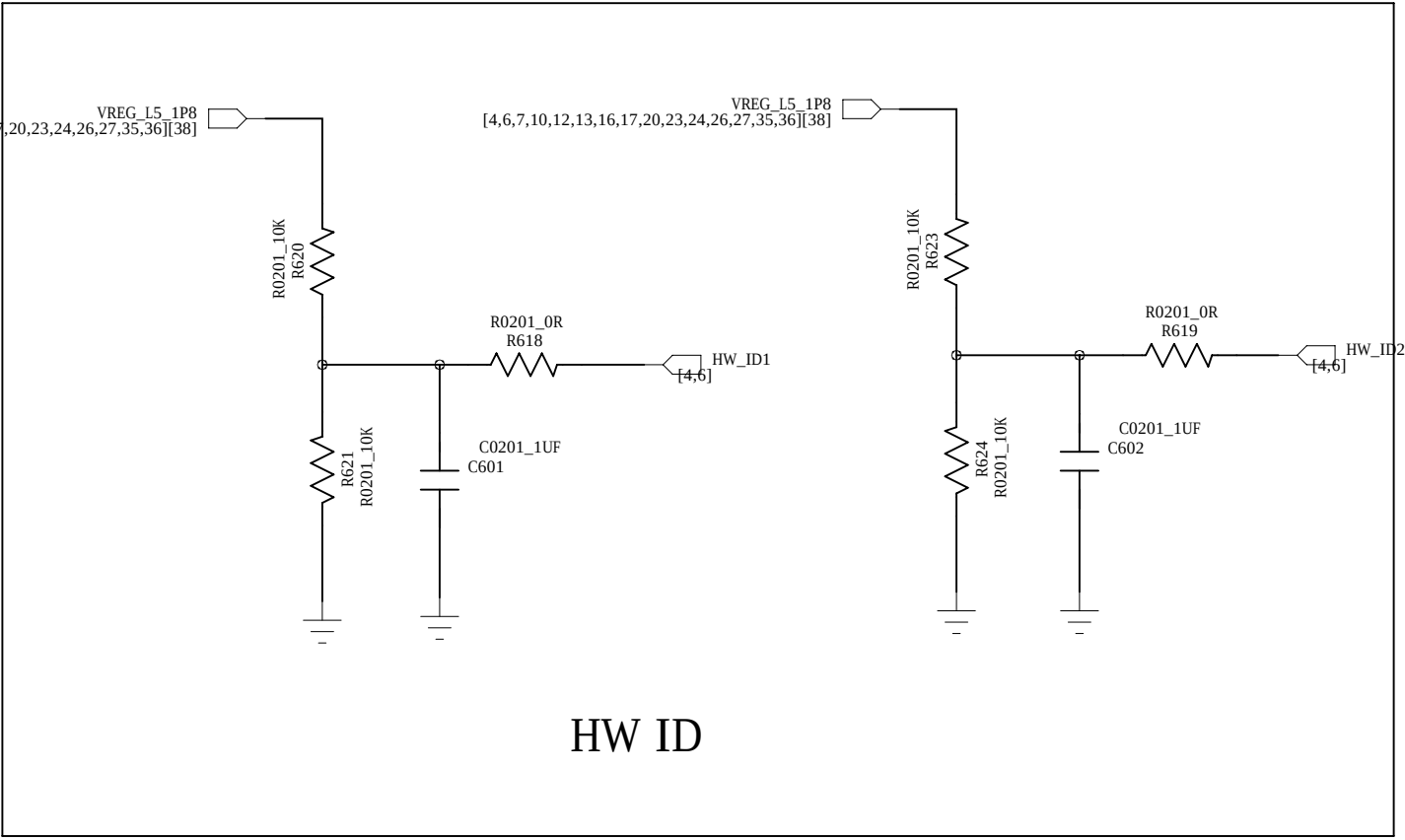
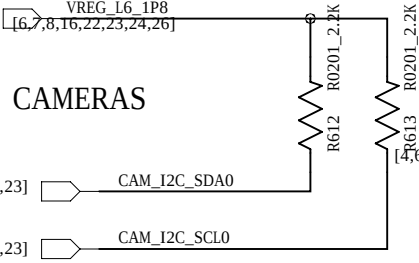
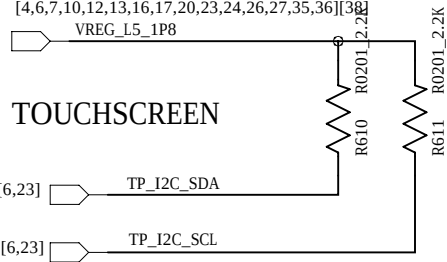
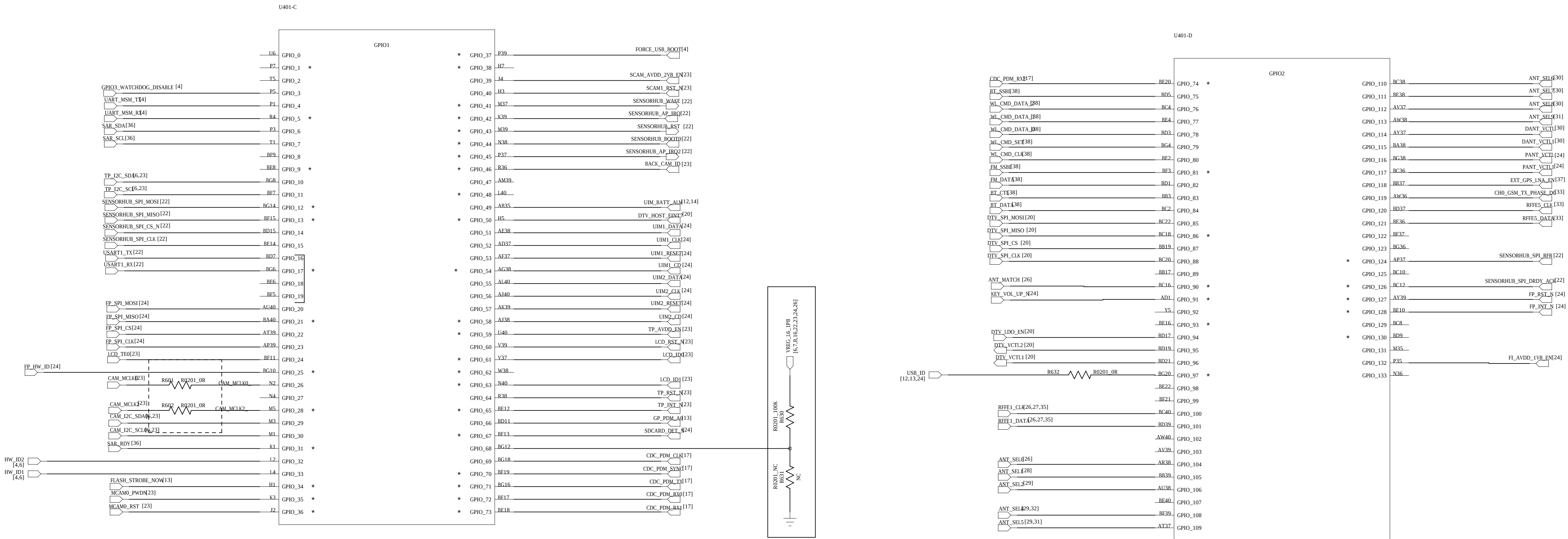
MSM8937 MIPI and RF Control

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



MSM8937 GPIO

Note: Asterisks (*) indicate modem power management (MPM) wake-up pins



REVISION RECORD			
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MSM8937 POWER1

REVISION RECORD			
TR	ECO NO:	APPROVED:	DATE:

D_D

D_D

C_C

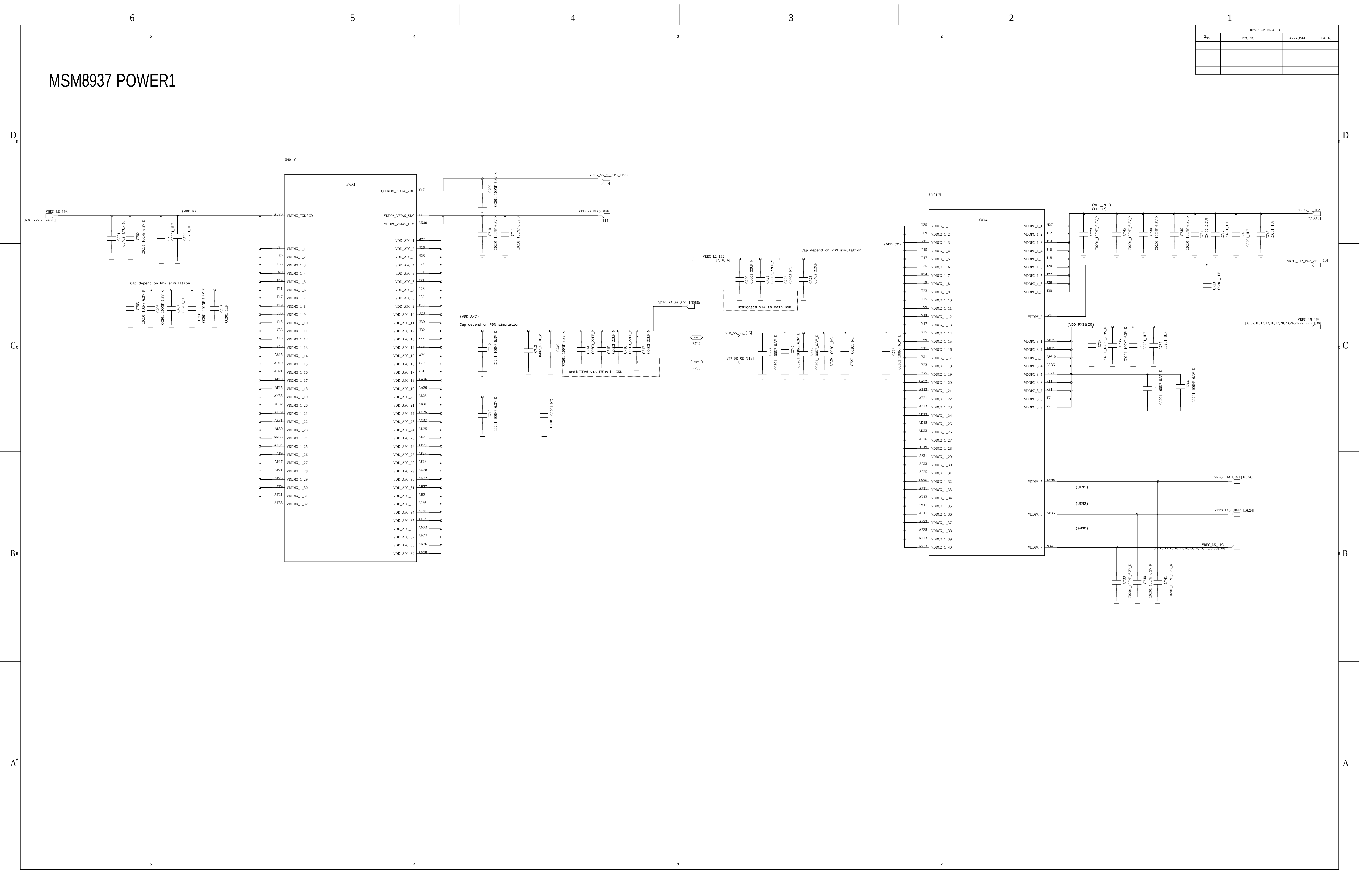
C_C

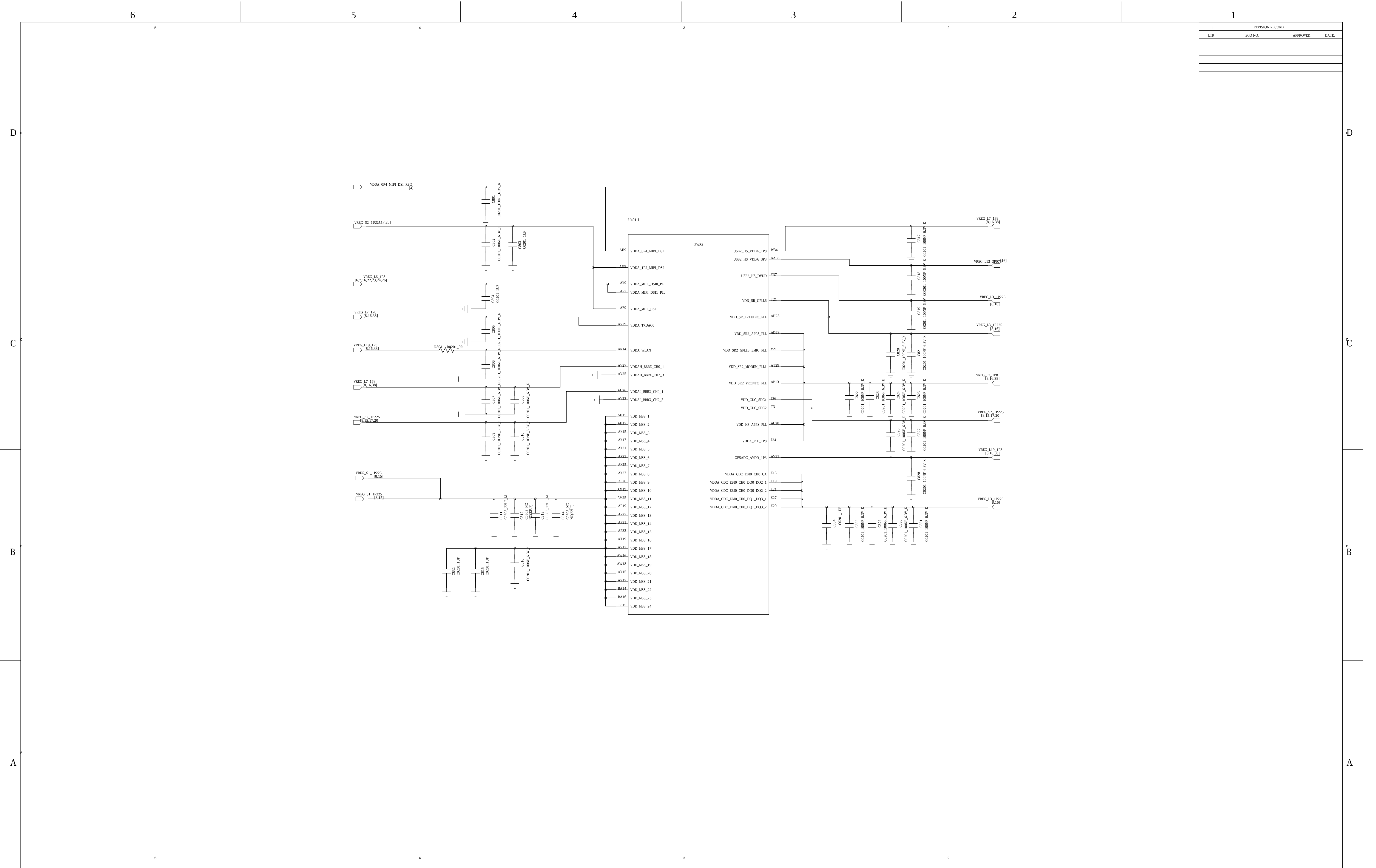
B_B

B_B

A_A

A_A





1REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

6

5

4

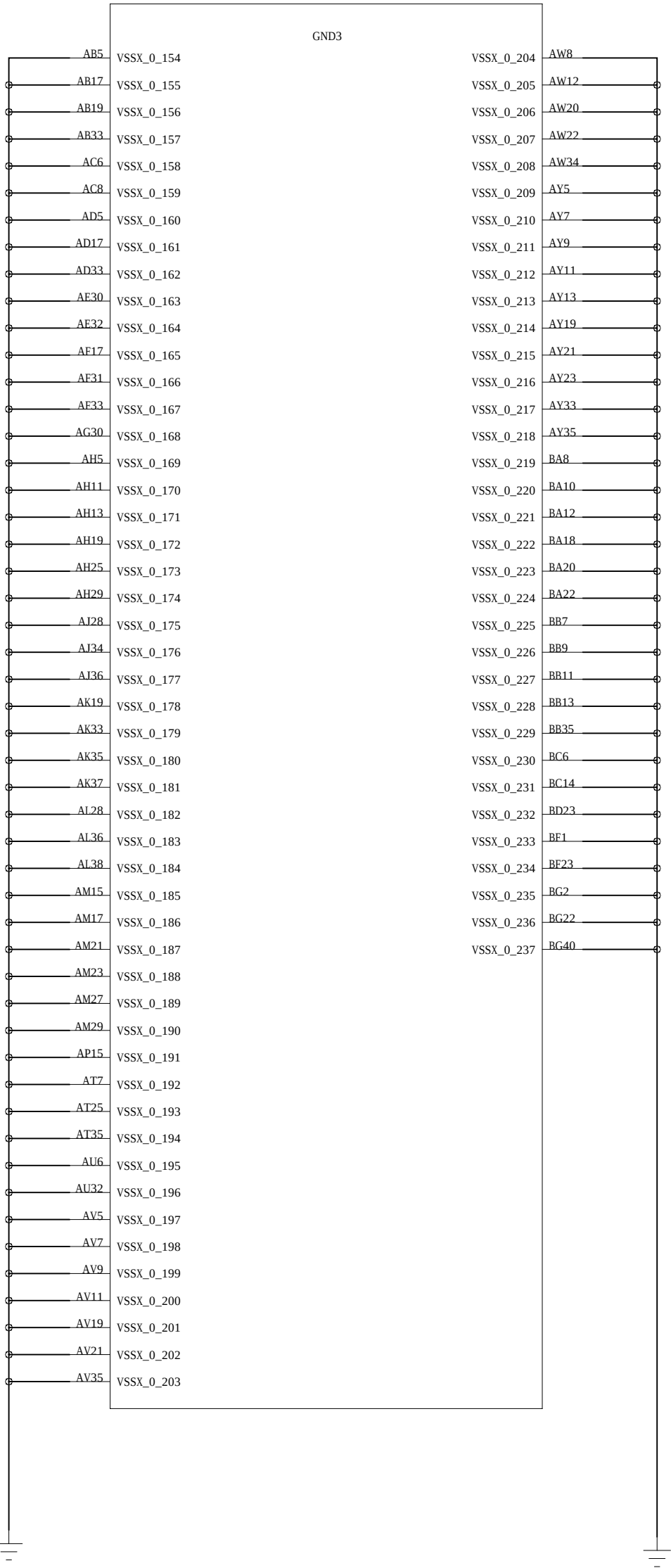
3

2

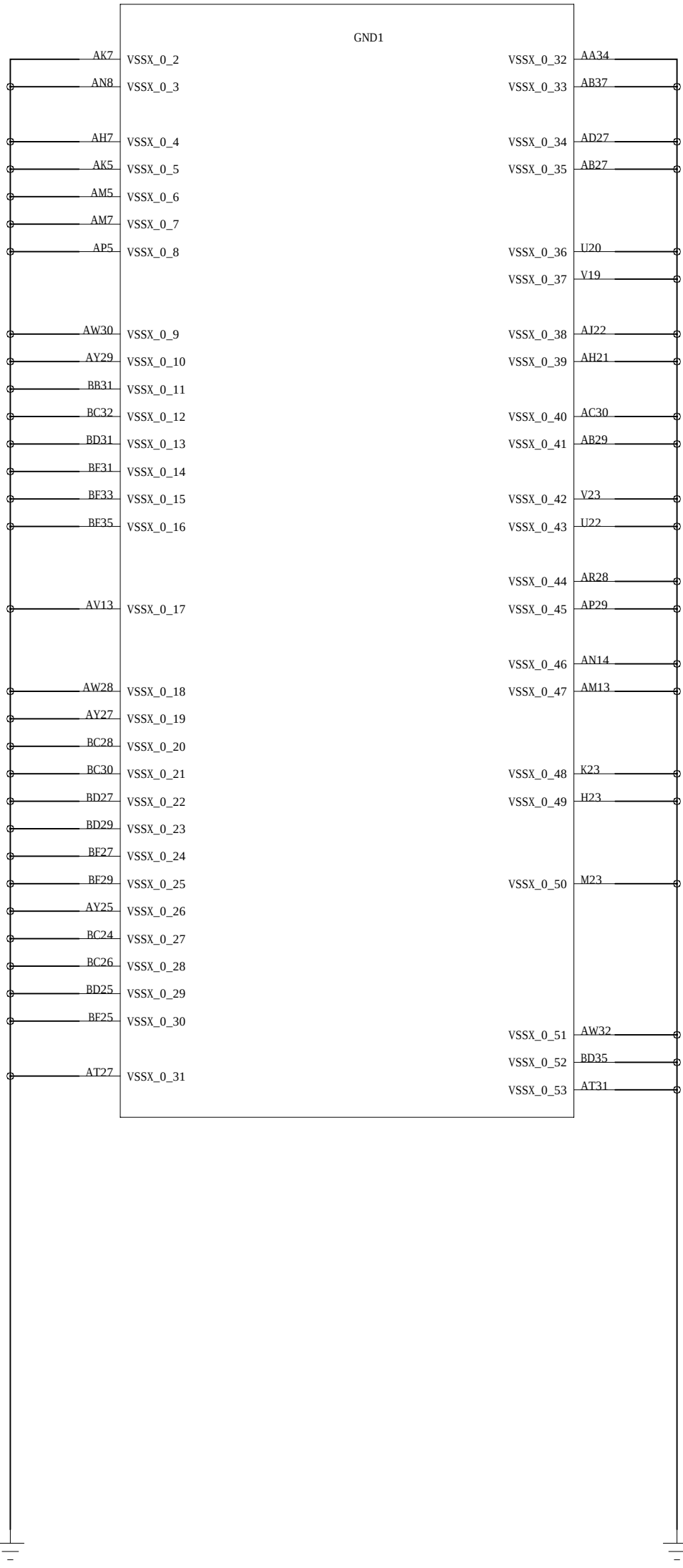
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REVISION RECORD			
4	UTR	ECO NO:	APPROVED:
			DATE:

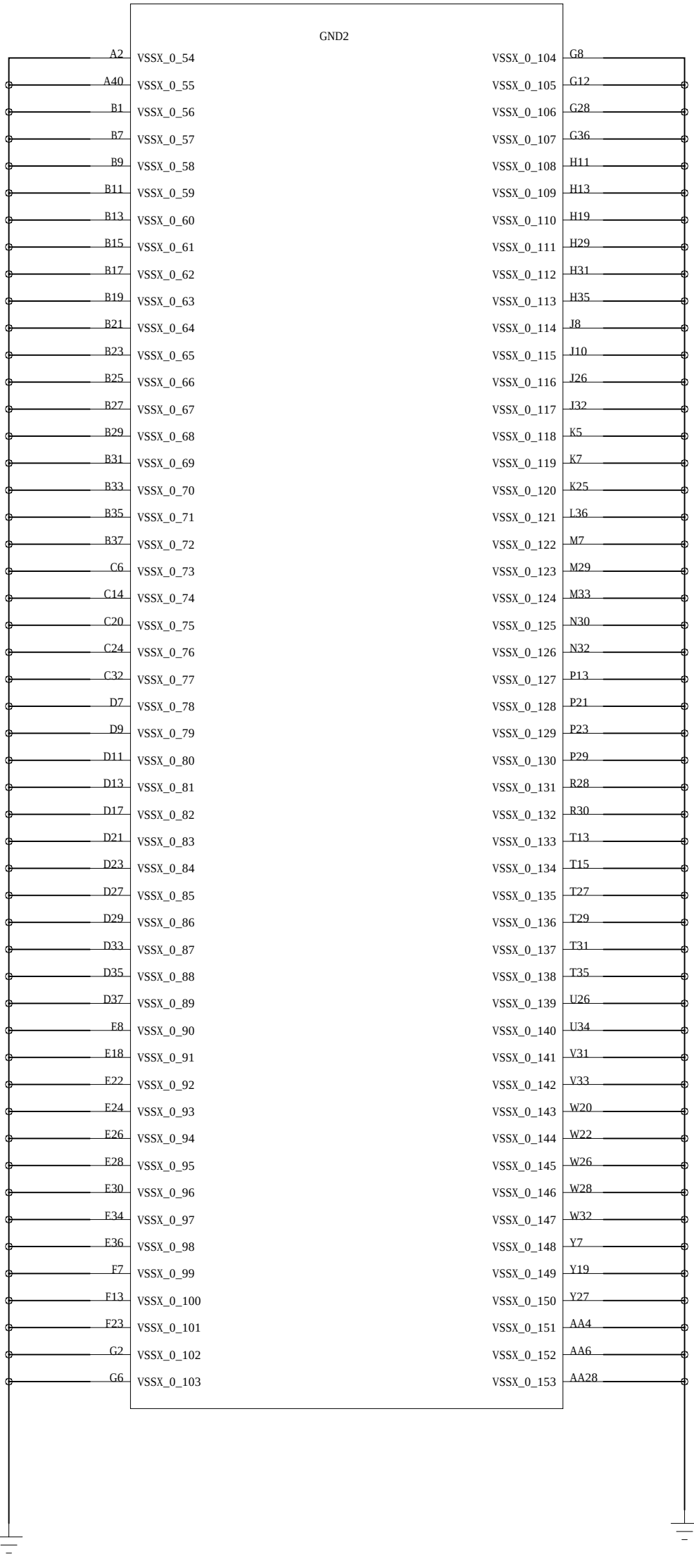
U401-L



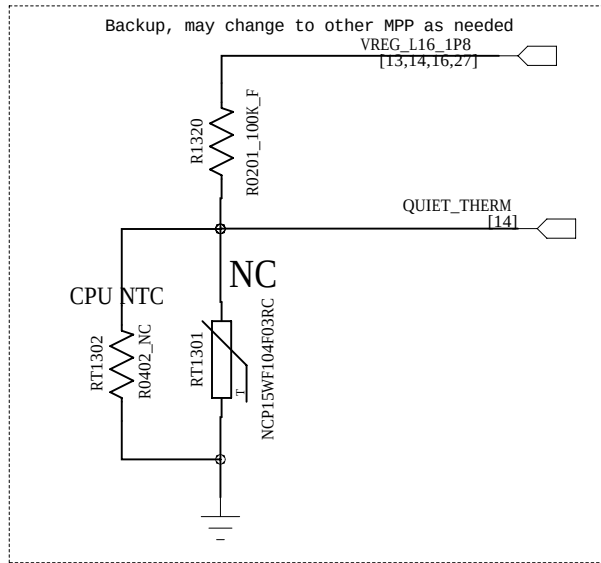
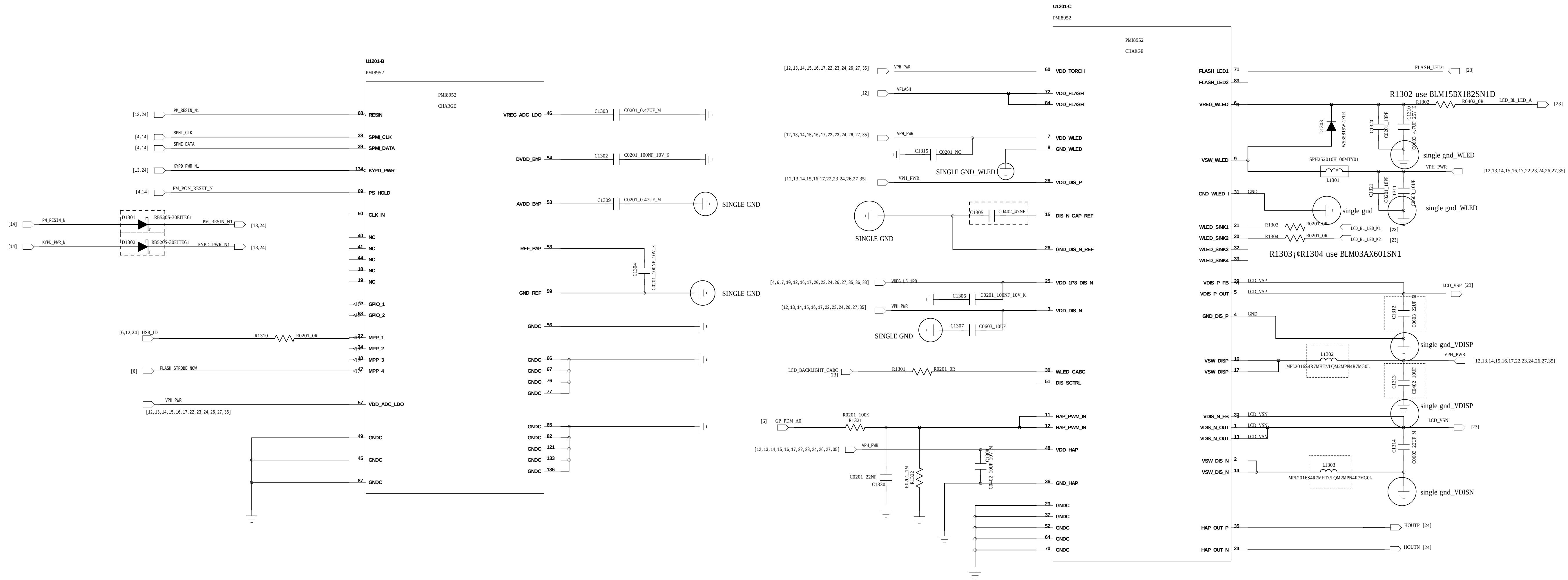
U401-I



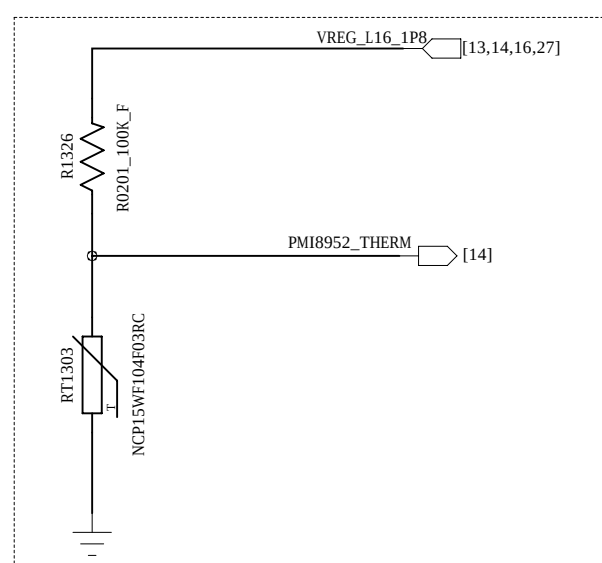
U401-K



13: PMI8952_CONTROL



RT1301 is reserved for main board temperature detect
RT1302 is for CPU surface temperature detect

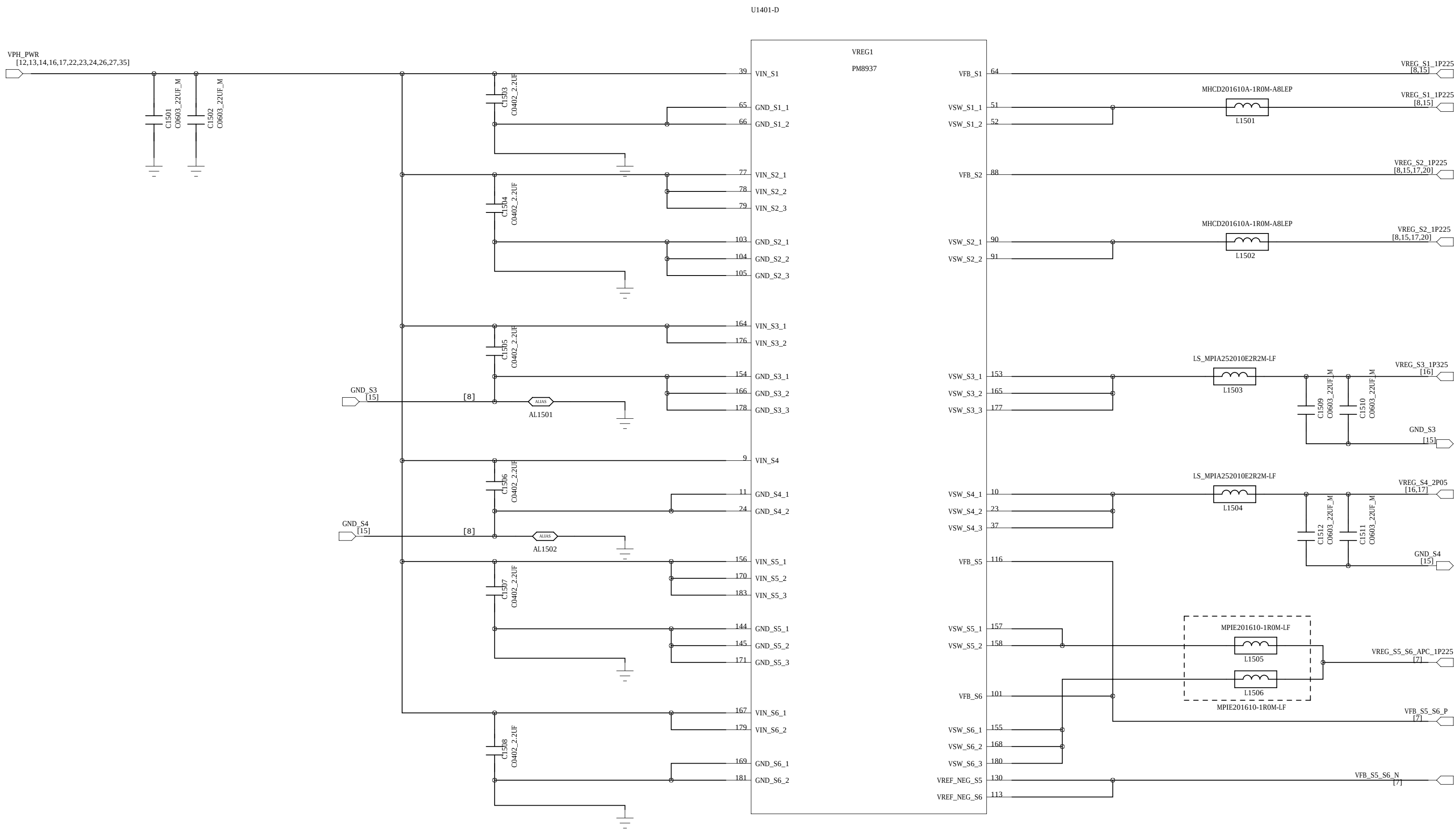


RT1303 is used for PMI surface temperature detect

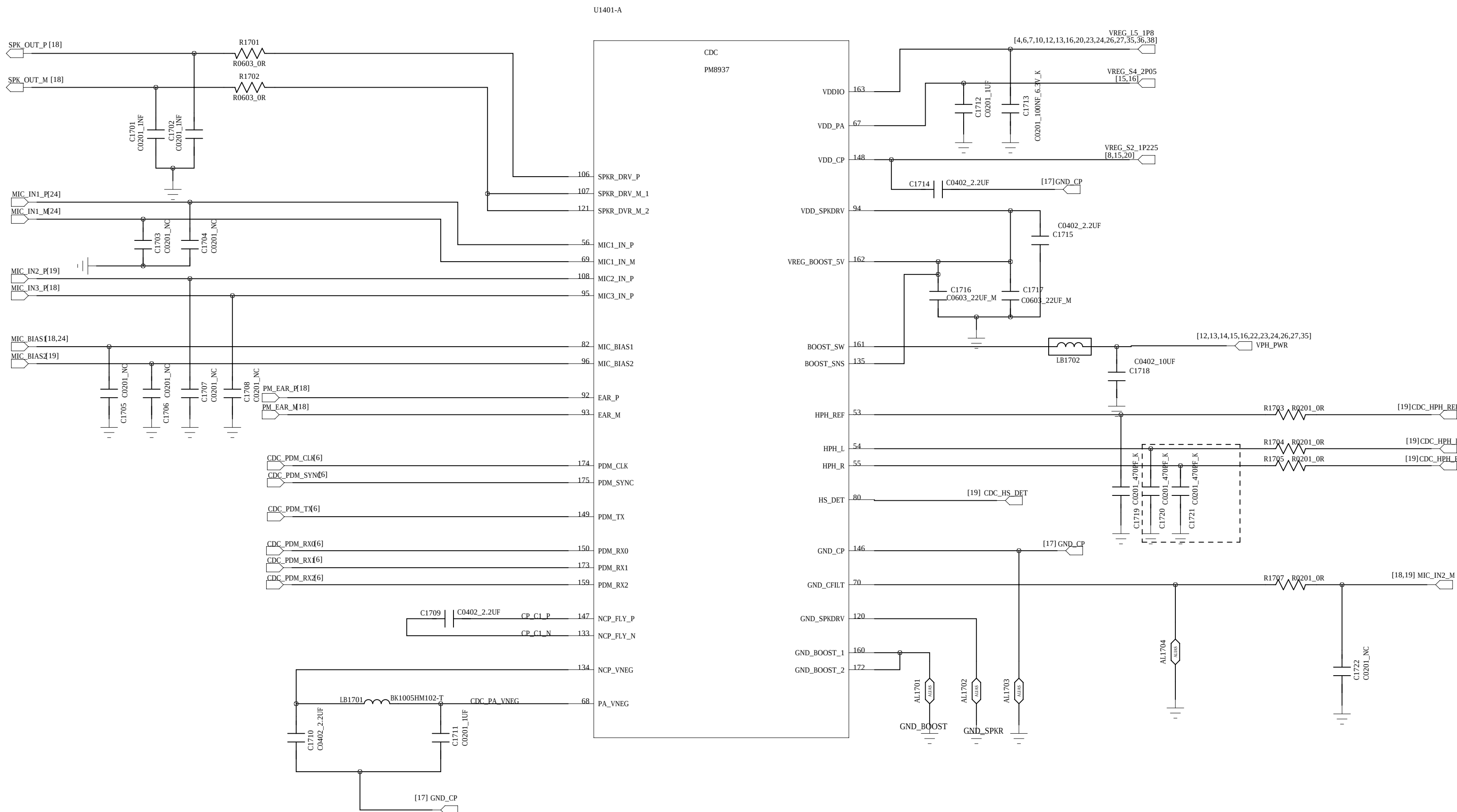
PMI8952 INTERFACE

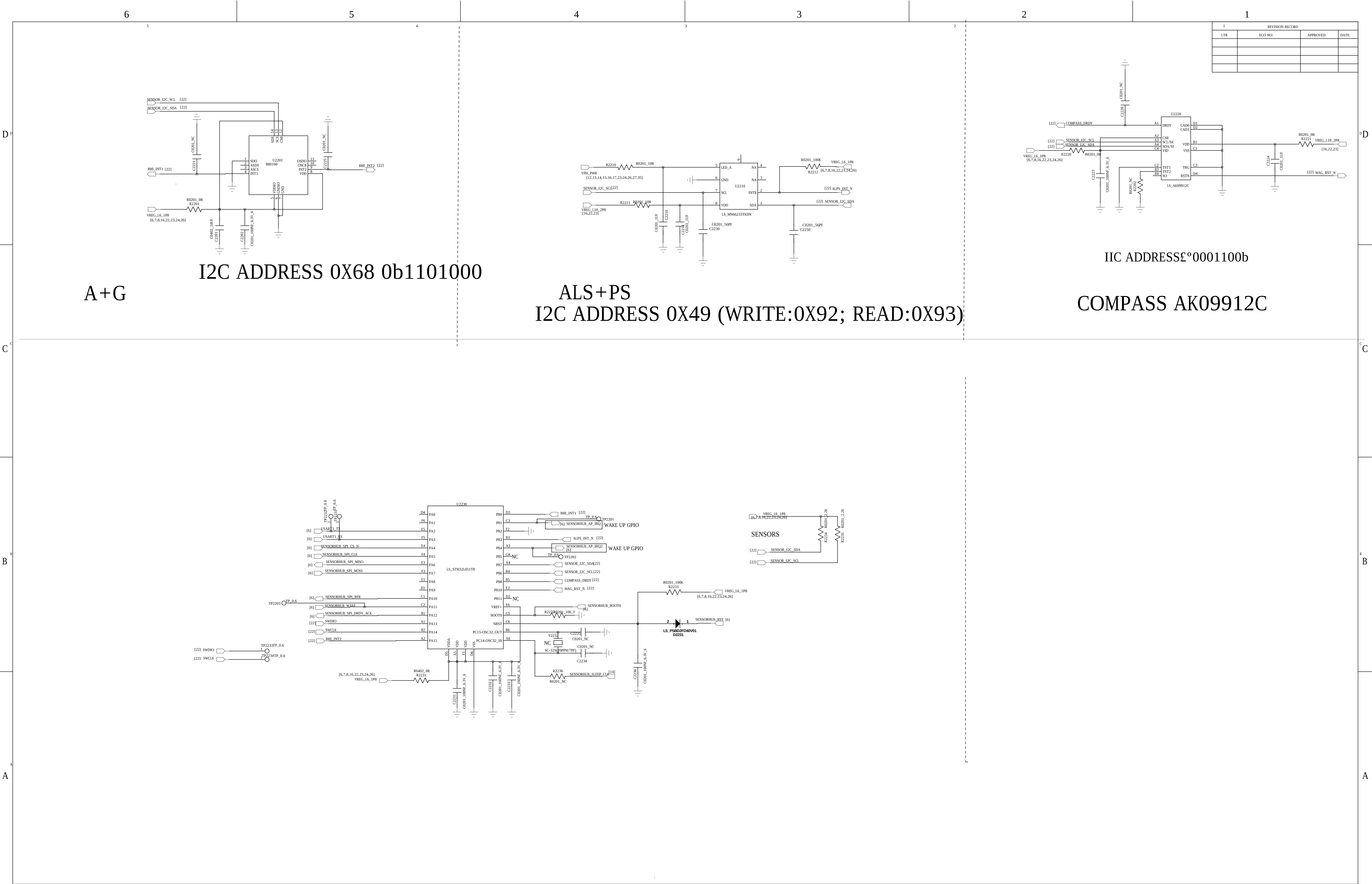
PM8937 Buck converter

1			
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

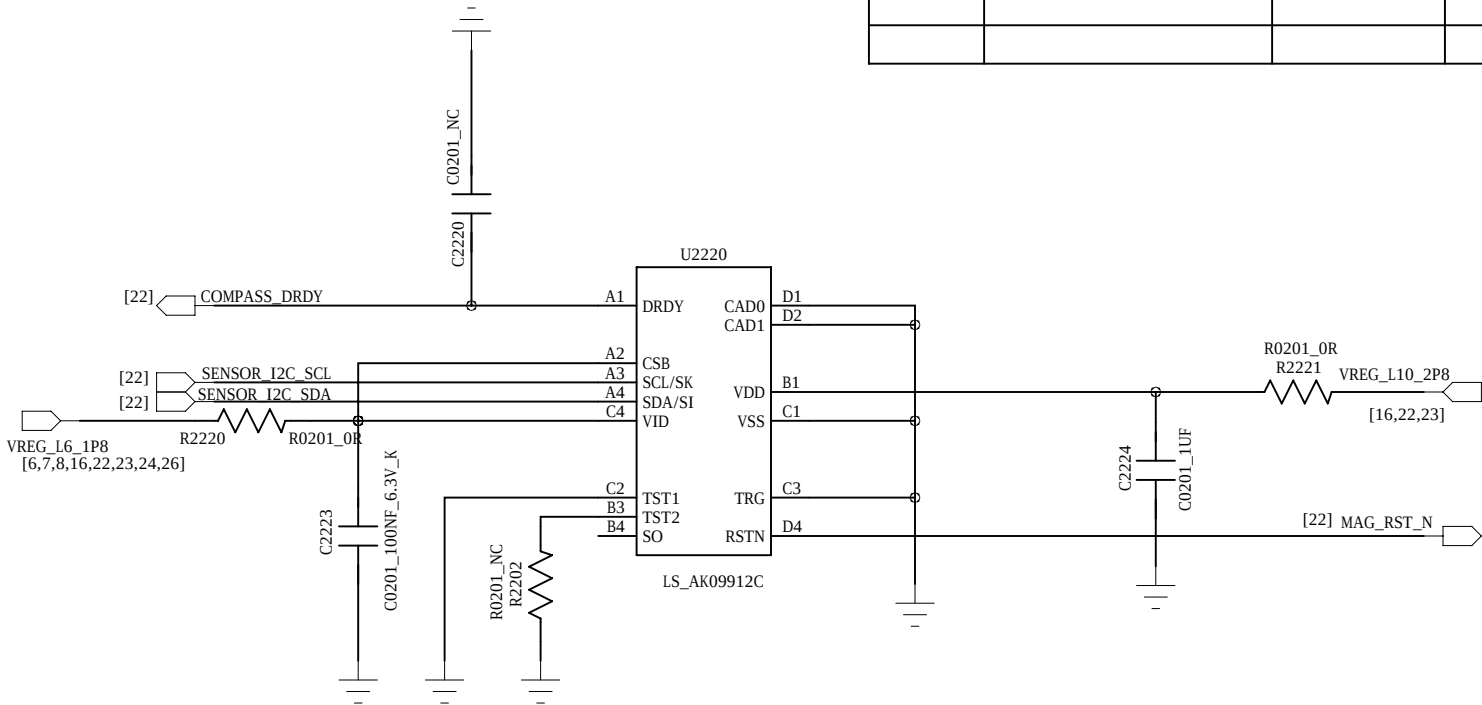


A



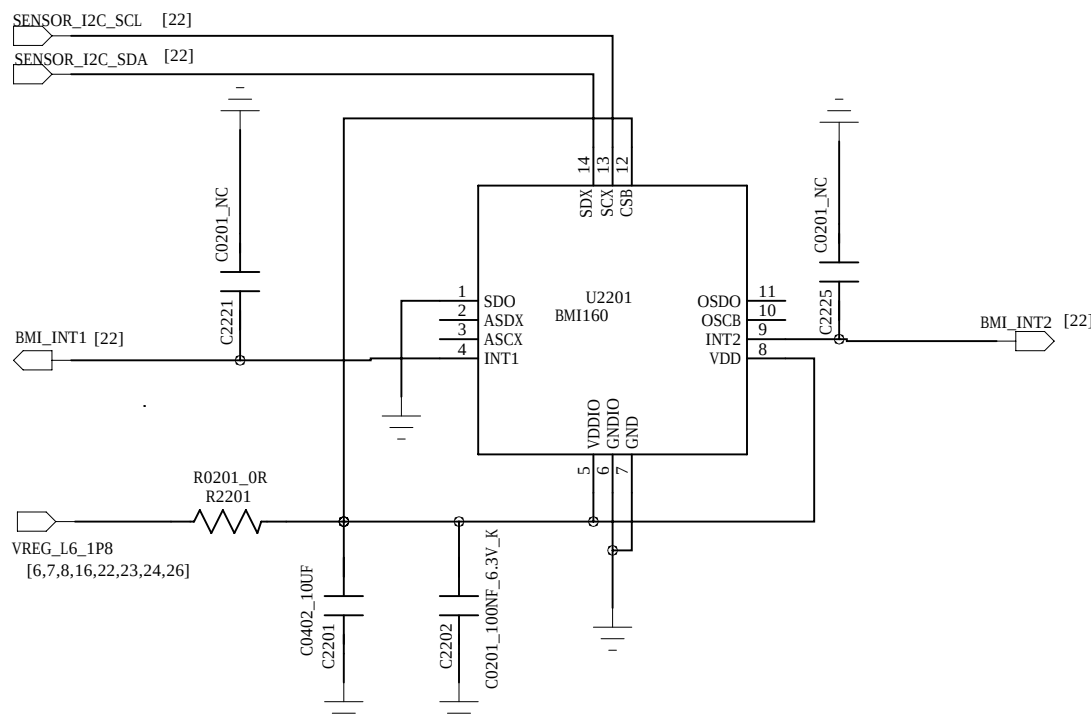


1REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



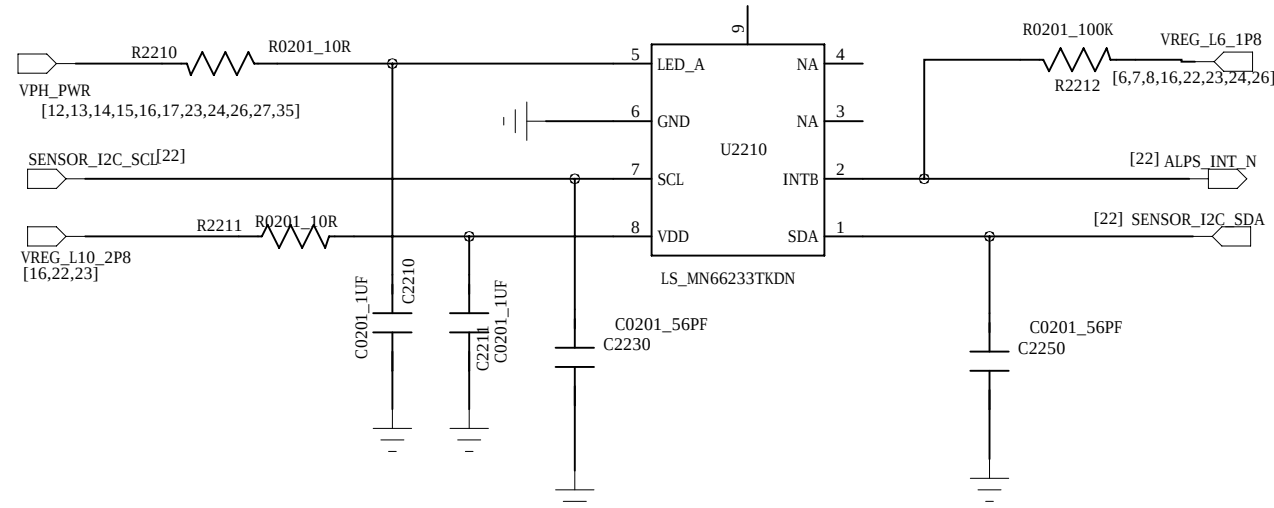
IIC ADDRESS£°0001100b

COMPASS AK09912C

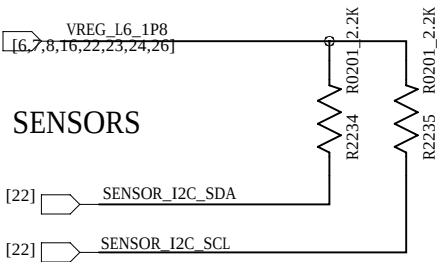
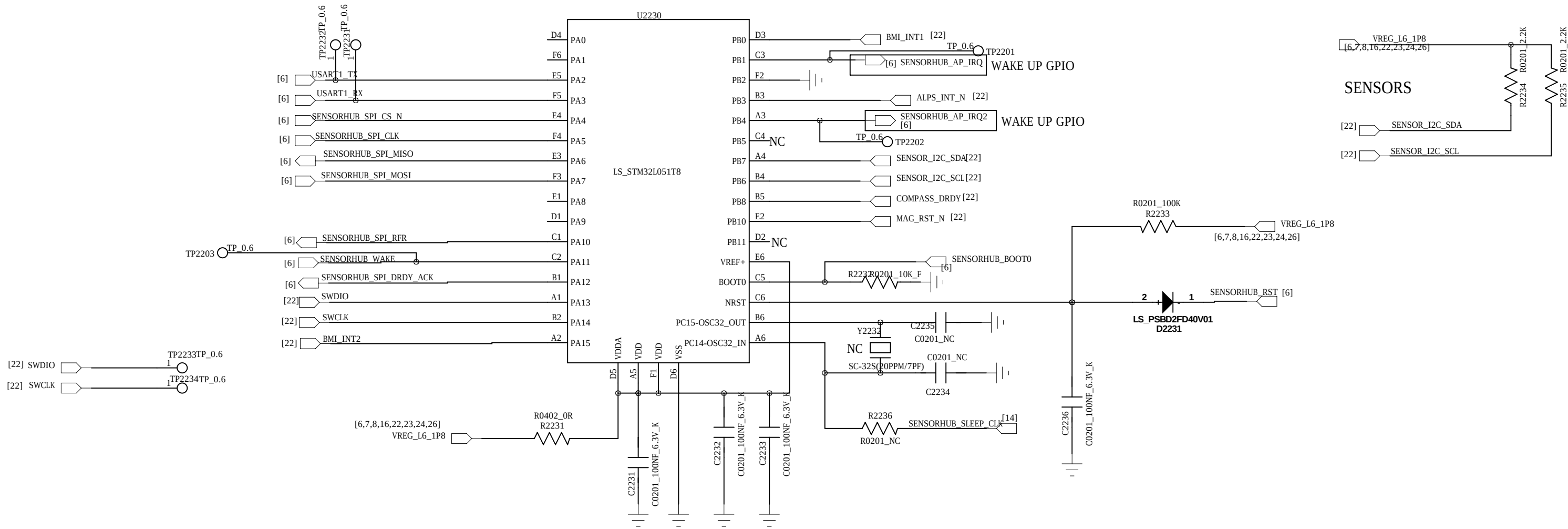


I2C ADDRESS 0X68 0b1101000

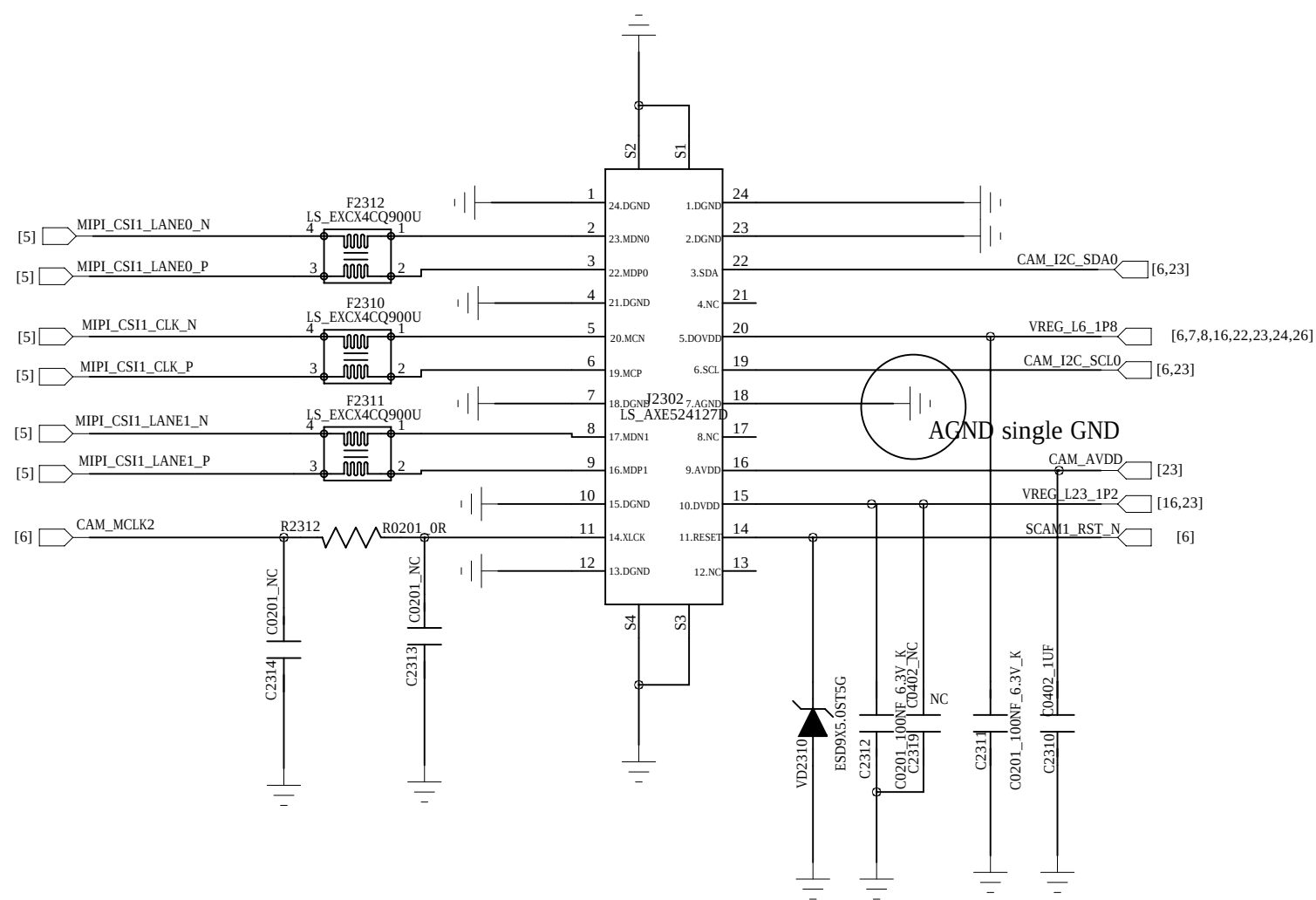
A+G



ALS+PS
I2C ADDRESS 0X49 (WRITE:0X92; READ:0X93)



Front Camera 5MP



LCD interface and backlight

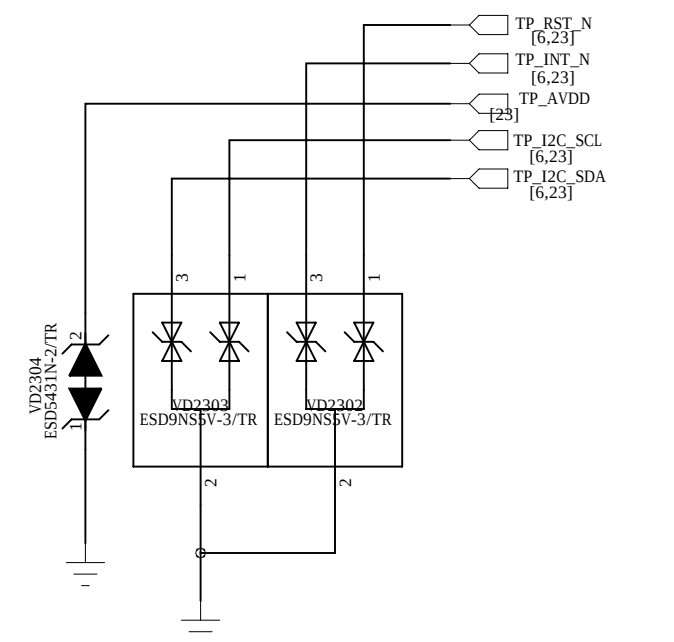
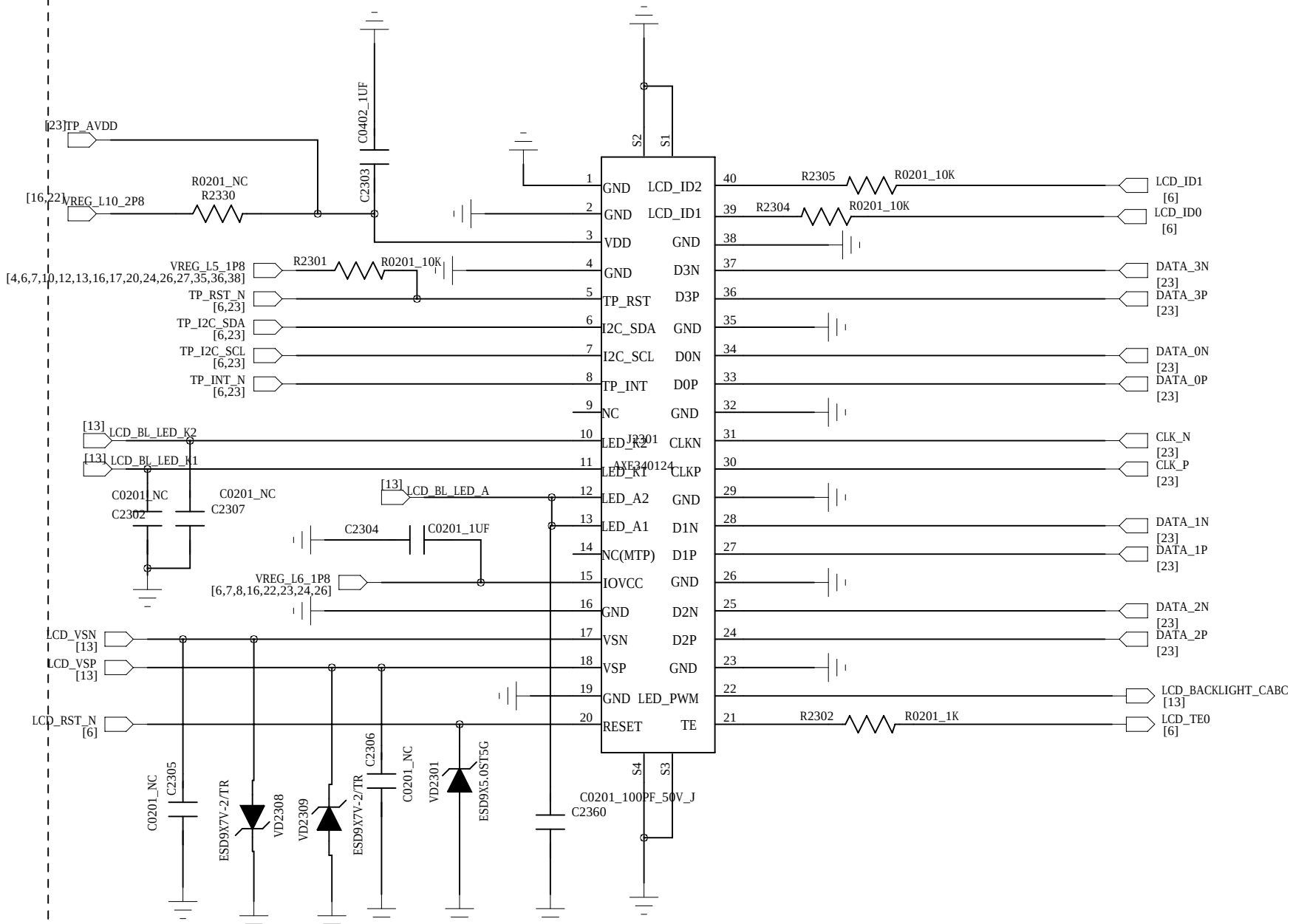
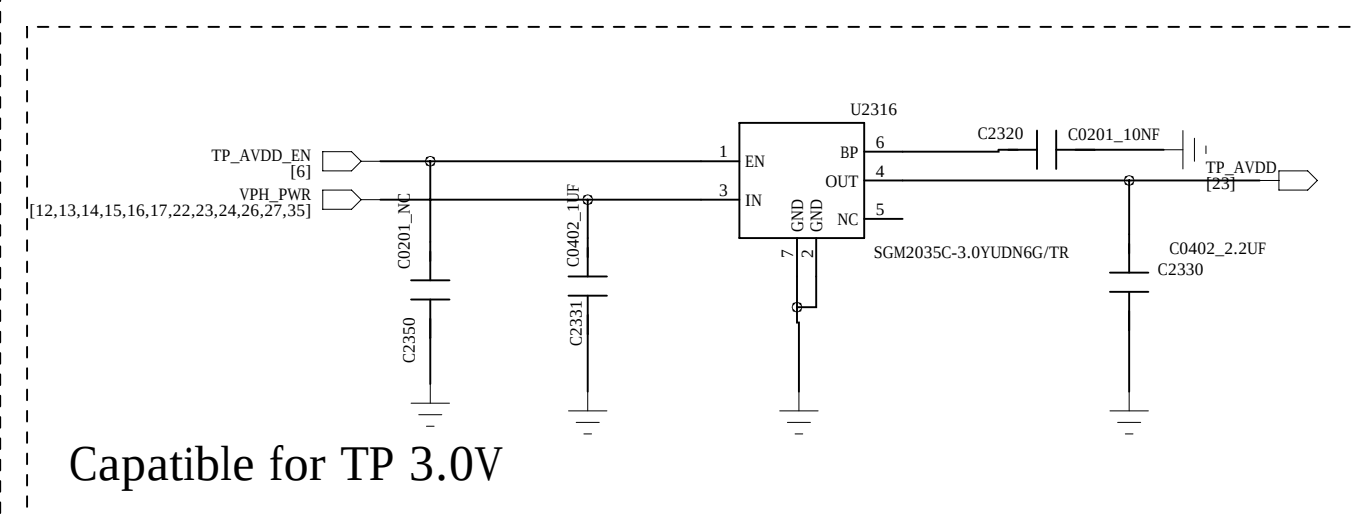
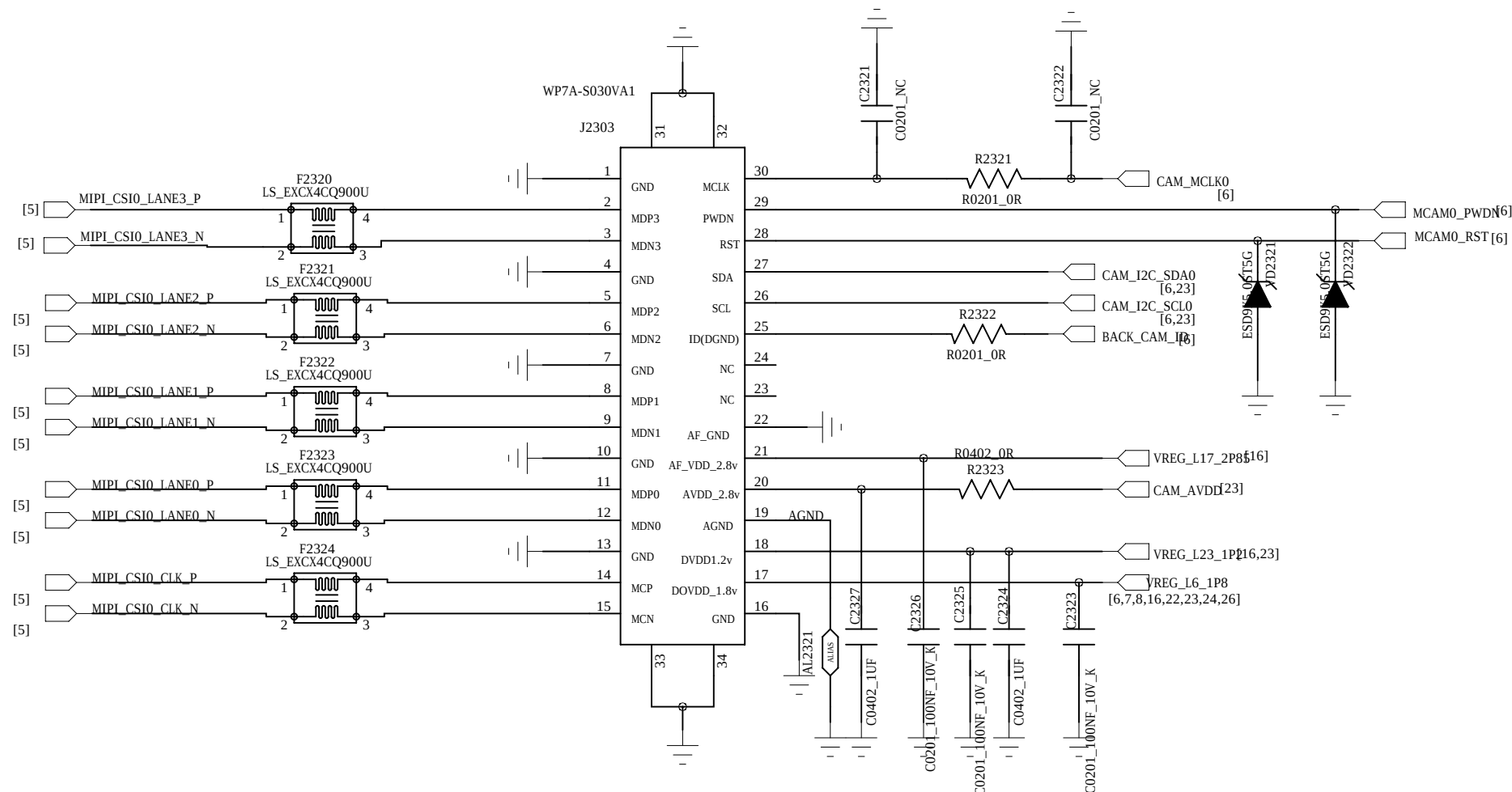
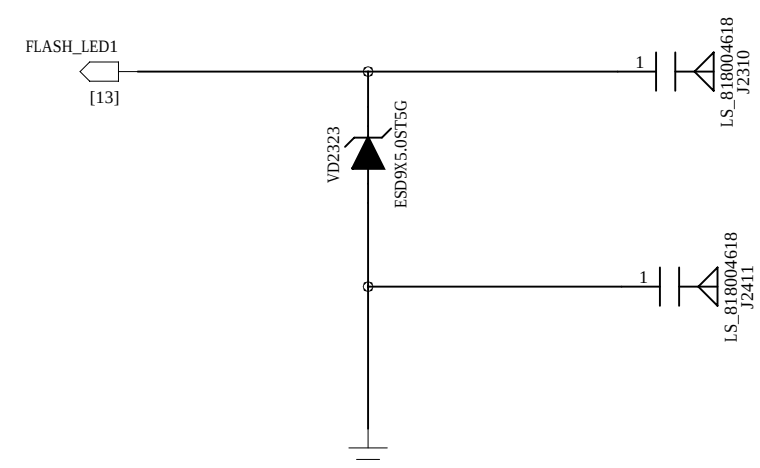


Figure 10 shows the block diagram of the F2300, F2302, F2303, F2304, and F2305. Each block is a 4-bit bus interface with inputs DATA_IN, DATA_OUT, DATA_2P, and DATA_2N, and outputs DATA_1P and DATA_1N. The blocks are labeled F2300, F2302, F2303, F2304, and F2305, with the internal logic being IS_F2C3ACQ2000U. The outputs are labeled MIP1_DSIO_LANE2_P, MIP1_DSIO_LANE2_N, MIP1_DSIO_LANE1_P, MIP1_DSIO_LANE1_N, MIP1_DSIO_CLK_P, MIP1_DSIO_CLK_N, MIP1_DSIO_LANE0_P, MIP1_DSIO_LANE0_N, MIP1_DSIO_LANE3_P, and MIP1_DSIO_LANE3_N. The outputs are also labeled with (5) and [5].

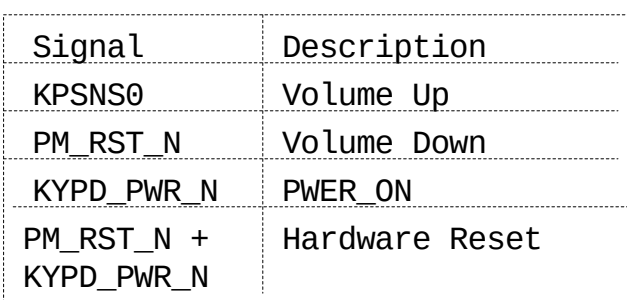
Main Camera 13MP



Capatible for TP 3.0V



1

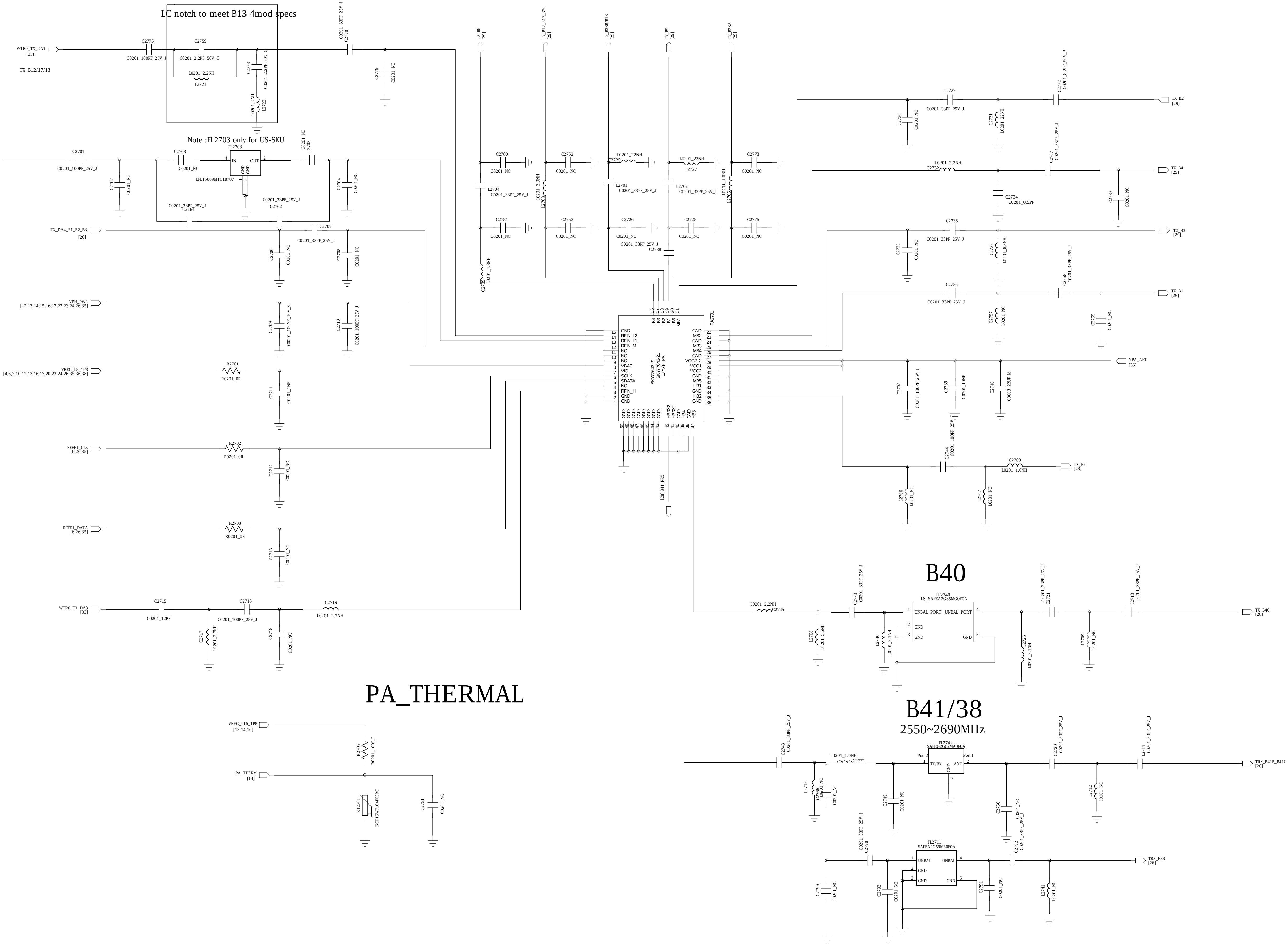


D



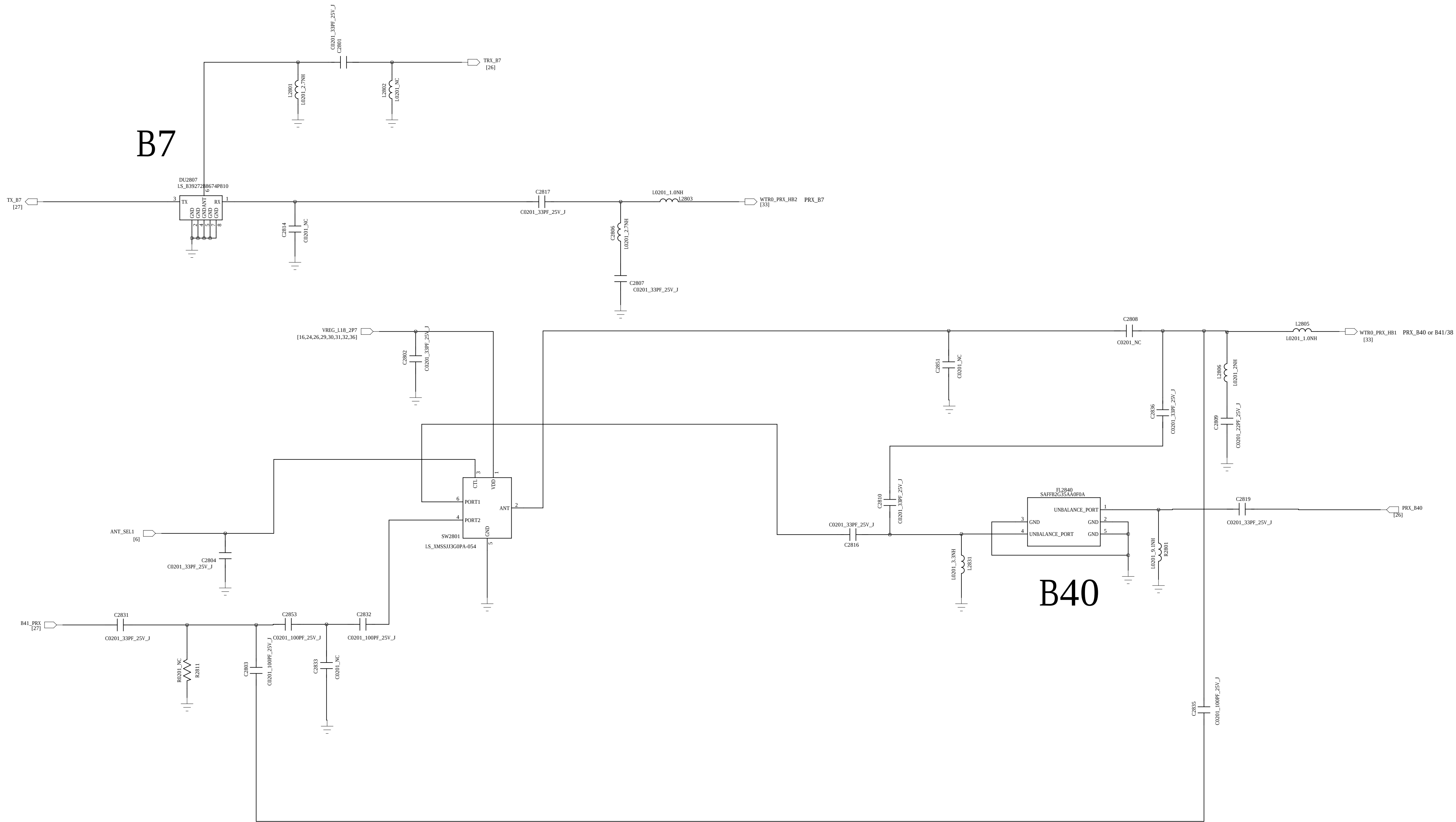
27: SKY77643 & PA THERMAL

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



28: TRX_HB

REVISION RECORD			
UTR	ECO NO:	APPROVED:	DATE:



29: TRX_LB_MB

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

D

D

C

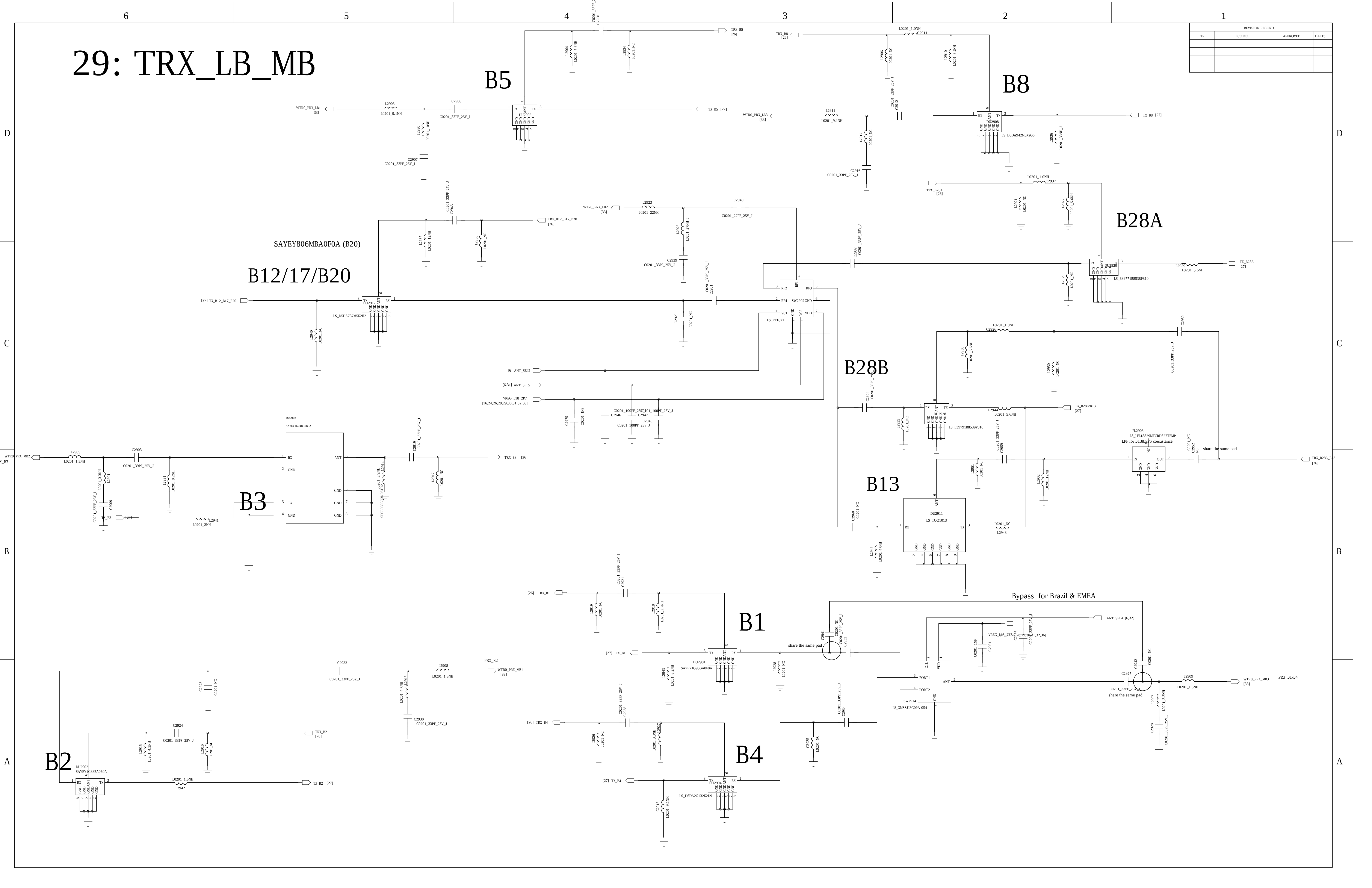
C

B

B

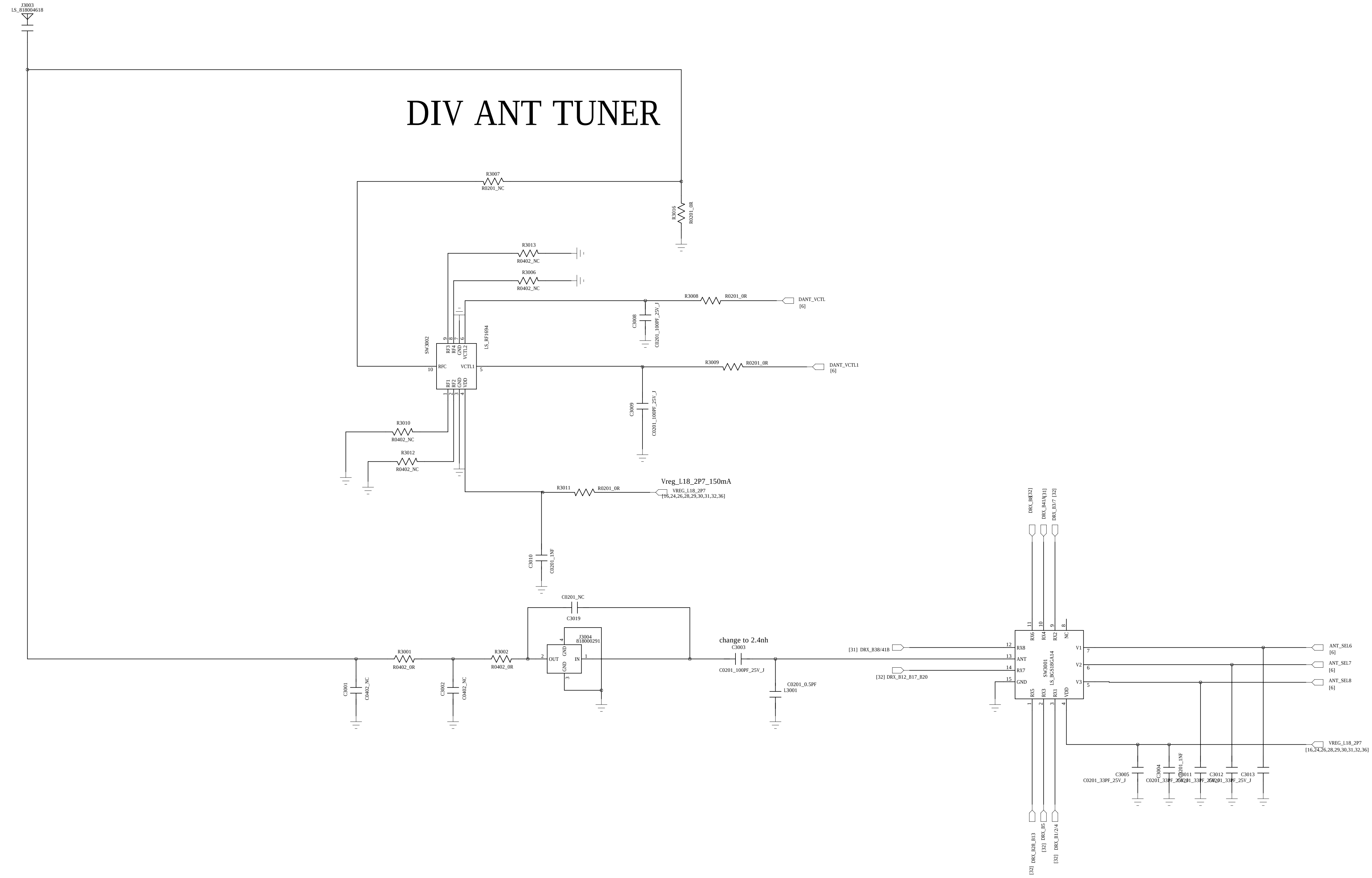
A

A



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

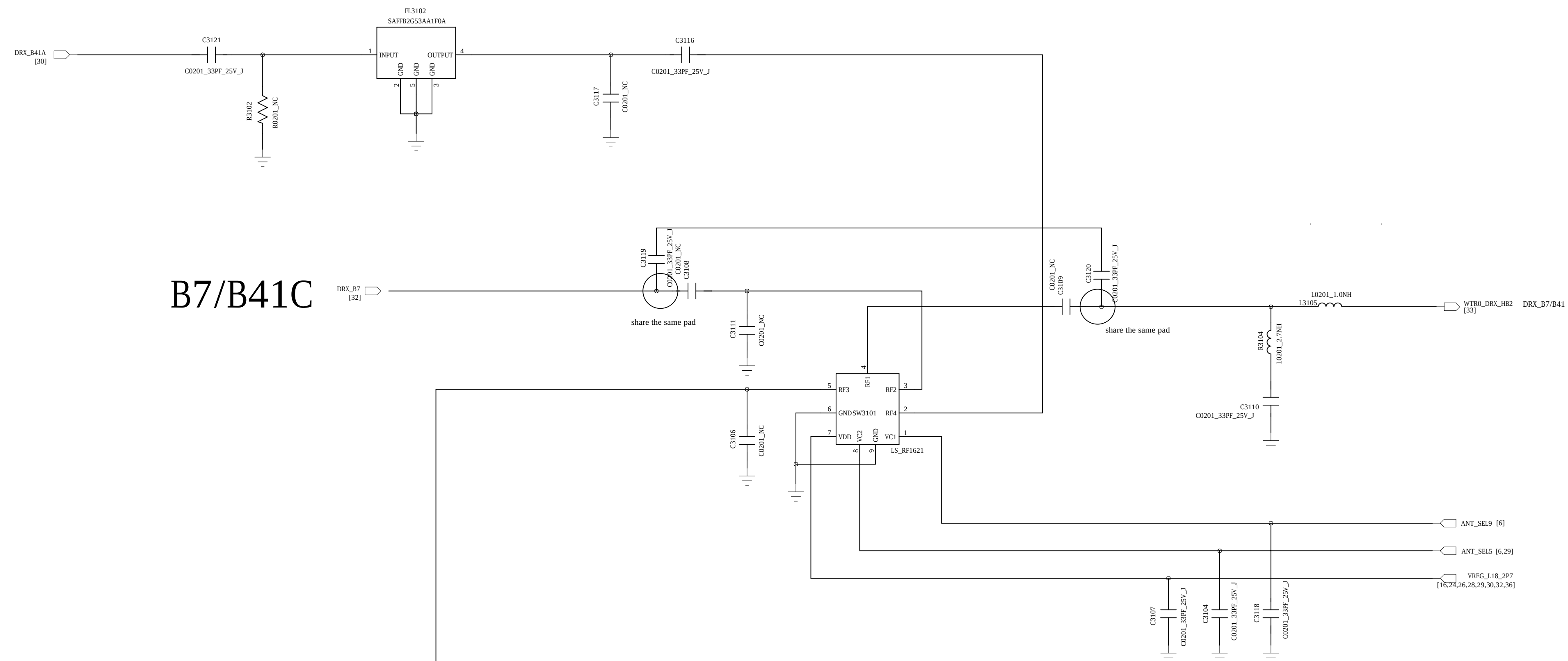
30: DIVERSITY_ANT



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

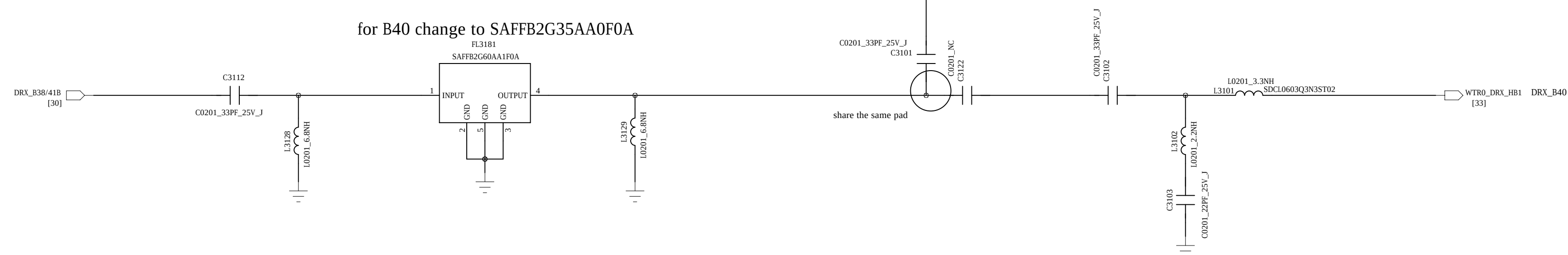
31: DRX_HB

DRX_B41A(2496~2570) or B41 China



DRX_B38/41B(2550~2655)/B40(China&EMEA)

for B40 change to SAFFB2G35AA0F0A

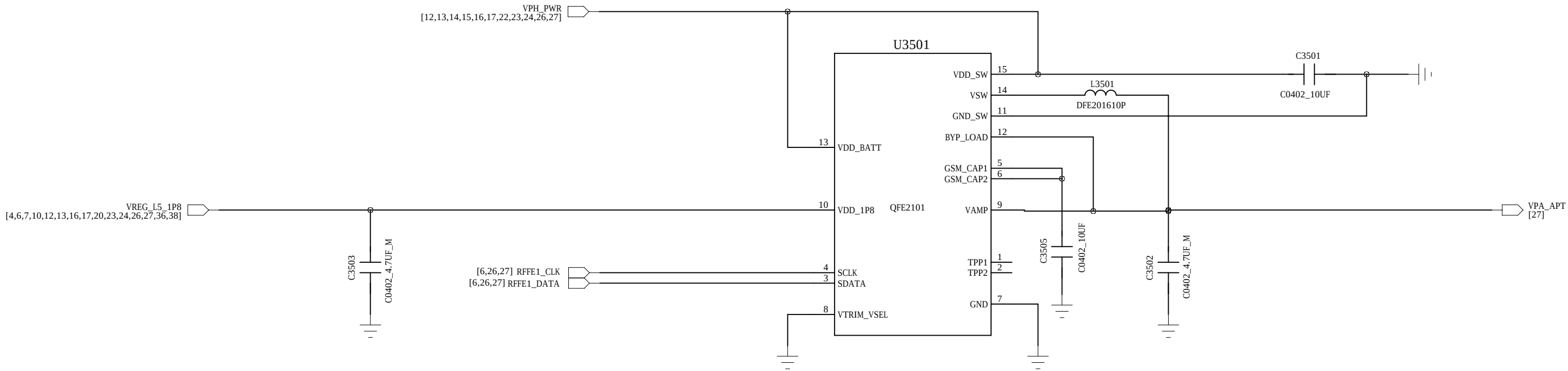


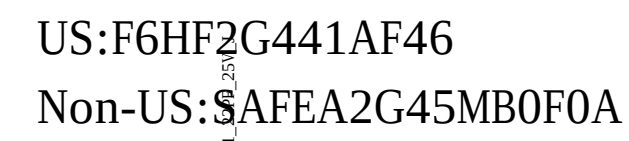
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



REVISION RECORD			
LTR	ECD NO:	APPROVED:	DATE:

35: QFE2101

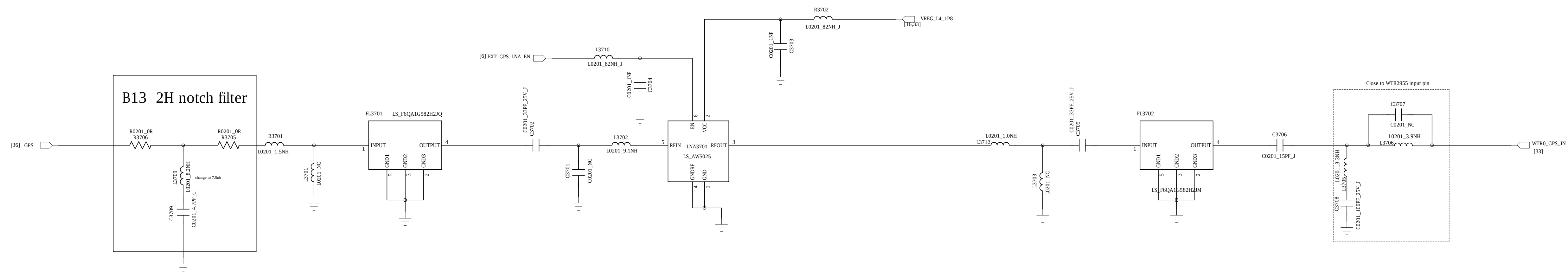


36£°WIFI_2.4G_5G

wifi 5G

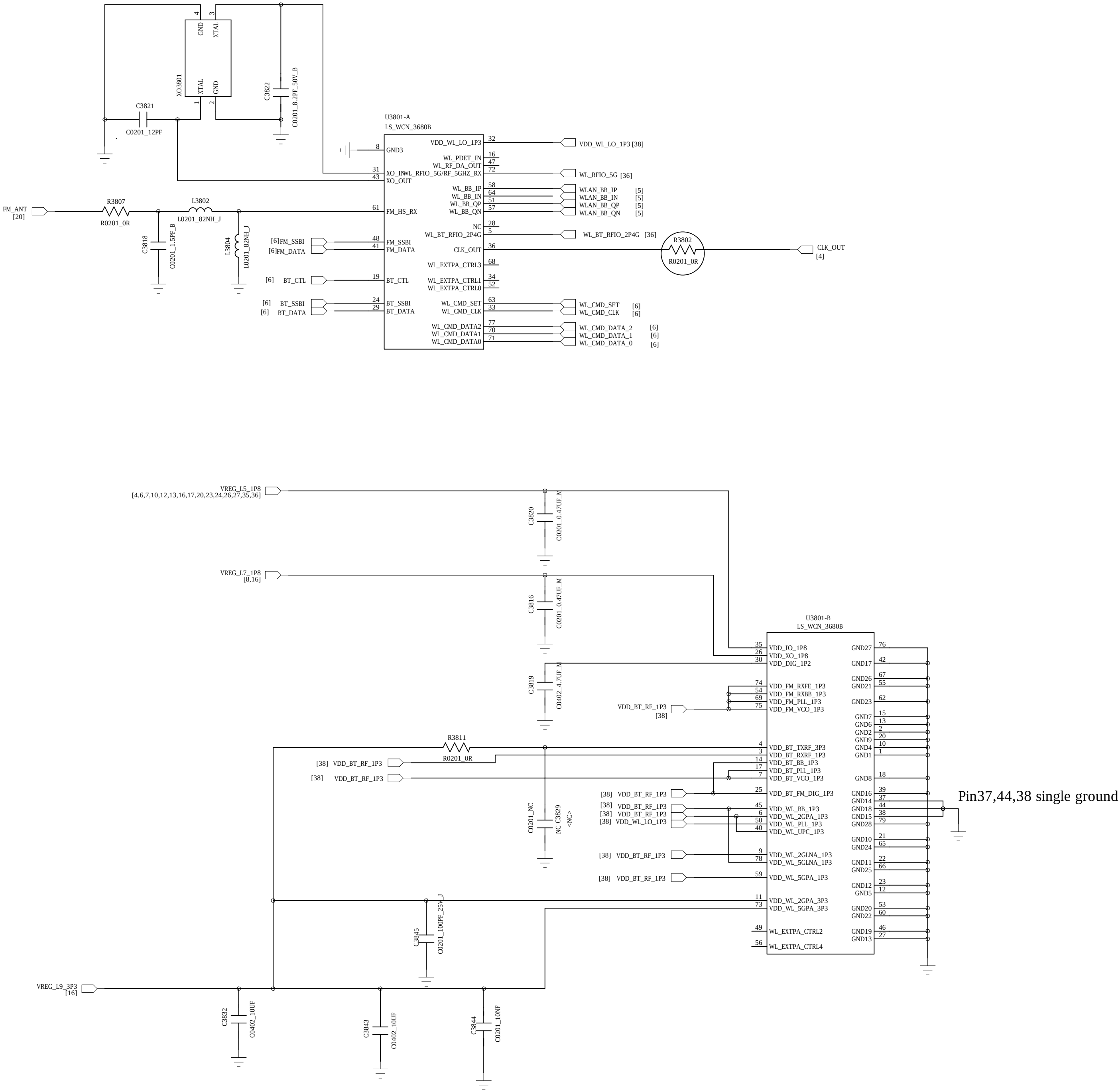
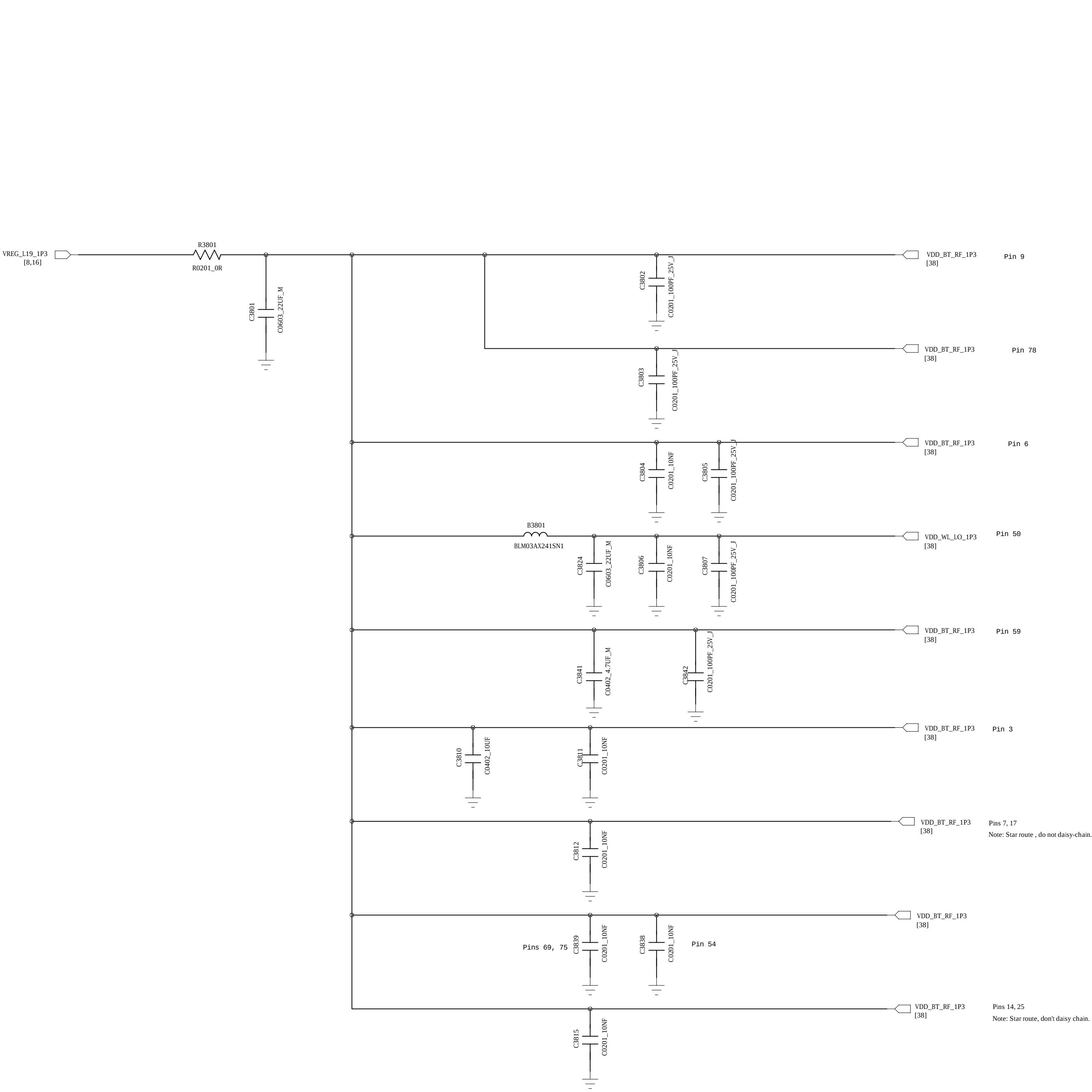
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

37: GPS



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

38£°WCN3660B



Pin37,44,38 single ground