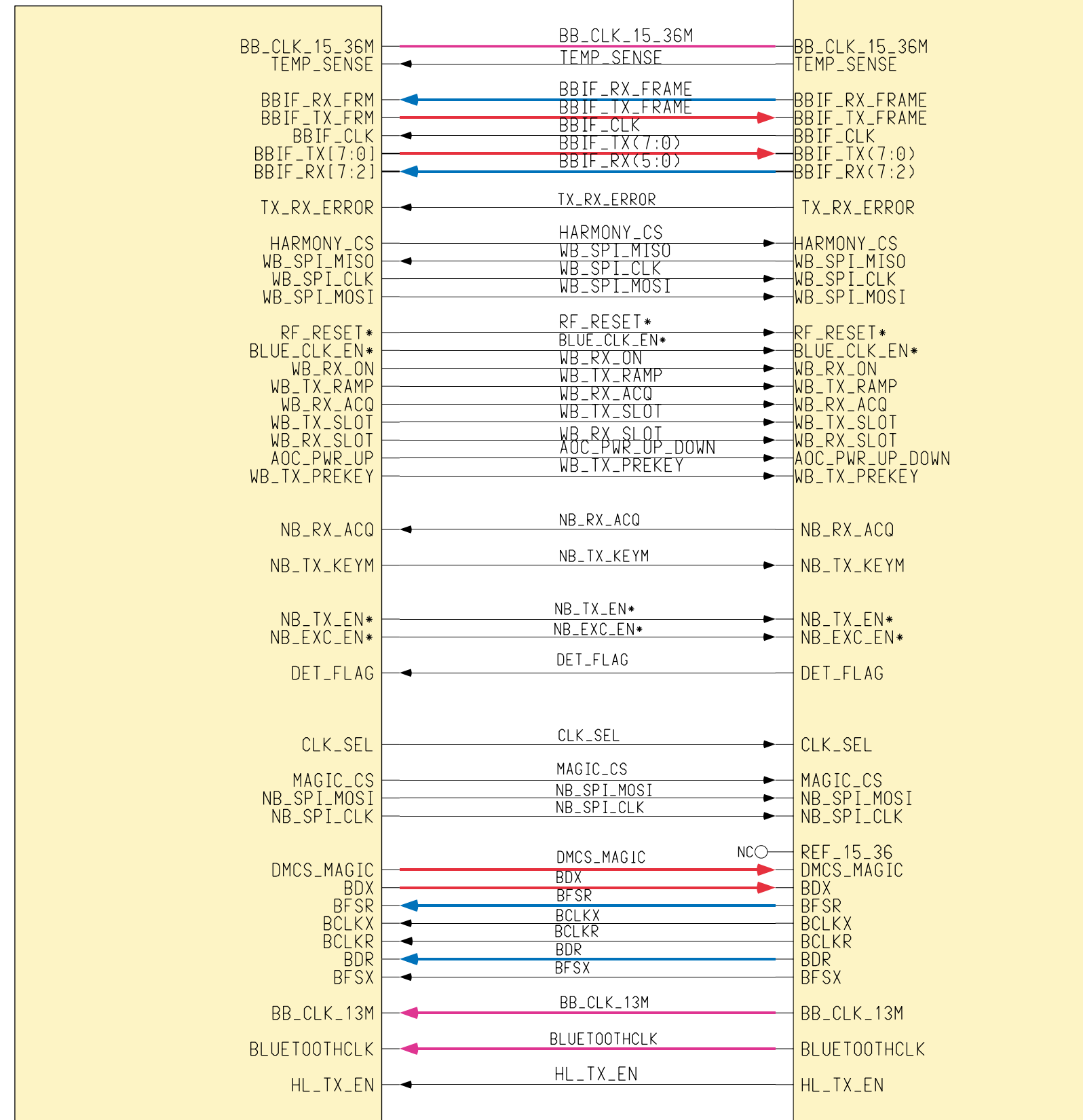


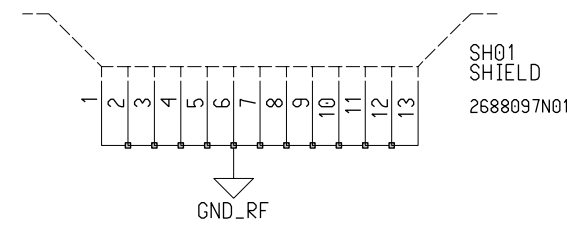
Engineer: Ed Naddeo	TITLE: Talon Integrated			Size: 11x17
Drawn by: Ed Naddeo	REV: P3			Page: 11
R&D CHK:	Drawing Number: 8489804N03			Of: 17
DOC CTRL CHK:	Top Level			
MFG CTRL CHK:	Date: January 7, 2003	Page:	Of:	
QA CHK:	Time: 10:43:18 am			
Changed by: Ed Naddeo				

Baseband

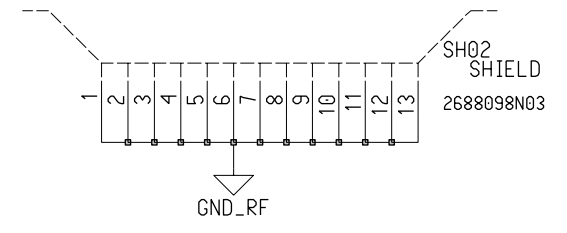
RF



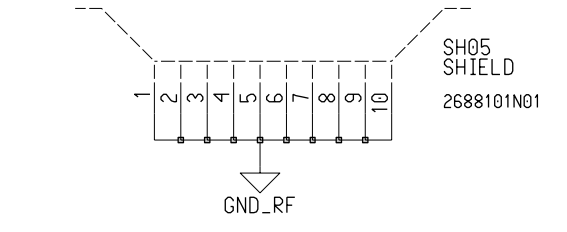
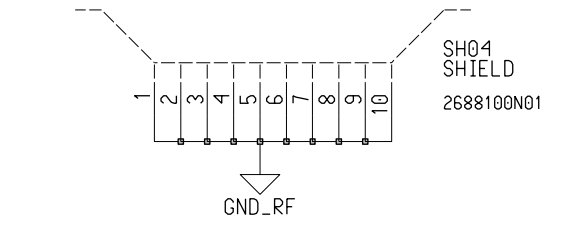
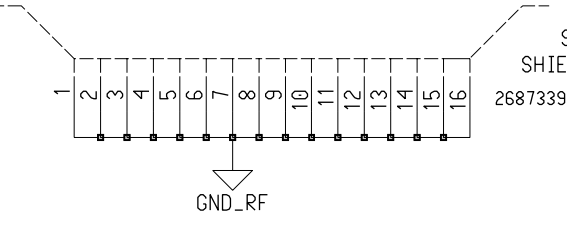
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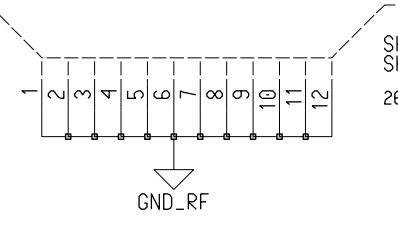
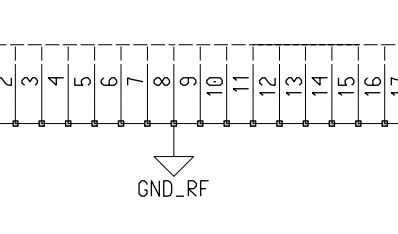
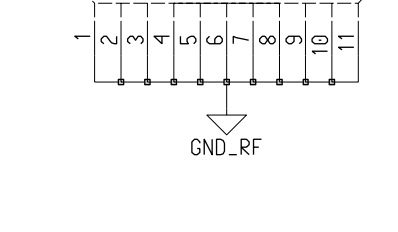
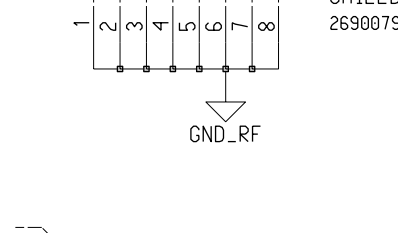
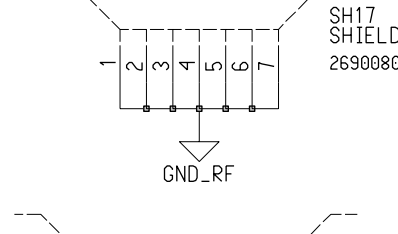
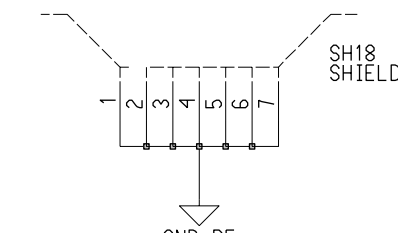
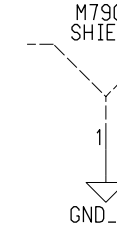
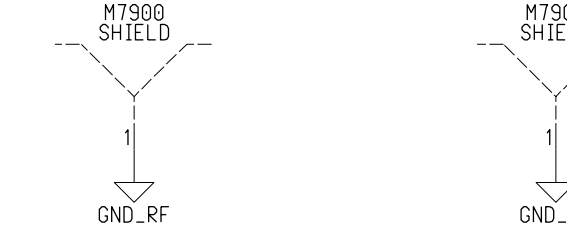
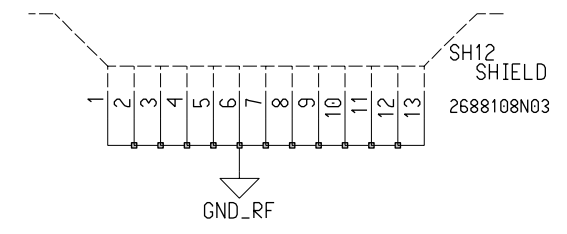
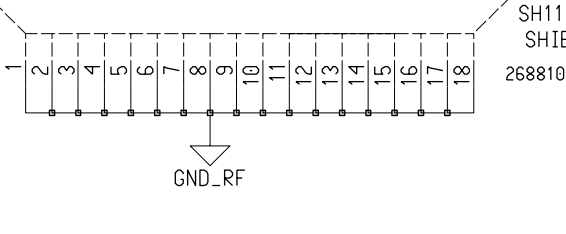
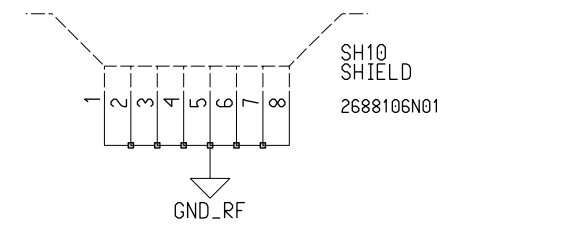
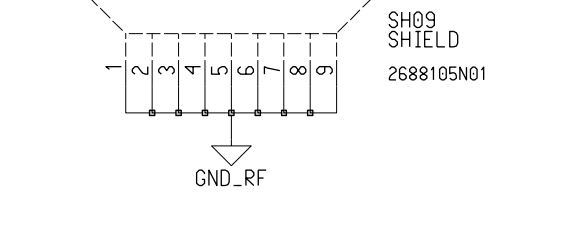
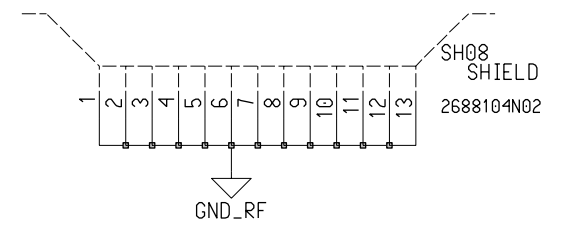
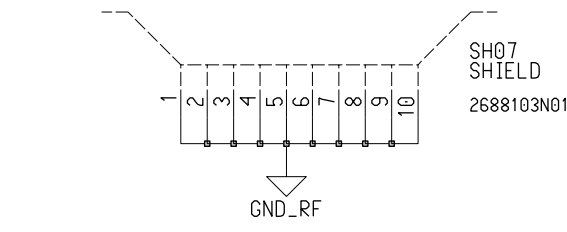
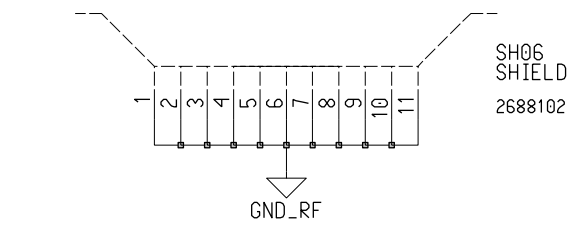
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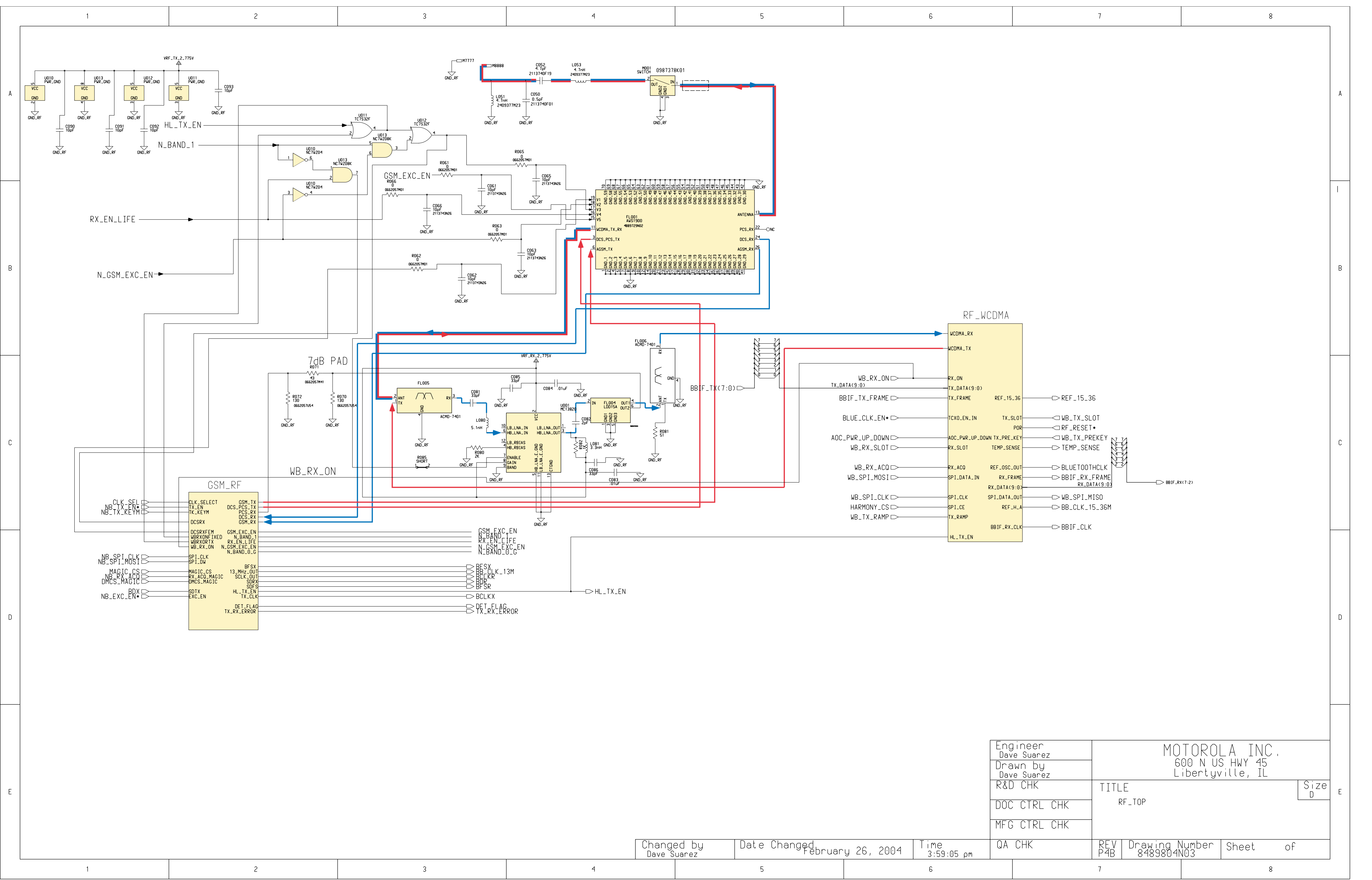


C



D

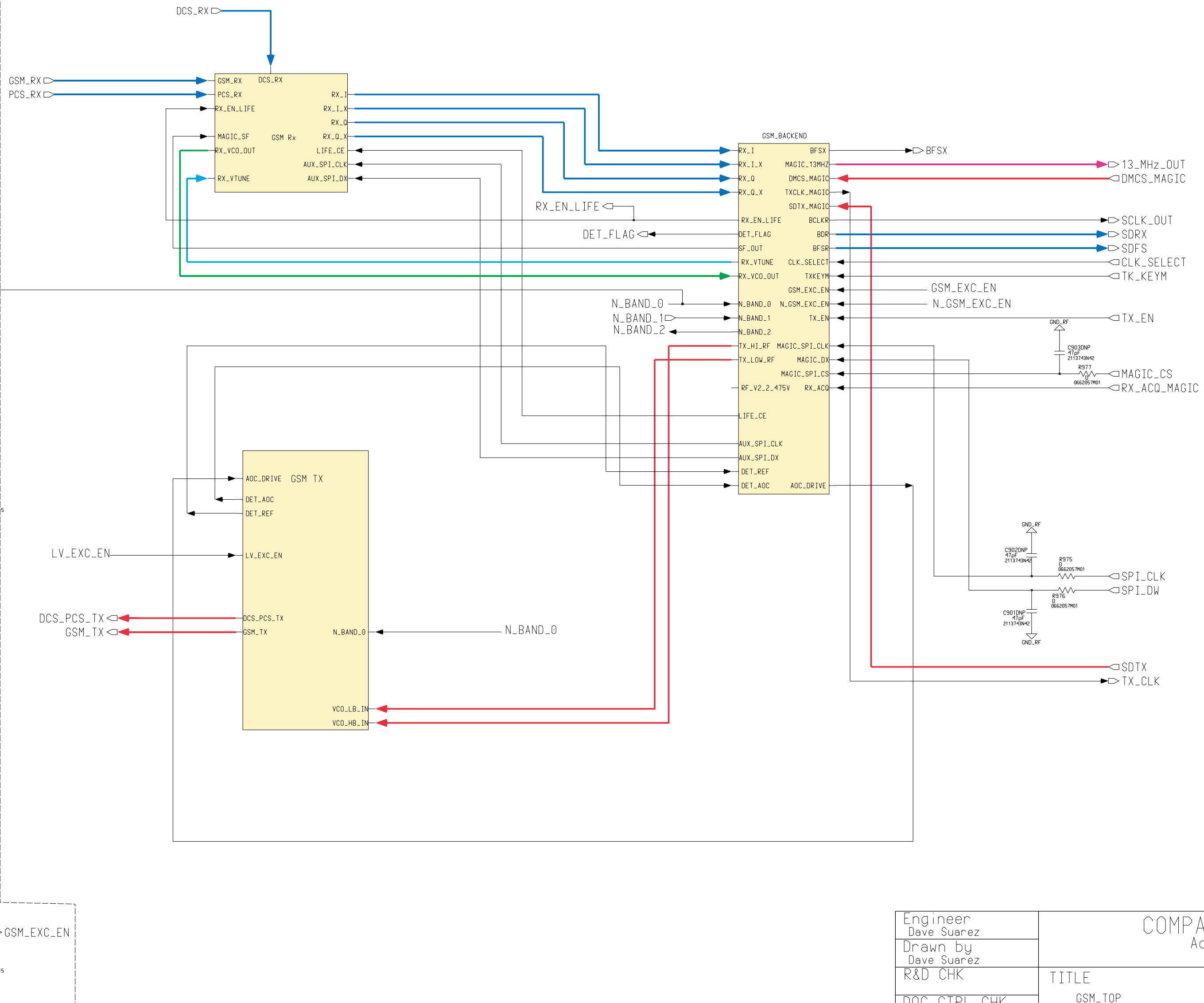




Engineer Dave Suarez	MOTOROLA INC. 600 N US HWY 45 Libertyville, IL			Size D
Drawn by Dave Suarez				TITLE RF_TOP
R&D CHK	REV P4B	Drawing Number 8489804N03	Sheet of	
DOC CTRL CHK	QA CHK	REV P4B	Sheet of	
MFG CTRL CHK	Time 3:59:05 pm	Time 3:59:05 pm	Sheet of	

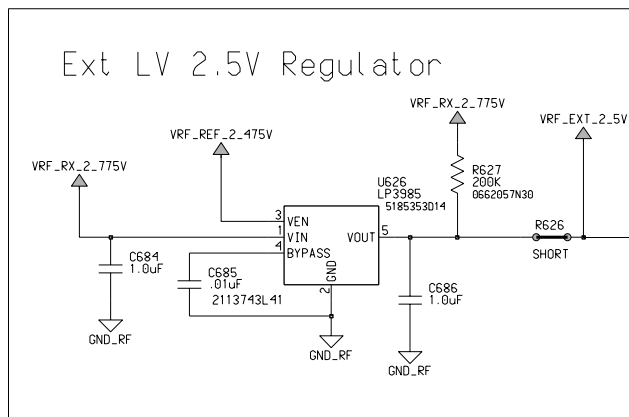
Changed by Dave Suarez Date Changed February 26, 2004 Time 3:59:05 pm

Band Select Circuits REF # 900-925

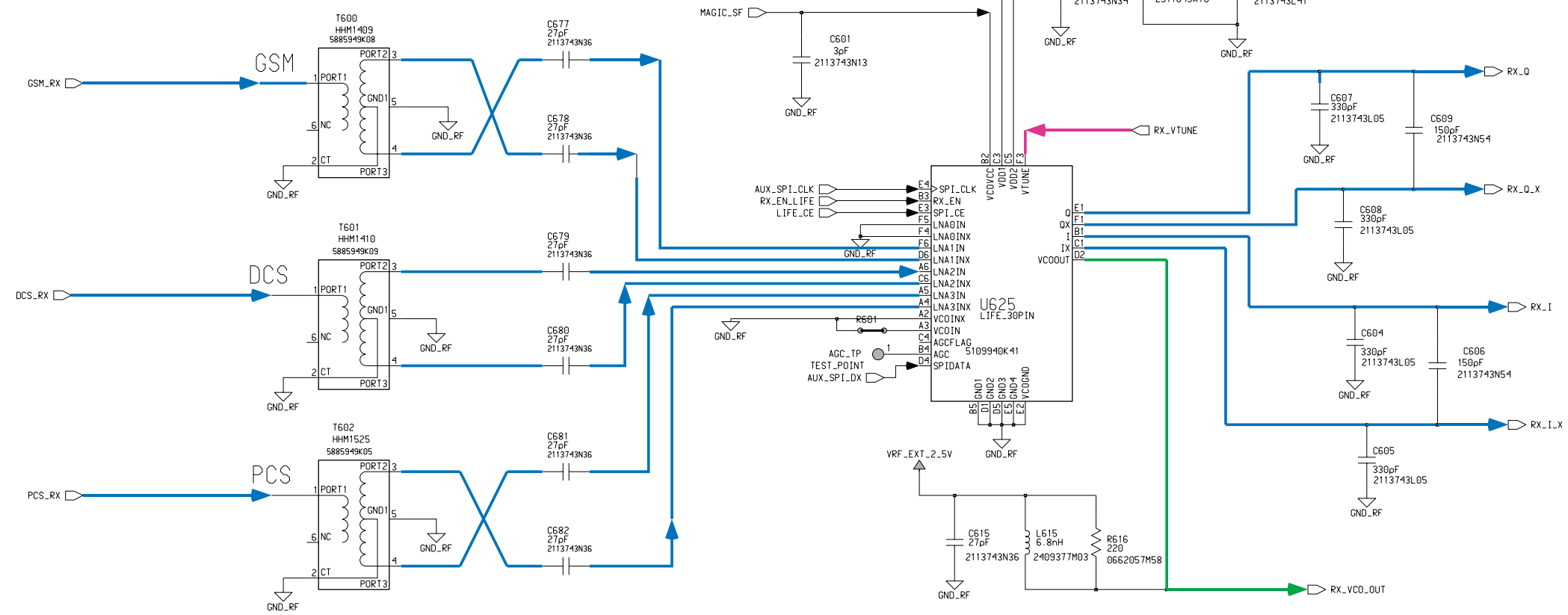


Engineer Dave Suarez	COMPANY NAME		
Drawn by Dave Suarez	Address City		
R&D CHK	TITLE GSM_TOP	Size D	
DOC CTRL CHK			
MFG CTRL CHK			
QA CHK	REV P3	Drawing Number 8489804N03	Sheet of

Changed by Dave Suarez Date Changed October 20, 2003 Time

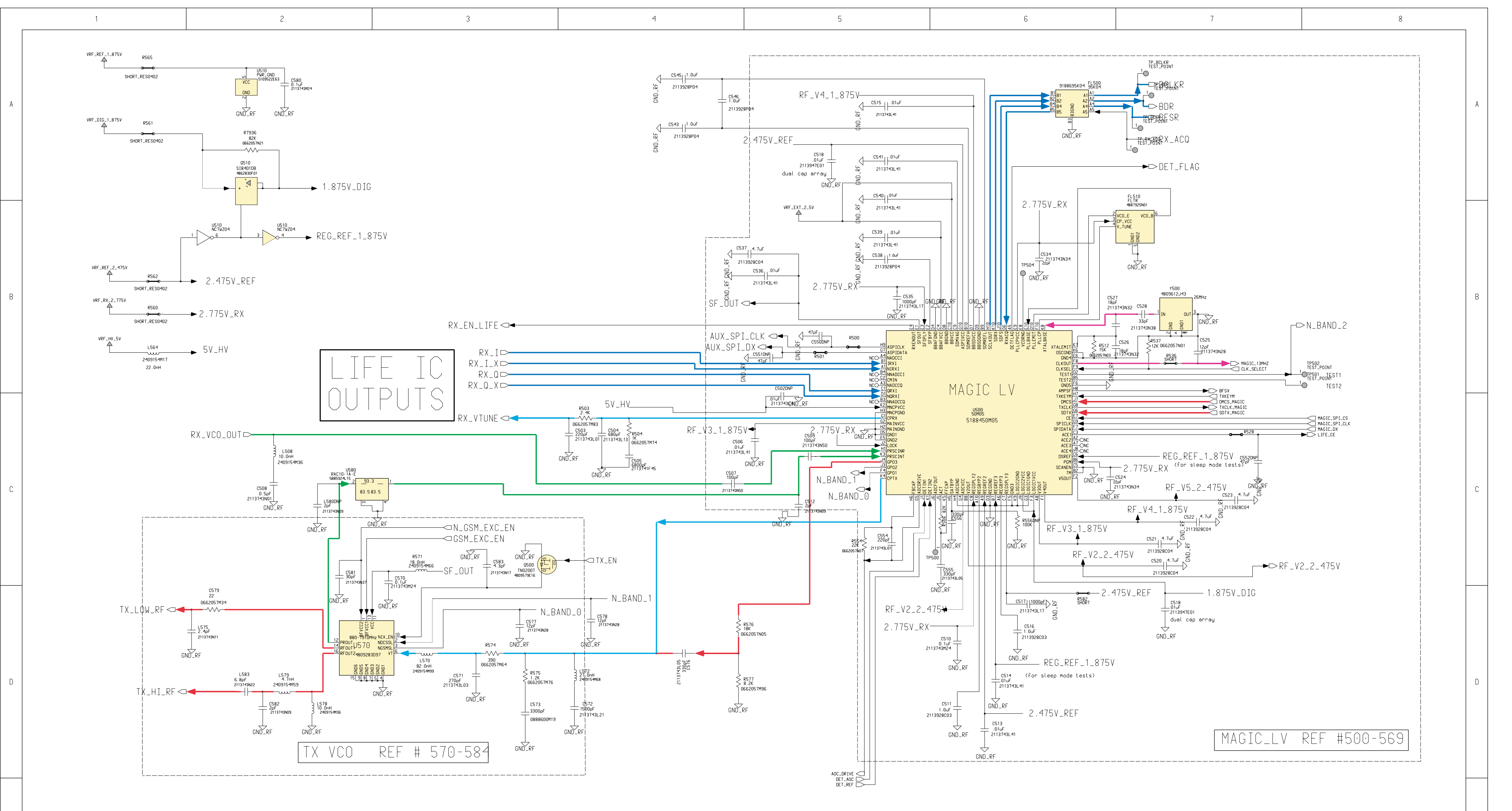


LIFE IC
REF # 600-649



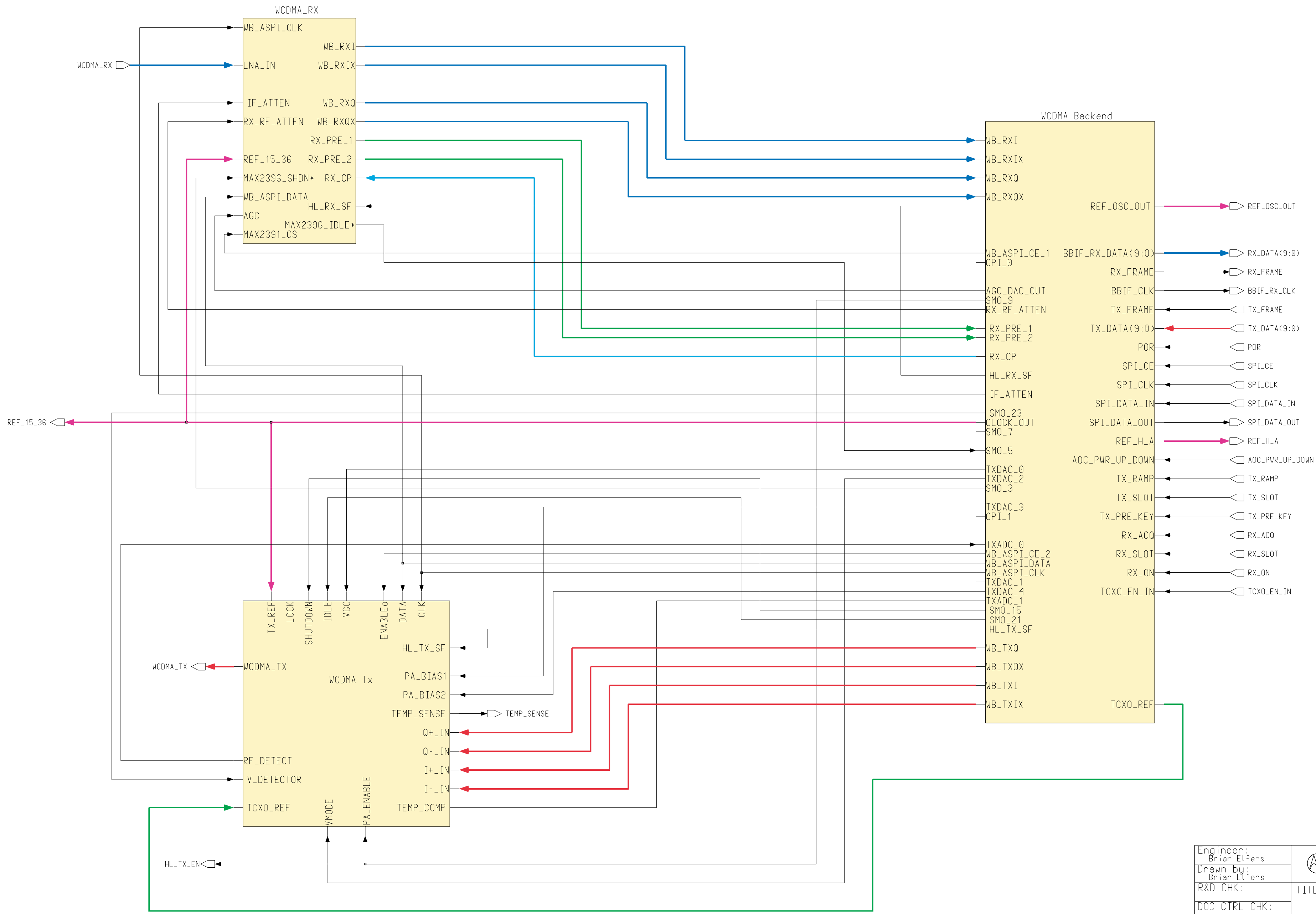
Engineer Brian Elfers	COMPANY NAME		
Drawn by Brian Elfers	Address City		
R&D CHK	TITLE GSM_Rx	Size D	
DOC CTRL CHK			
MFG CTRL CHK			
QA CHK	REV P3	Drawing Number 8487729N03	Sheet of

Changed by Dave Suarez Date Changed January 21, 2003 Time

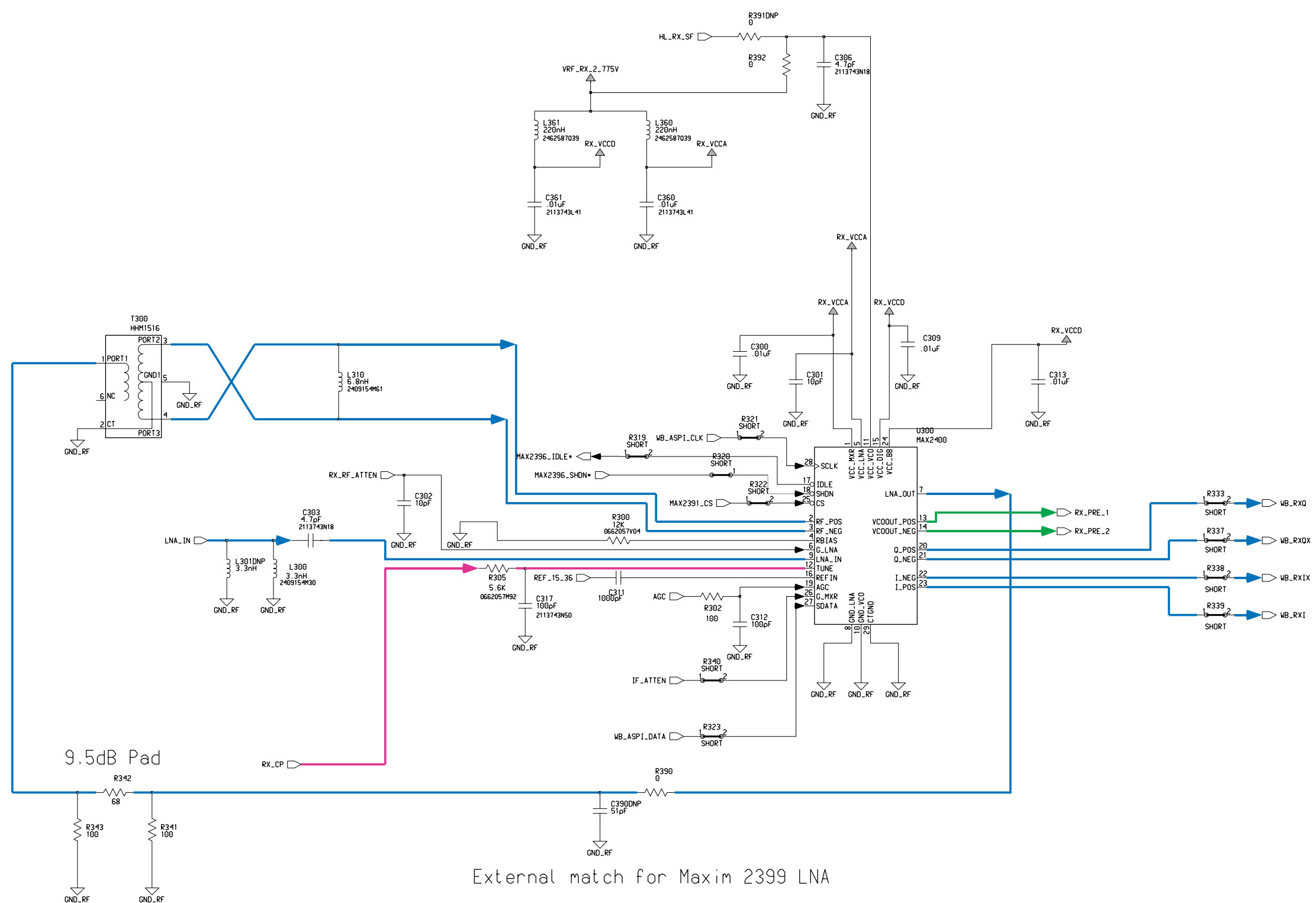


Engineer Andy Ma	MOTOROLA INC. LIBERTYVILLE IL		
Drawn by Andy Ma	TITLE		
R&D CHK	Talon_rev_0	GSM_backend	Size D
DOC CTRL CHK			
MFG CTRL CHK			
QA CHK	REV P3	Drawing Number 8489804N03	Sheet of

Changed by Dave Suarez Date Changed February 6, 2004 Time

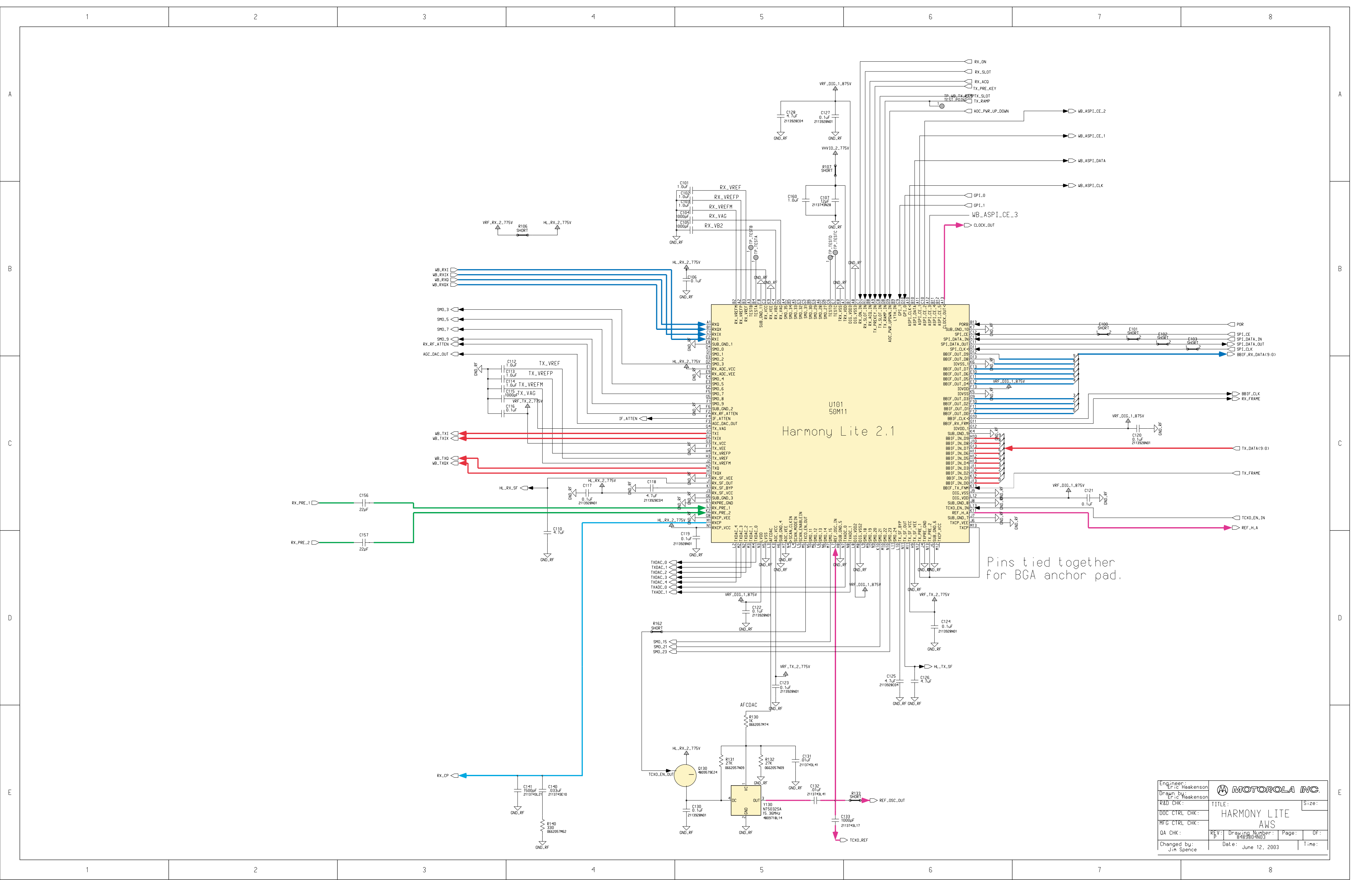


Engineer: Brian Elfers			
Drawn by: Brian Elfers			
R&D CHK:	TITLE: Talon_rev_0	Size: 11x17	
DOC CTRL CHK:			
MFG CTRL CHK:			
QA CHK:	REV: P3	Drawing Number: 8487729N03	Page: 0F:
Changed by: Dave Suarez	Date: Friday, April 20, 2001	Time: 4:07:28 pm	



External match for Maxim 2399 LNA

Engineer: Randy Wiessner			
Drawn by: Randy Wiessner			
R&D CHK:	TITLE: WCDMA_Rx	Size: B	
DOC CTRL CHK:			
MFG CTRL CHK:			
QA CHK:	REV: P6	Drawing Number: 8487123N05	Page: 1 of 1
Changed by: Dave Suarez	Date: June 12, 2003		Time: 4:33:16 pm

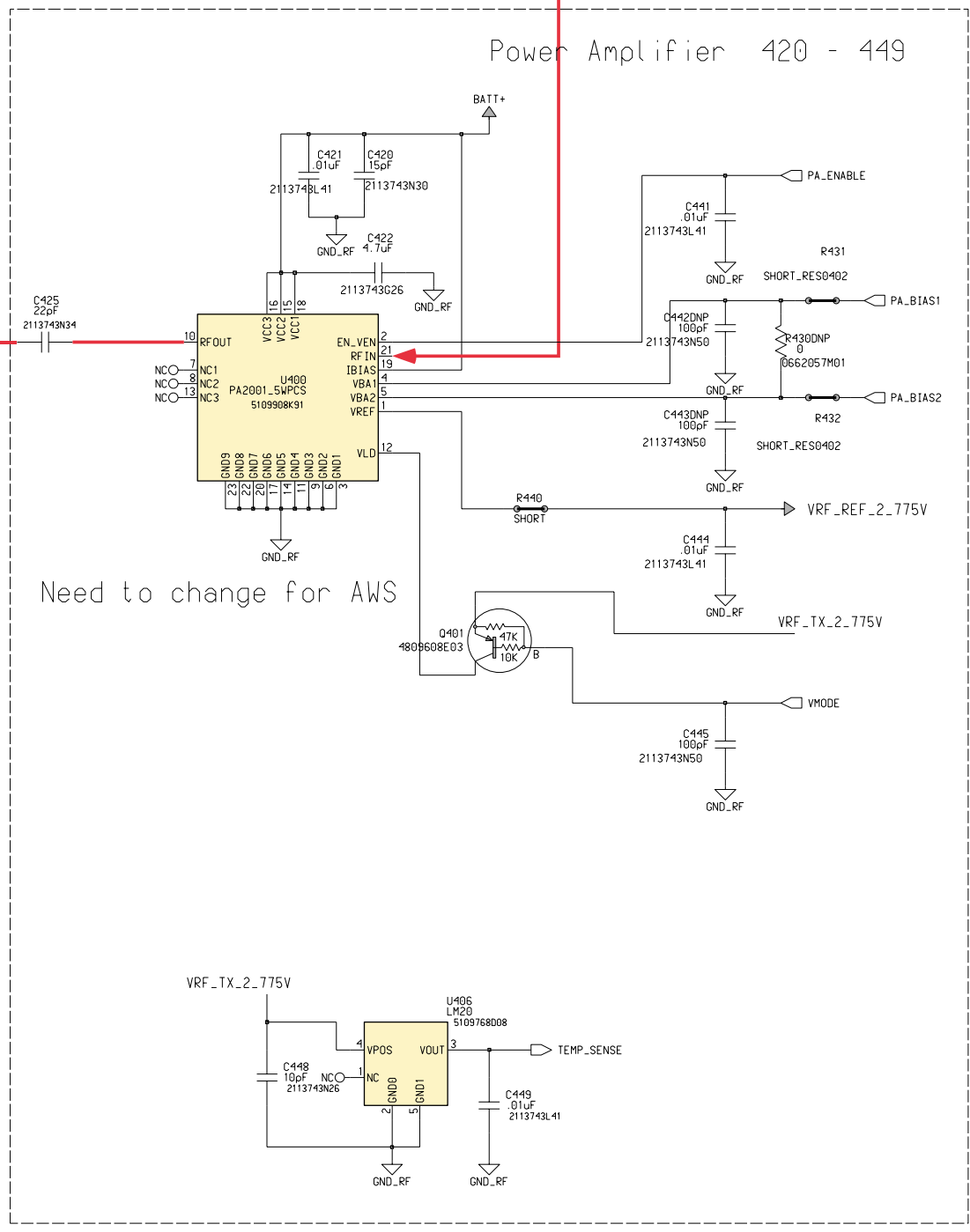
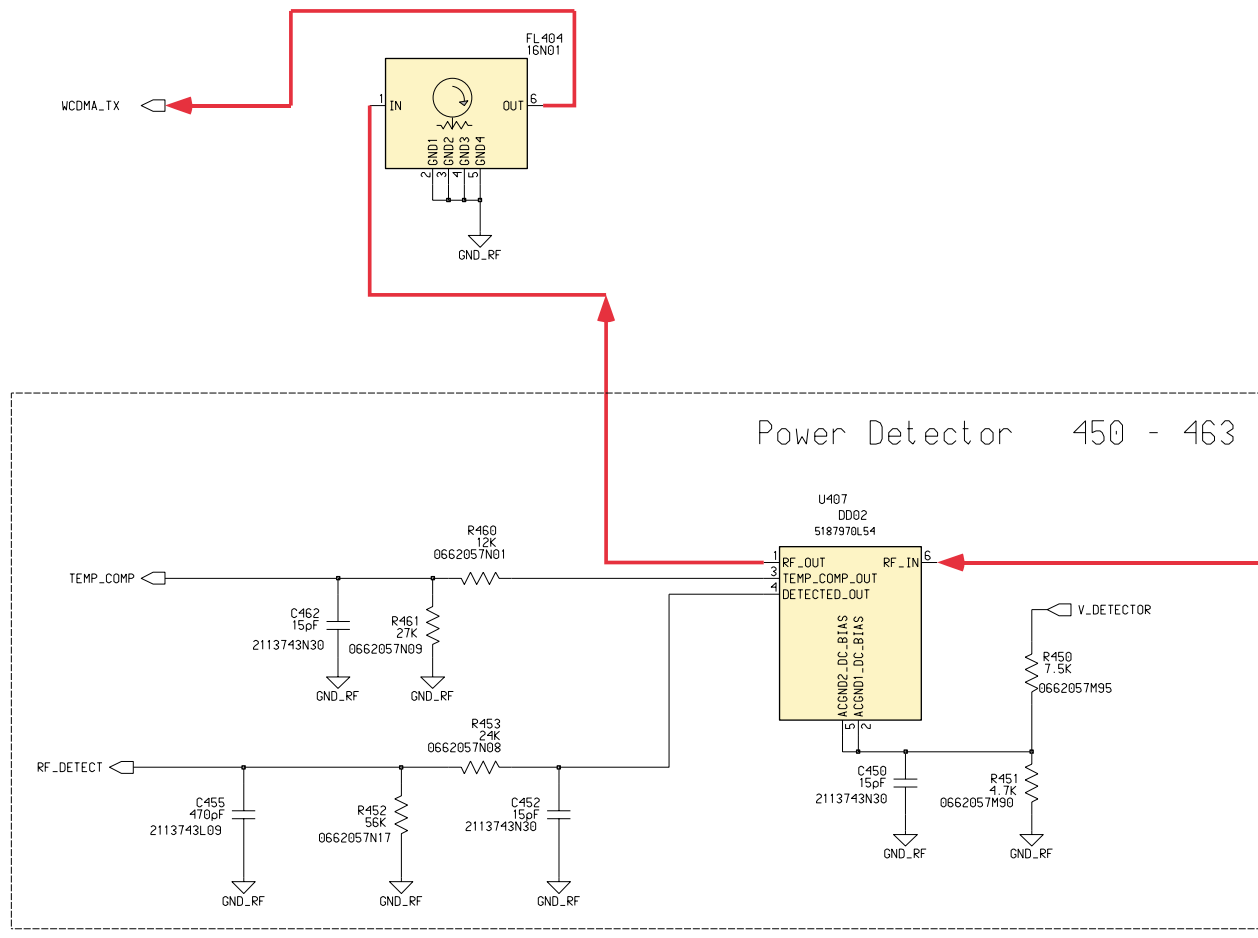


U101
50M11
Harmony Lite 2.1

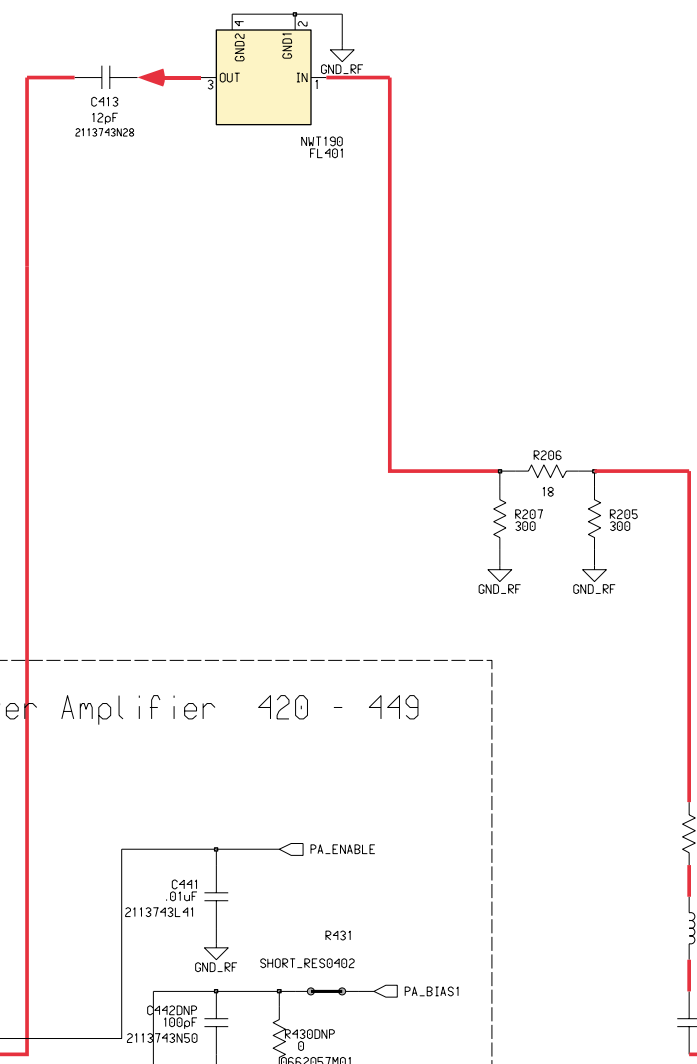
Pins tied together for BGA anchor pad.

Engineer:	Eric Haekenson		Size:
Drawn by:	Eric Haekenson		TITLE:
R&D CHK:		HARMONY LITE	
DOC CTRL CHK:		AWS	
MFG CTRL CHK:		REV: Drawing Number: 8483804N03	Page: 0F:
QA CHK:		Date: June 12, 2003	Time:
Changed by:	Jim Spence		

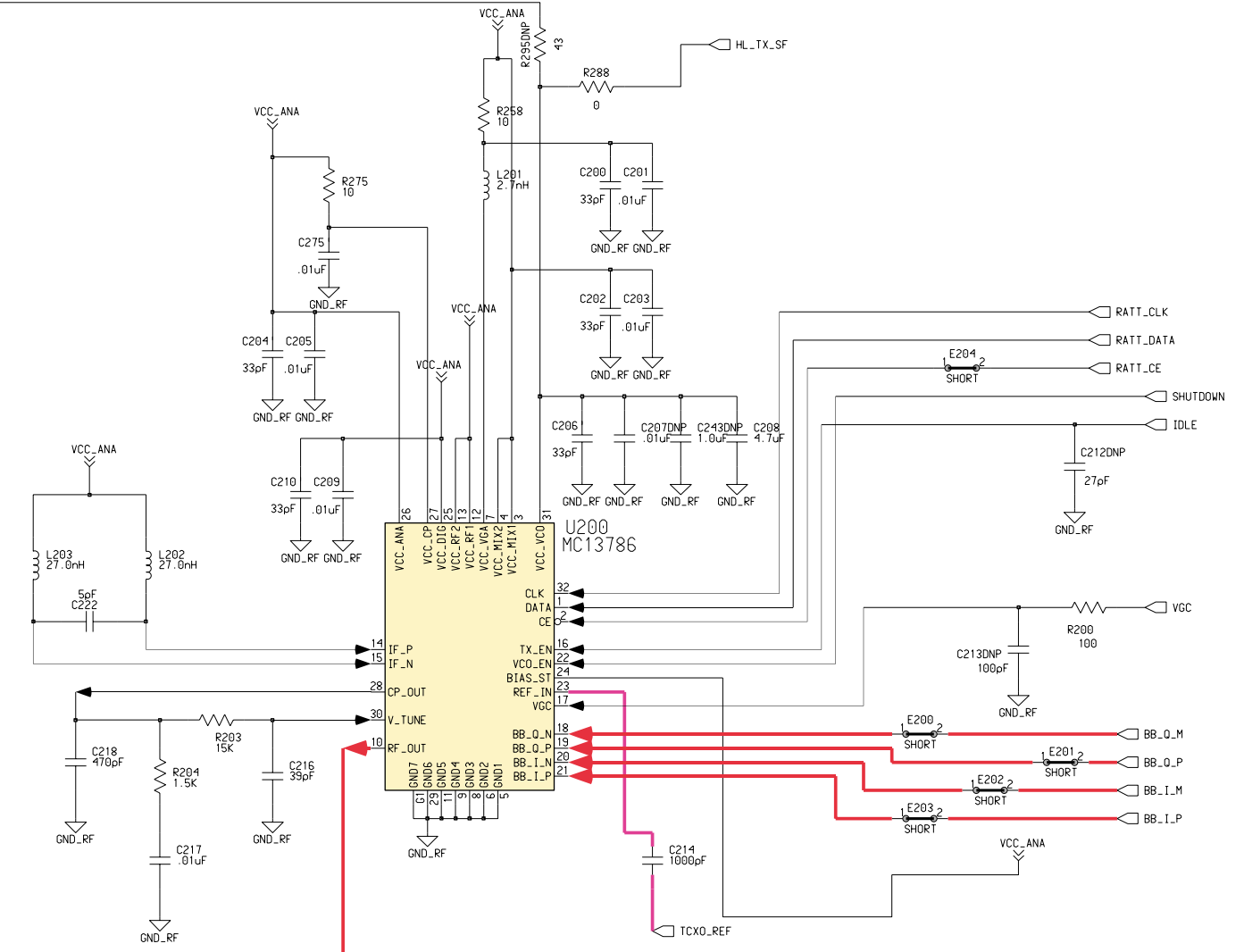
WCDMA Transmitter (TALON)



Interstage filter 1850 - 1910



Quadrature modulator and upmixer 200-299



Engineer John Mura	COMPANY NAME	
Drawn by John Mura	Address City	
R&D CHK	TITLE WCDMA_Tx	Size D
DOC CTRL CHK		
MFG CTRL CHK		
QA CHK	REV P4B	Drawing Number 8489804N04
Changed by James Spence	Date Changed February 25, 2004	Time 4:39:58 pm
	Sheet 1	of 1

Changed by James Spence Date Changed February 25, 2004 Time 4:39:58 pm

MOTOROLA CONFIDENTIAL PROPRIETARY

Engineer: John S. Kerr	MOTOROLA INC.	
Drawn by: Todd Roessler		
R&D CHK:	TITLE: Talon Integrated	Size: 11x17
DOC CTRL CHK:	Baseband Top Level	
MFG CTRL CHK:	REV:	Page: Of:
QA CHK:	Drawing Number:	Page: Of:
Changed by: Ed Neddeo	Date: Monday, March 4, 2002	Time: 10:39:56 am

A

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Misc Connectors

- Display Connector USIM
- Keypad Connector IrDA
- Headset Jack Vibrator
- Fun Lights

CORE LOGIC BLOCK

- POG
- Memories
- Video
- AGPS
- Embedded SD

CE Bus Connector

- VID_RTS*
- LOGIC_SENSE
- VID_CTS*
- VID_GPS_RTS*
- SWB+_EN
- HKSW
- MUTE*
- DSEL2
- DSEL1
- DSEL0
- OPT1
- OPT2
- DATALOG*

- BATT_FDBK
- AUDIO_IN
- AUDIO_OUT

Bluetooth

- BLUETOOTH_WAKE*
- BLUETOOTH_INT*
- BLUE_TX
- BLUE_CTS*
- BLUE_RTS*
- BLUE_RX
- WB_SPI_CLK
- WB_SPI_MOSI
- WB_SPI_MISO
- HARMONY_CS
- CLK_32_768K_BUFF
- ASAP_FS
- ASAP_CLK
- ASAP_RX
- ASAP_TX
- STEREO_FS
- STEREO_CLK
- STEREO_TX
- VSIMC_EN
- MUXCTL
- BATT_IO
- RXD_VMIN
- TXD_VPOUT
- RTS*_XRXD
- USB_TXEN*
- USB_VMOUT
- USB_VPIN
- USB_DETECT
- BB_SPI_CLK
- BB_SPI_MISO
- BB_SPI_MOSI
- PCAP_CS
- PCAP_INT
- PCAP_RESET*
- USER_OFF
- POWER_FAIL
- PCAP_MCU_RESET*
- WATCHDOG
- CLK_32_768K
- NB_STANDBY
- VID_GPSb_SEL
- MIDRATE_1
- MIDRATE_2
- NB_TX_KEYM
- BATT_FDBK_SW
- BATT_DETb

PCAP (Power/Audio)

- ASAP_FS
- ASAP_CLK
- ASAP_RX
- ASAP_TX
- STEREO_FS
- STEREO_CLK
- STEREO_TX
- VSIMC_EN
- MUXCTL
- BATT_IO
- RXD_VMIN
- TXD_VPOUT
- RTS*_XRXD
- USB_TXEN*
- USB_VMOUT
- USB_VPIN
- USB_DETECT
- BB_SPI_CLK
- BB_SPI_MISO
- BB_SPI_MOSI
- PCAP_CS
- PCAP_INT
- PCAP_RESET*
- USER_OFF
- POWER_FAIL
- PCAP_MCU_RESET*
- WATCHDOG
- CLK_32_768K
- NB_STANDBY
- VID_GPSb_SEL
- MIDRATE_1
- MIDRATE_2
- NB_TX_KEYM
- BATT_FDBK_SW
- BATT_DETb
- AUDIO_OUT
- AUDIO_IN
- D-
- D+
- BATT_FDBK
- CLK_32_768K_BUFF
- PCAP_CLK_IN
- ALRT+
- ALRT-
- HS_MAKE_DETECT
- HS_MIC
- HS_SPKR_L
- HS_SPKR_R
- TEMP_SENSE
- ON_OFF_END*
- ELEN1
- V_VIB
- EAR_SPKR
- EAR_SPKR+

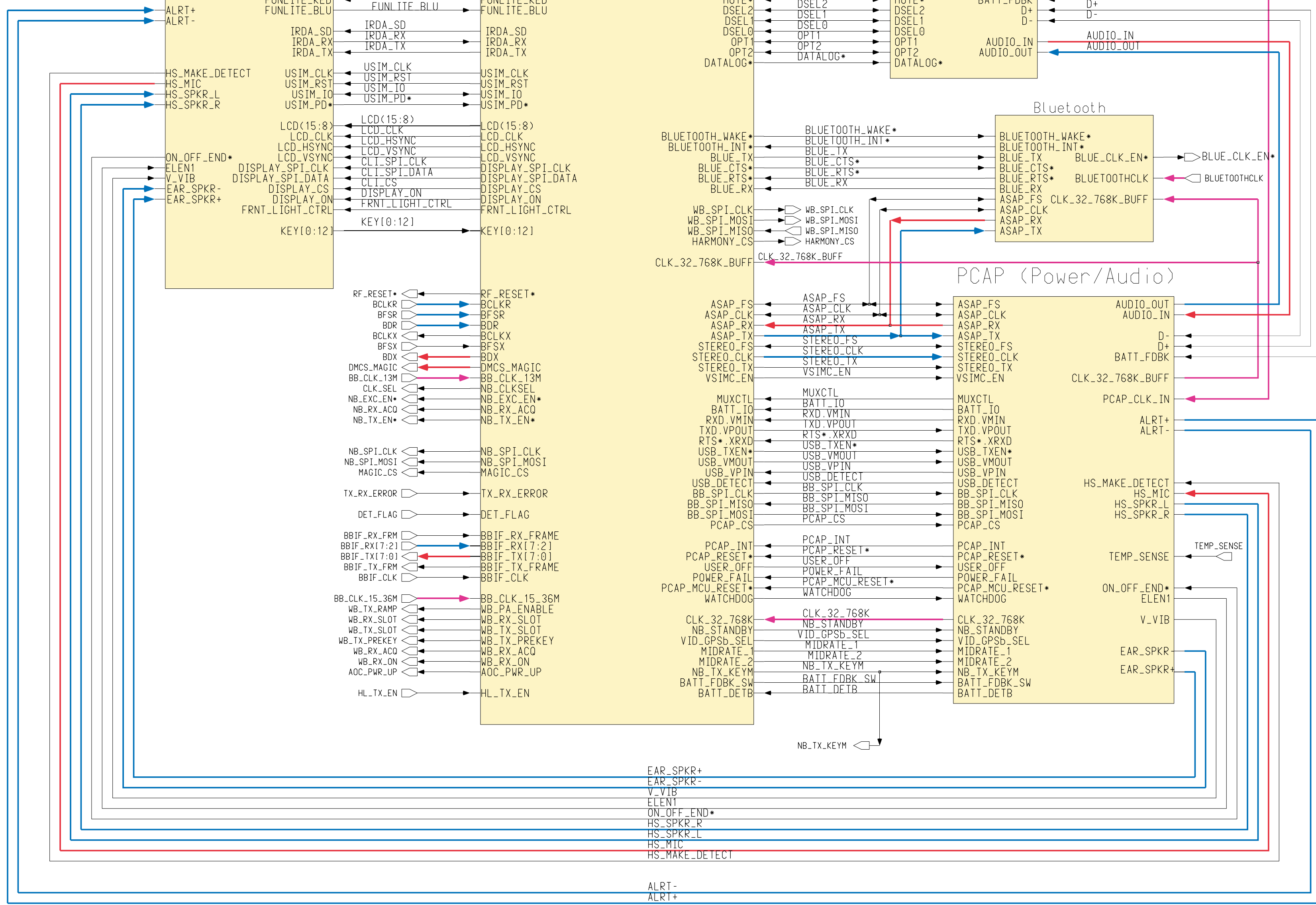
- RF_RESET*
- BCLKR
- BFSR
- BDR
- BCLKX
- BFSX
- BDX
- DMCS_MAGIC
- BB_CLK_13M
- CLK_SEL
- NB_EXC_EN*
- NB_RX_ACO
- NB_TX_EN*

- NB_SPI_CLK
- NB_SPI_MOSI
- MAGIC_CS
- TX_RX_ERROR
- DET_FLAG

- BBIF_RX_FRM
- BBIF_RX[7:2]
- BBIF_TX[7:0]
- BBIF_TX_FRM
- BBIF_CLK

- BB_CLK_15_36M
- WB_TX_RAMP
- WB_RX_SLOT
- WB_TX_SLOT
- WB_TX_PREKEY
- WB_RX_ACO
- WB_RX_ON
- AOC_PWR_UP
- HL_TX_EN

- EAR_SPKR+
- EAR_SPKR-
- V_VIB
- ELEN1
- ON_OFF_END*
- HS_SPKR_R
- HS_SPKR_L
- HS_MIC
- HS_MAKE_DETECT
- ALRT-
- ALRT+



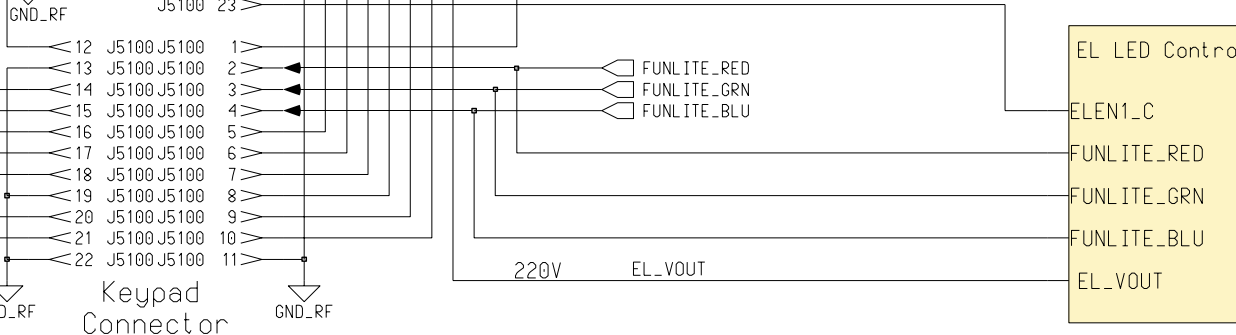
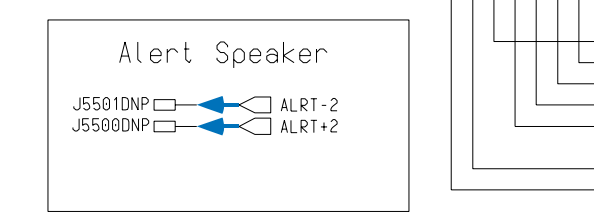
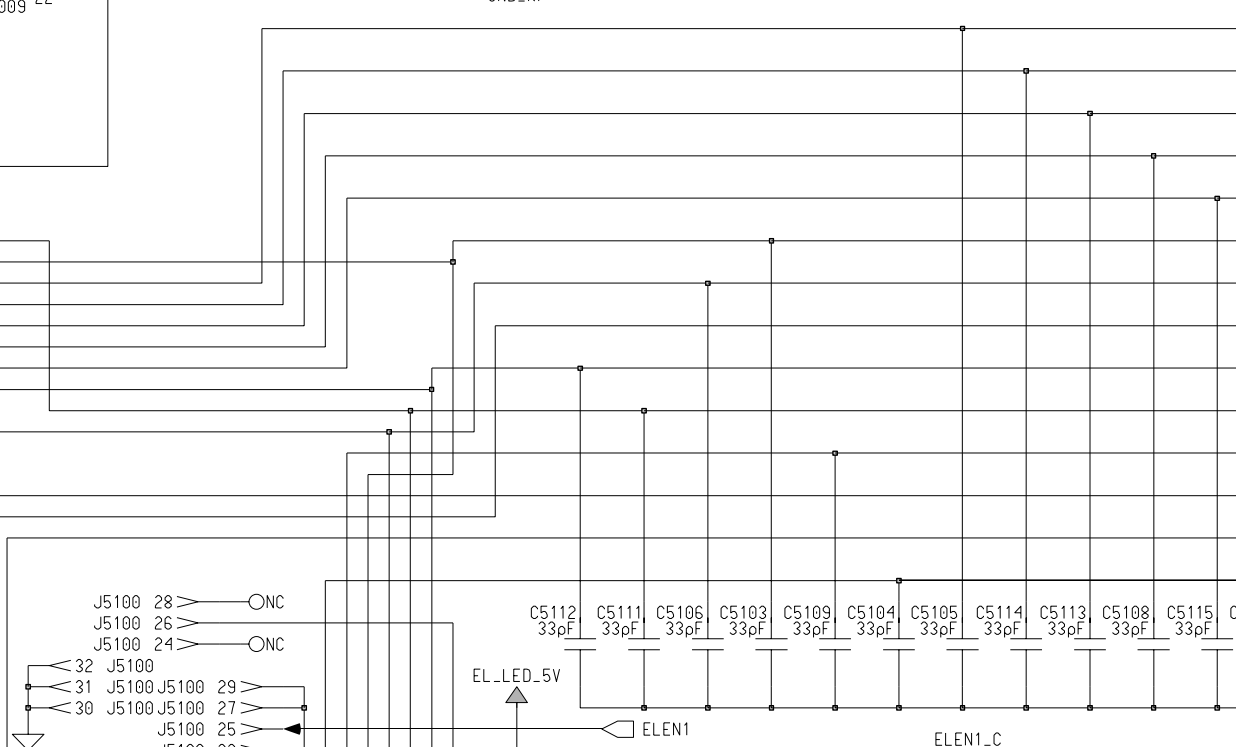
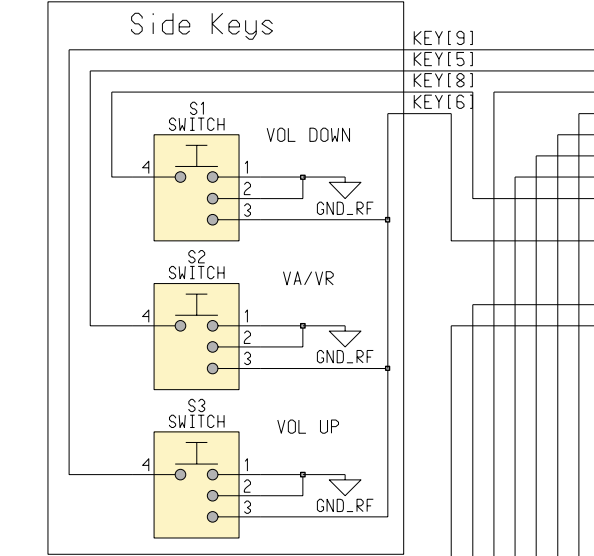
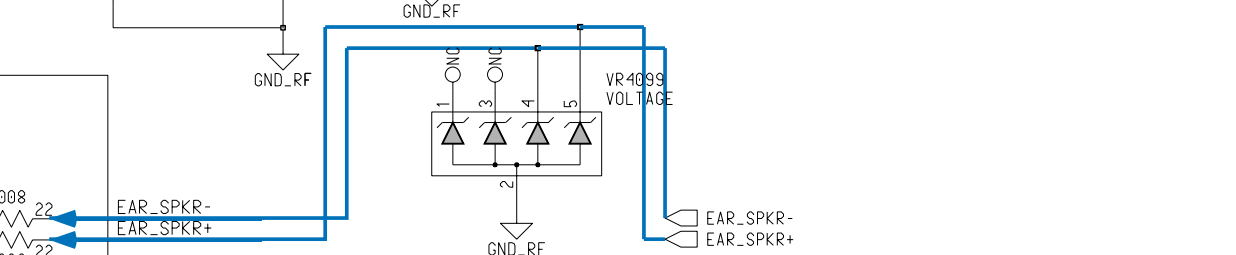
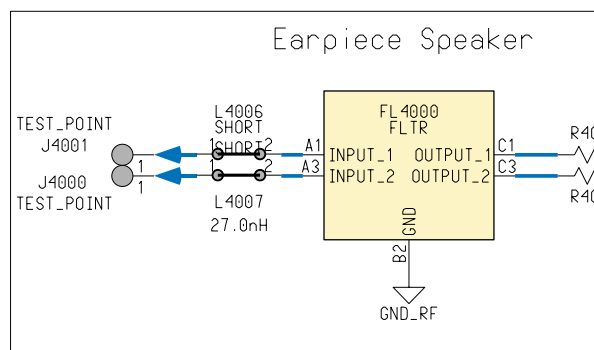
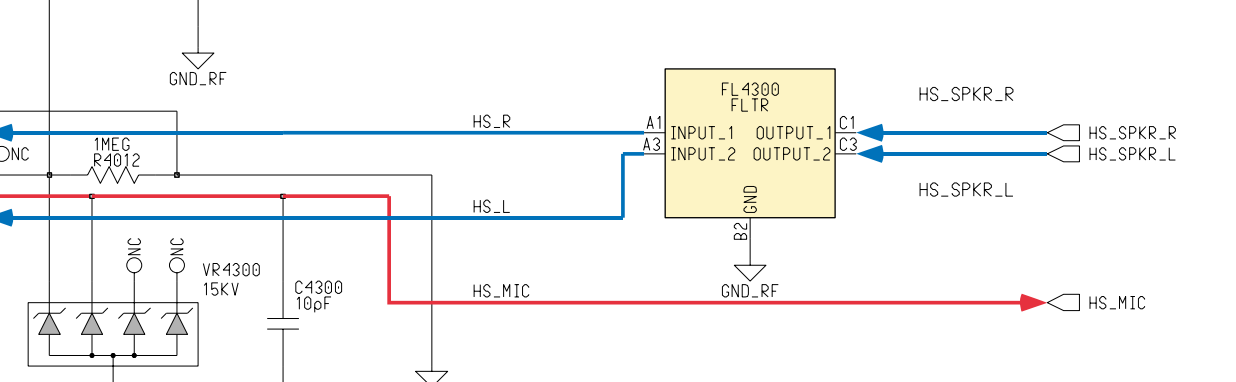
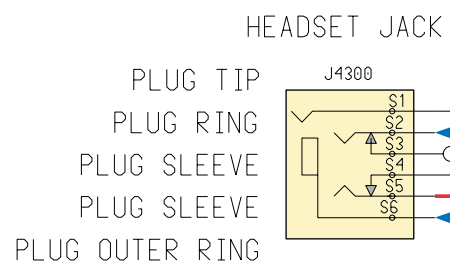
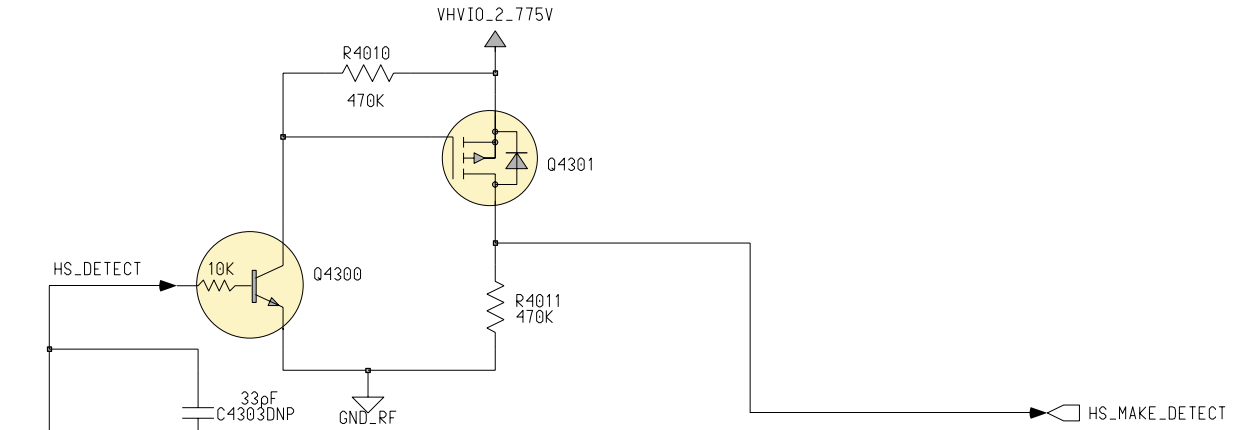
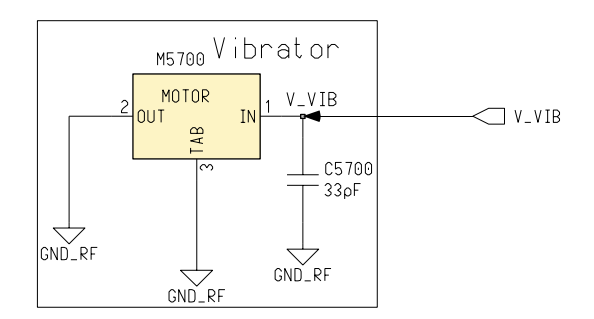
Engineer: Ed Naddeo	TITLE: TALON INTEGRATED Misc Connectors	Size: 11x21
Drawn by: Ed Naddeo		
R&D CHK:	REV: Drawing Number: Page: Of: 1	
DOC CTRL CHK:		
MFG CTRL CHK:		
QA CHK:		
Changed by: Ed Naddeo	Date: Monday, April 23, 2001	Time: 11:27:59 am

A

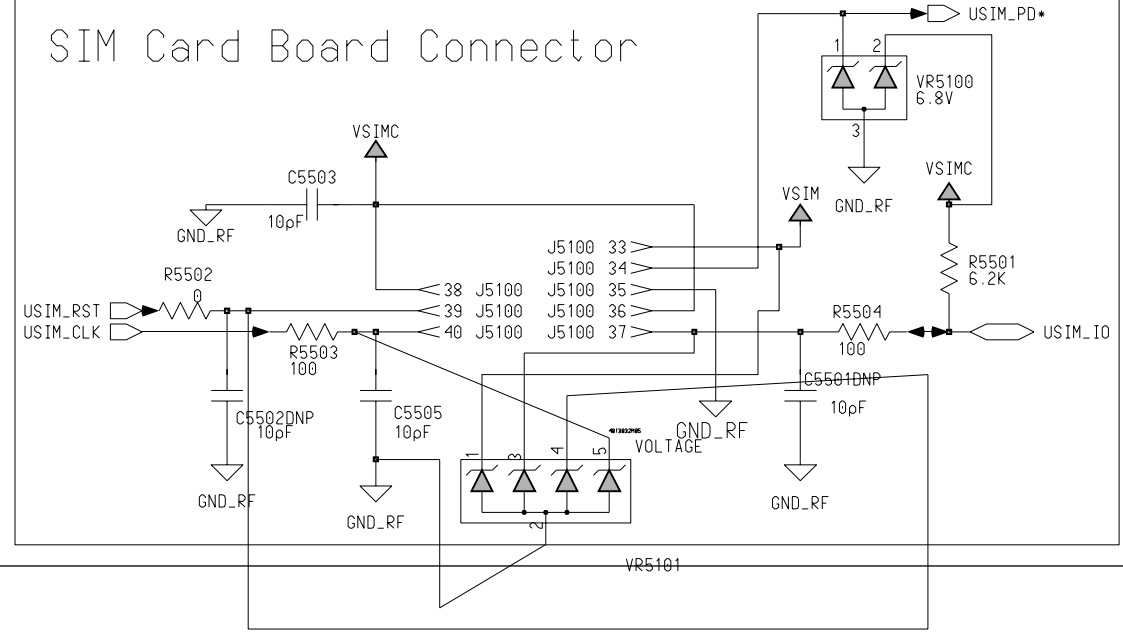
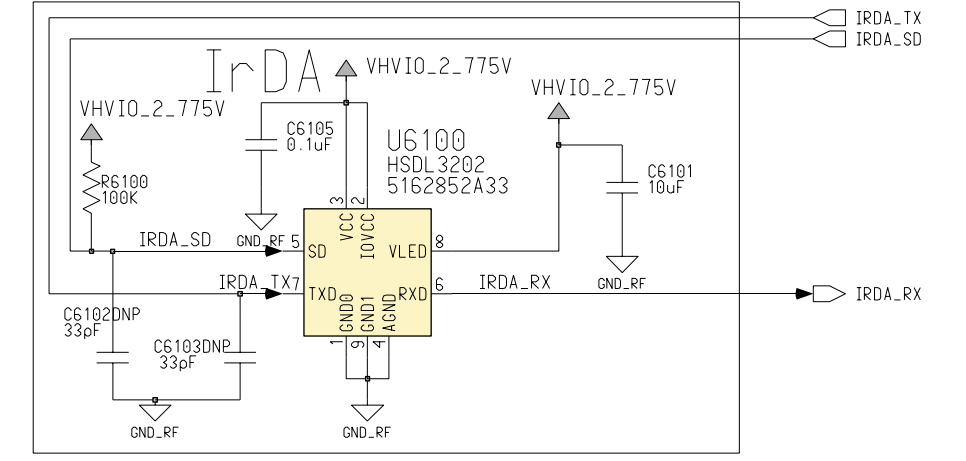
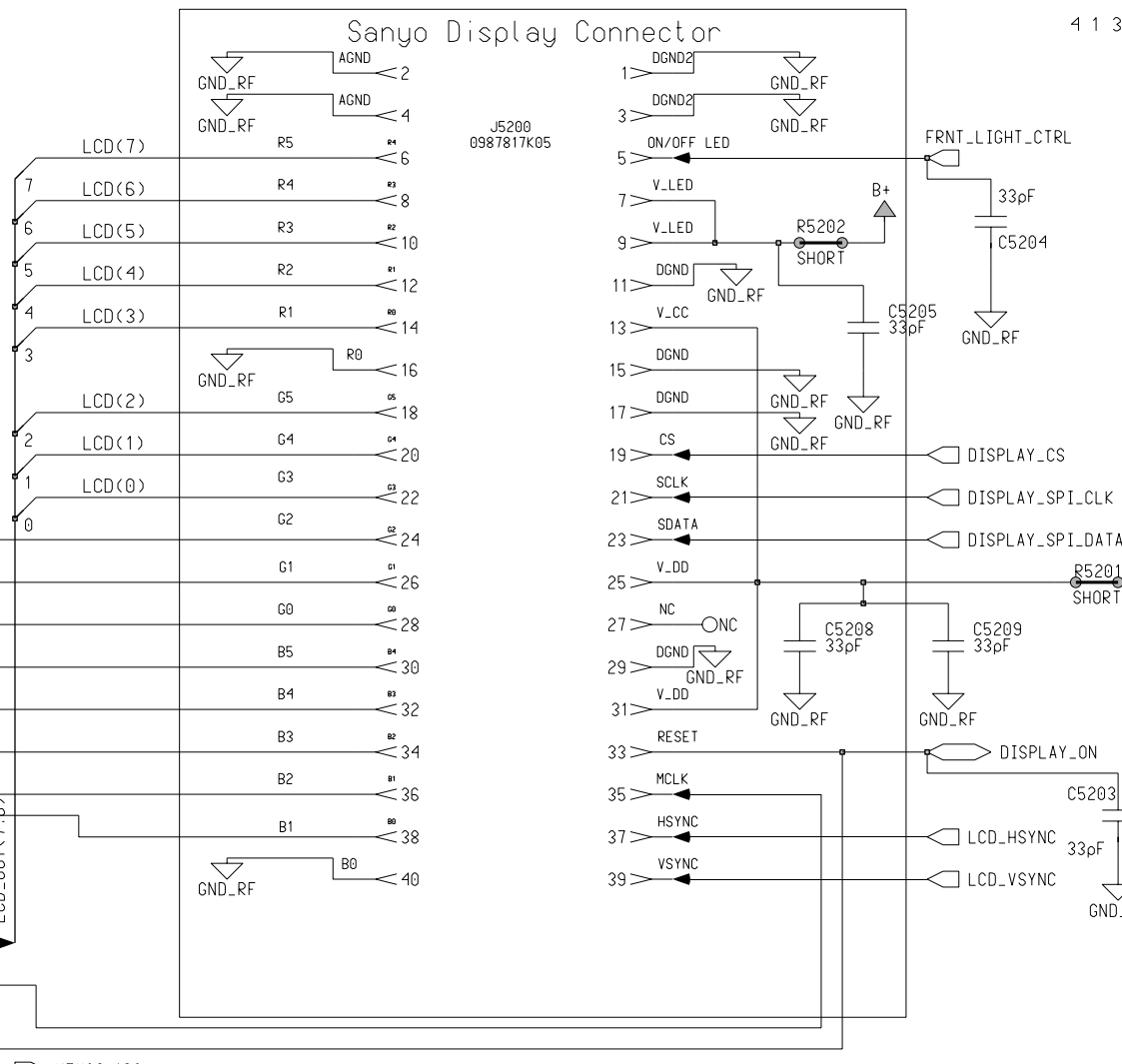
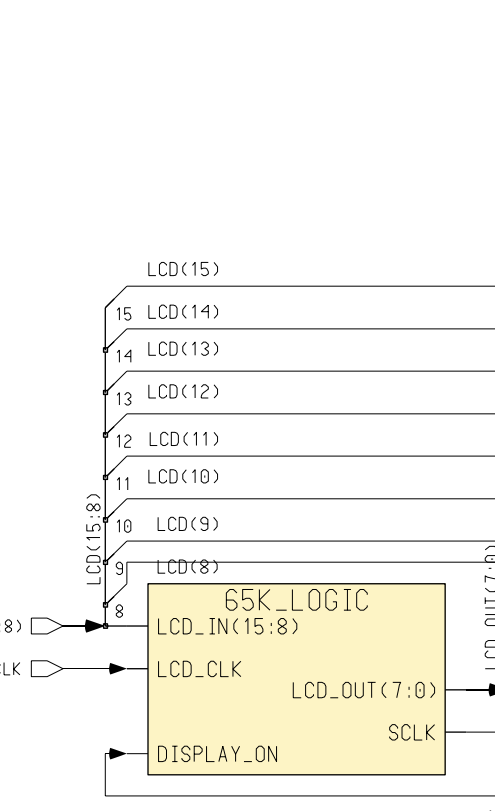
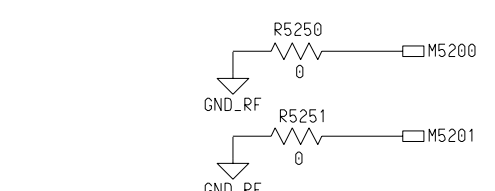
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C

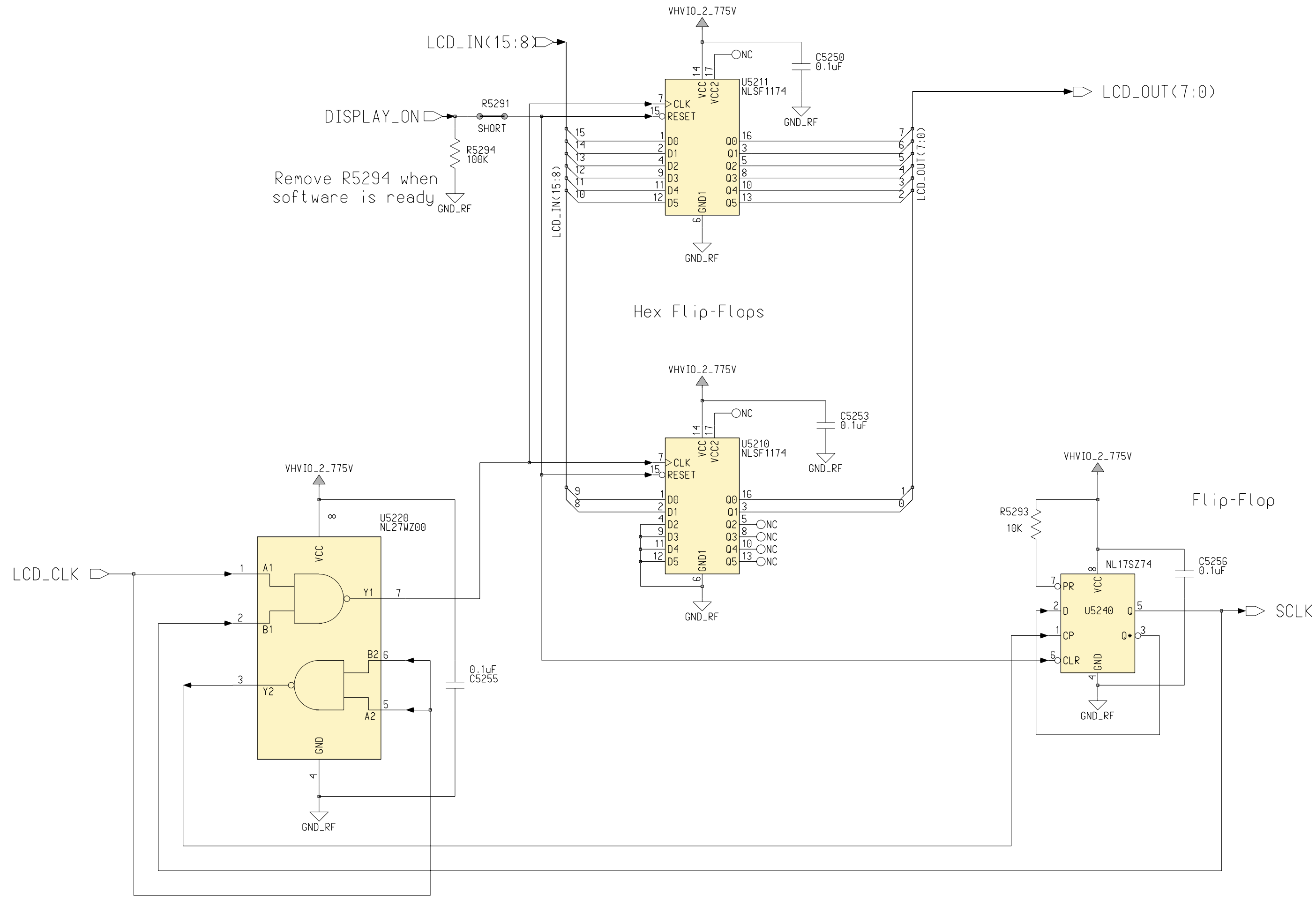
D



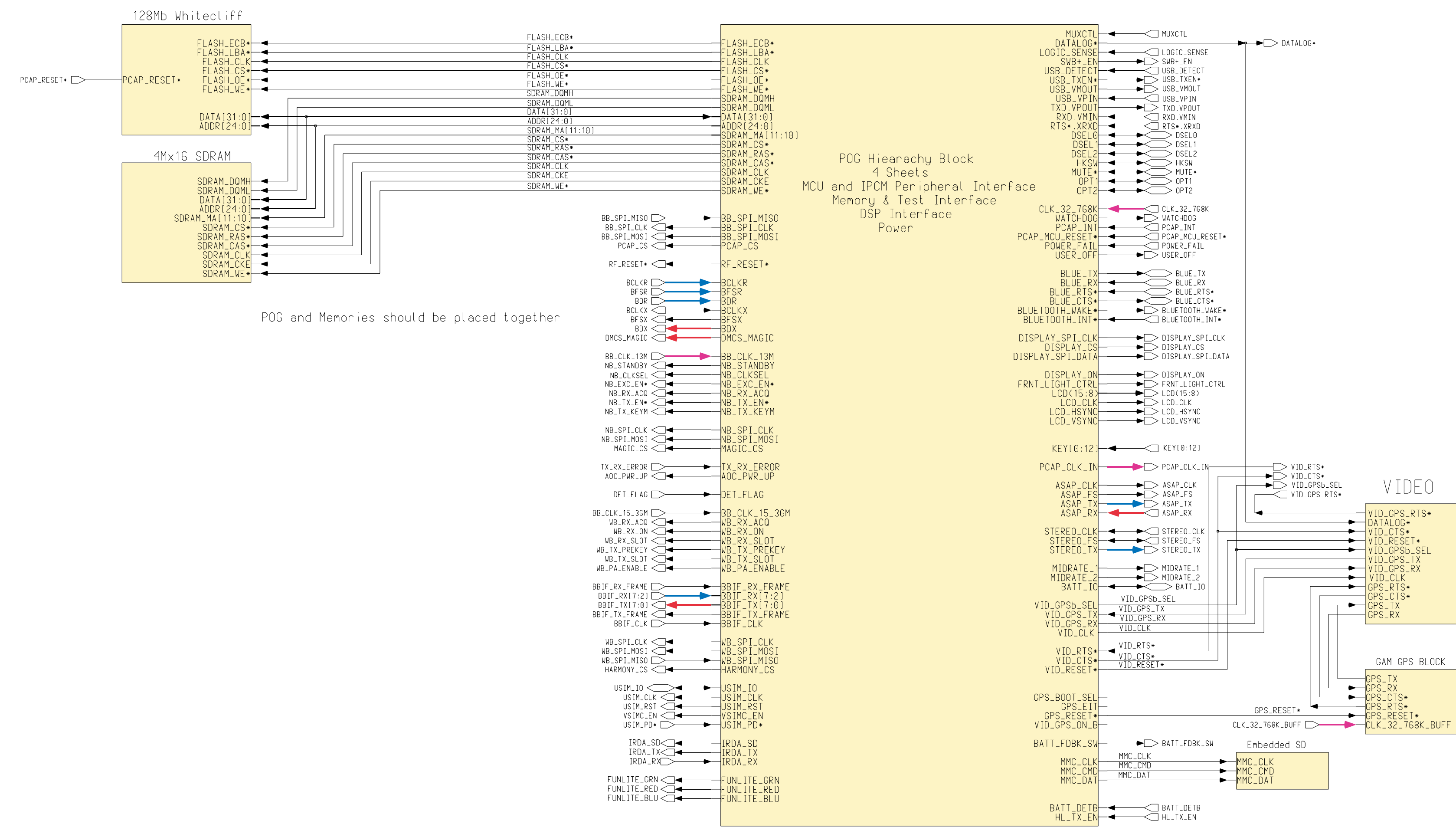
Display Bezel U-clips



Engineer: Mike Lanfalusì			
Drawn by: Mike Lanfalusì			
R&D CHK:	TITLE: 65K_LOGIC	Page: 1	Of: 1
DOC CTRL CHK:			
MFG CTRL CHK:			
QA CHK:	REV:	Drawing Number:	Time: 2:04:10 pm
Changed by: w16808	Date: Thursday, May 2, 2002		



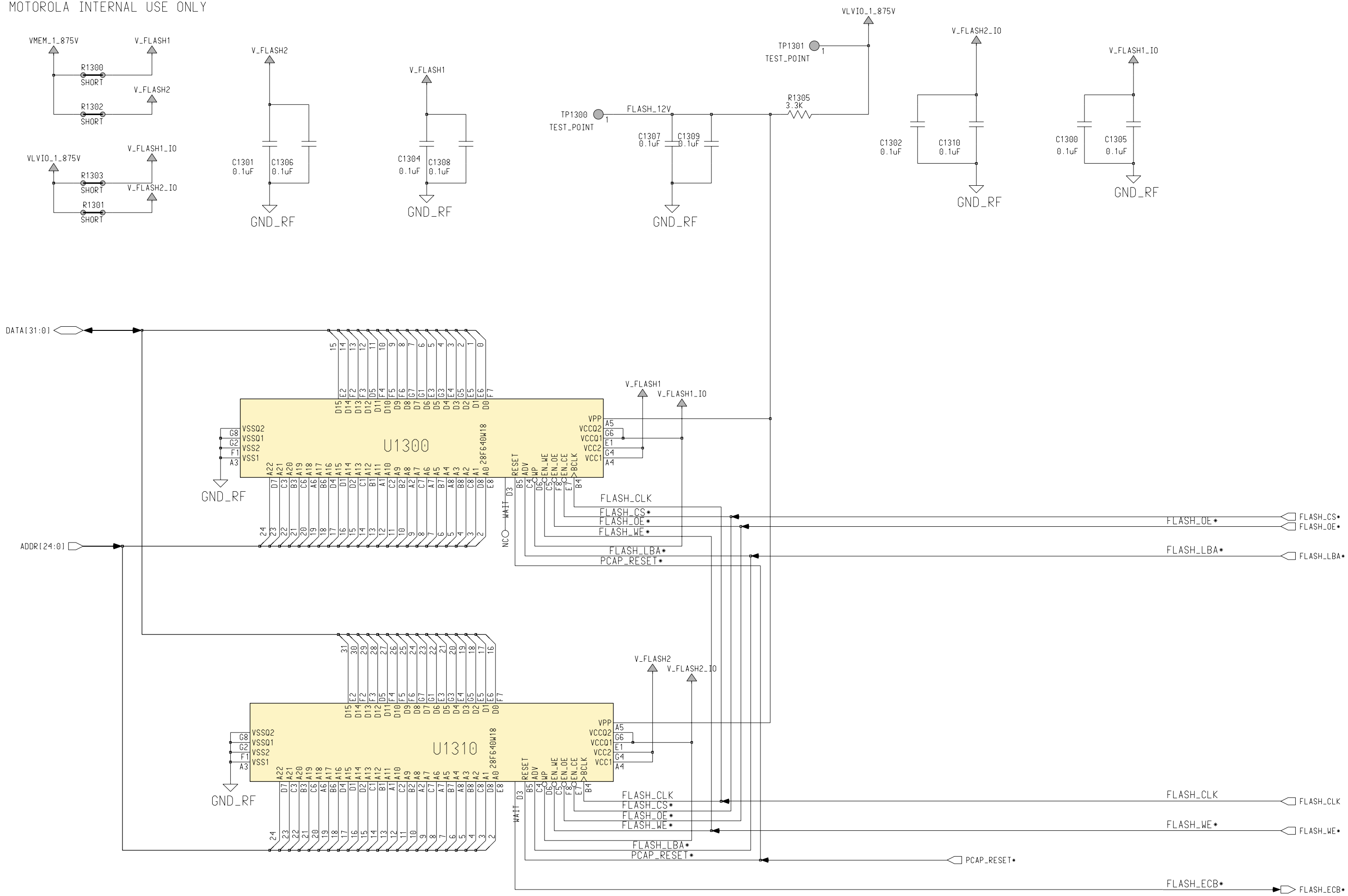
A
B
C
D



POG and Memories should be placed together

POG Hierarchy Block
4 Sheets
MCU and IPCM Peripheral Interface
Memory & Test Interface
DSP Interface
Power

MOTOROLA INTERNAL USE ONLY



4 1 3 A

Engineer: Ed Naddeo			
Drawn by: Ed Naddeo			
R&D CHK:	TITLE: Talon Integrated Flash Memory	Size: 11x21	
DOC CTRL CHK:			
MFG CTRL CHK:			
QA CHK:	REV: 0.1	Drawing Number: Page 1	Of: 1
Changed by: Chris Pipe	Date: Monday, May 6, 2002	Time: 9:32:29 am	

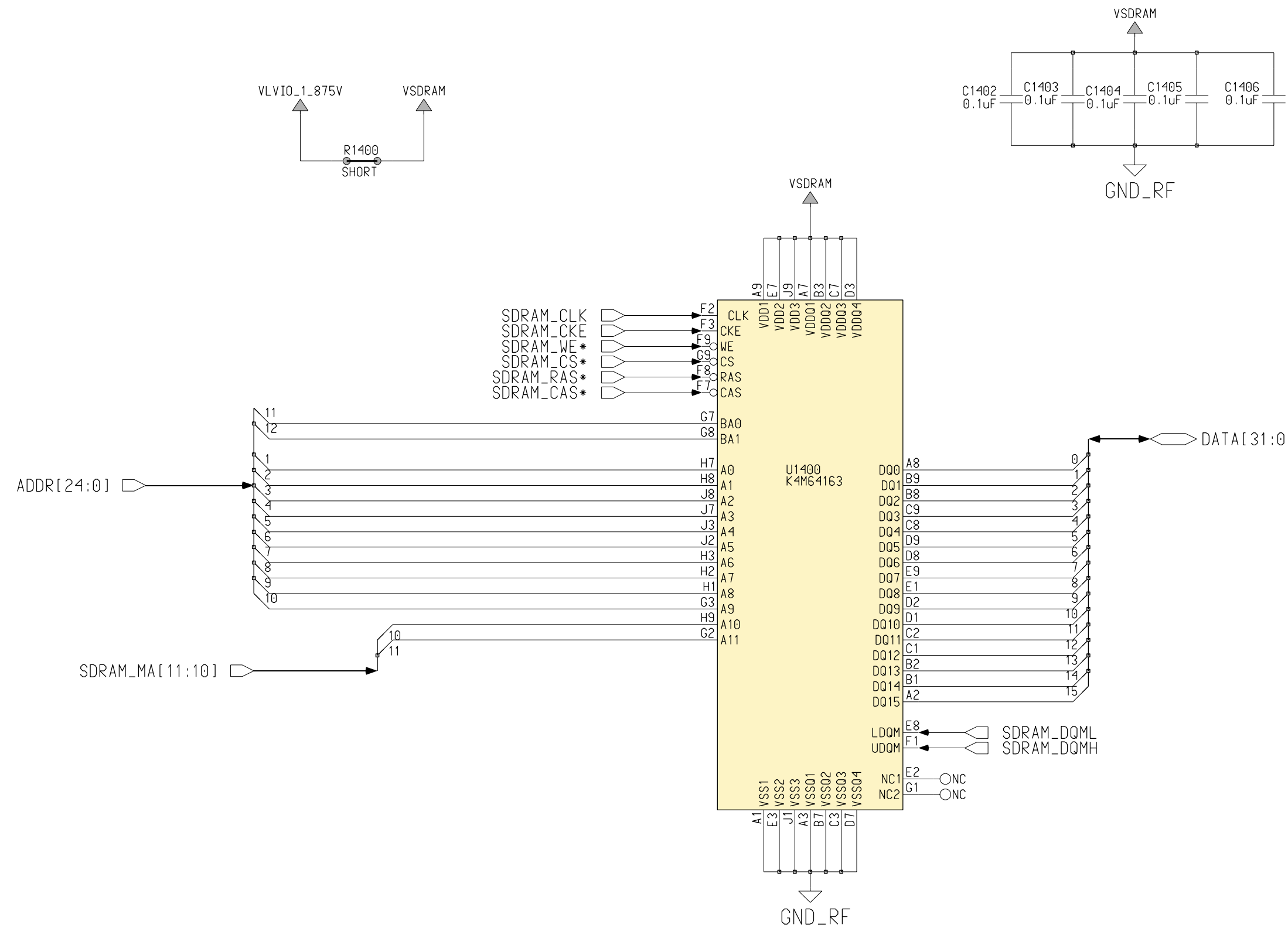
Engineer:
Ed Naddeo
 Drawn by:
Ed Naddeo
 R&D CHK:
 DOC CTRL CHK:
 MFG CTRL CHK:
 QA CHK:

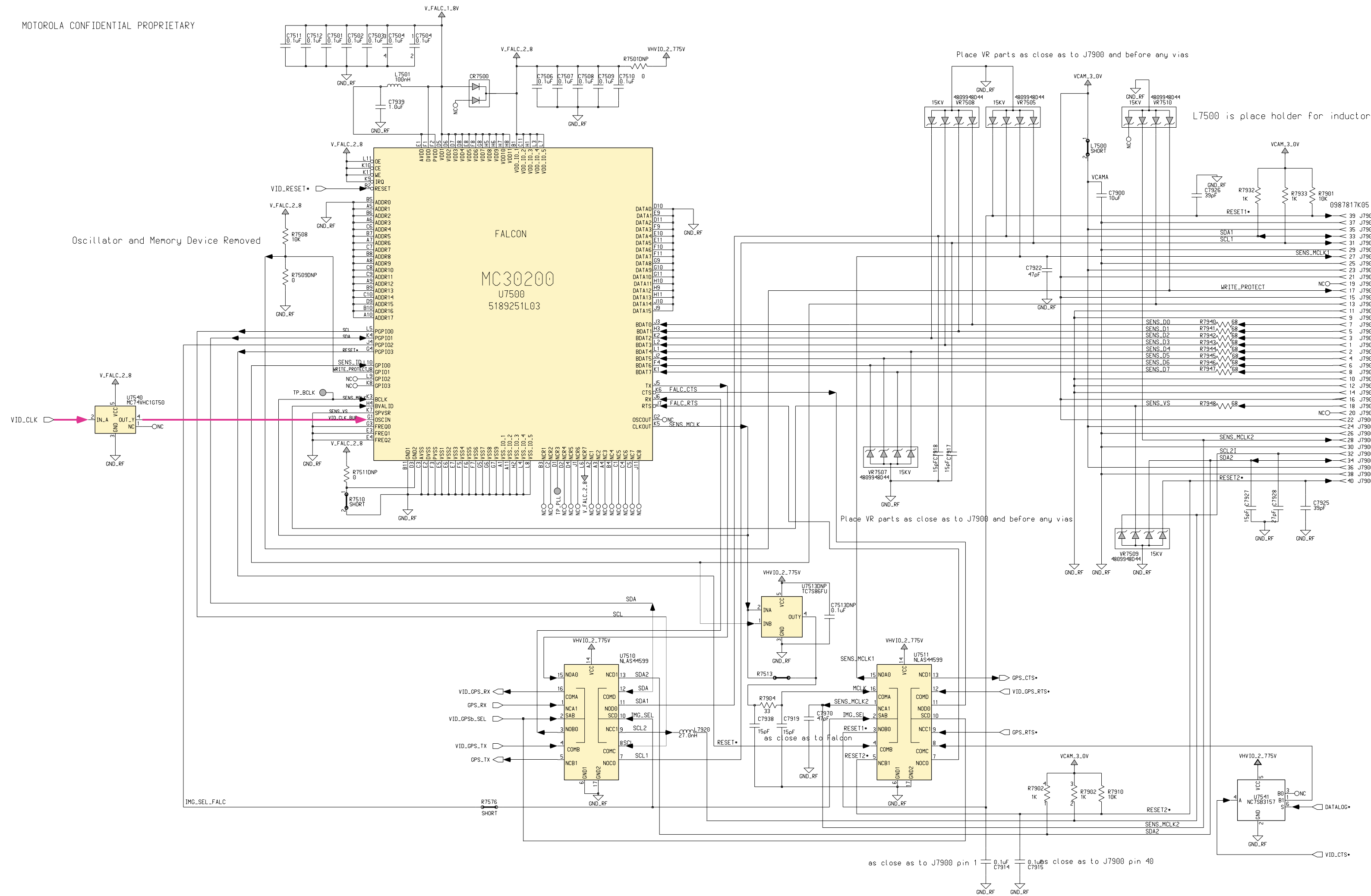


TITLE: Talon Integrated
4Mx16 SDRAM
 Size: 11x17

REV: 1.0 Drawing Number: 8488888x88 Page: 1 Of: 1

Changed by: wlen01 Date: Friday, March 1, 2002 Time: 3:37:06 pm





Sensor Connector
Molex 54722-0308

as close as to J7900 pin 1 0.1uF C7314 0.1uF C7315 as close as to J7900 pin 40

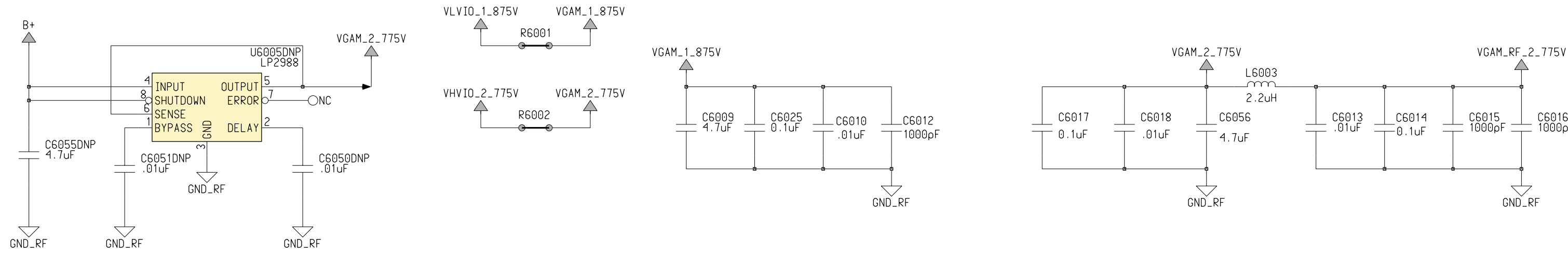
A
B
C
D
E

Engineer:
Danny Ng
 Drawn by:
Danny Ng
 R&D CHK:
 DOC CTRL CHK:
 MFG CTRL CHK:
 QA CHK:

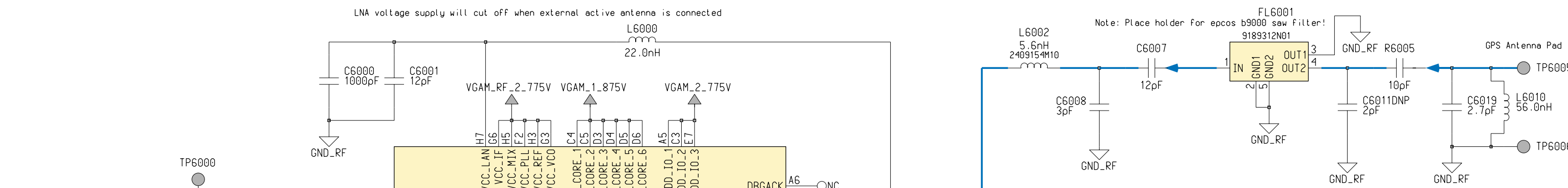


TITLE: GAM GPS BLOCK for E1000 Size: 11x17
 REV: 0.2 Drawing Number: Page: 1 Of: 1
 Date: Monday, August 18, 2003 Time: 3:17:11 pm

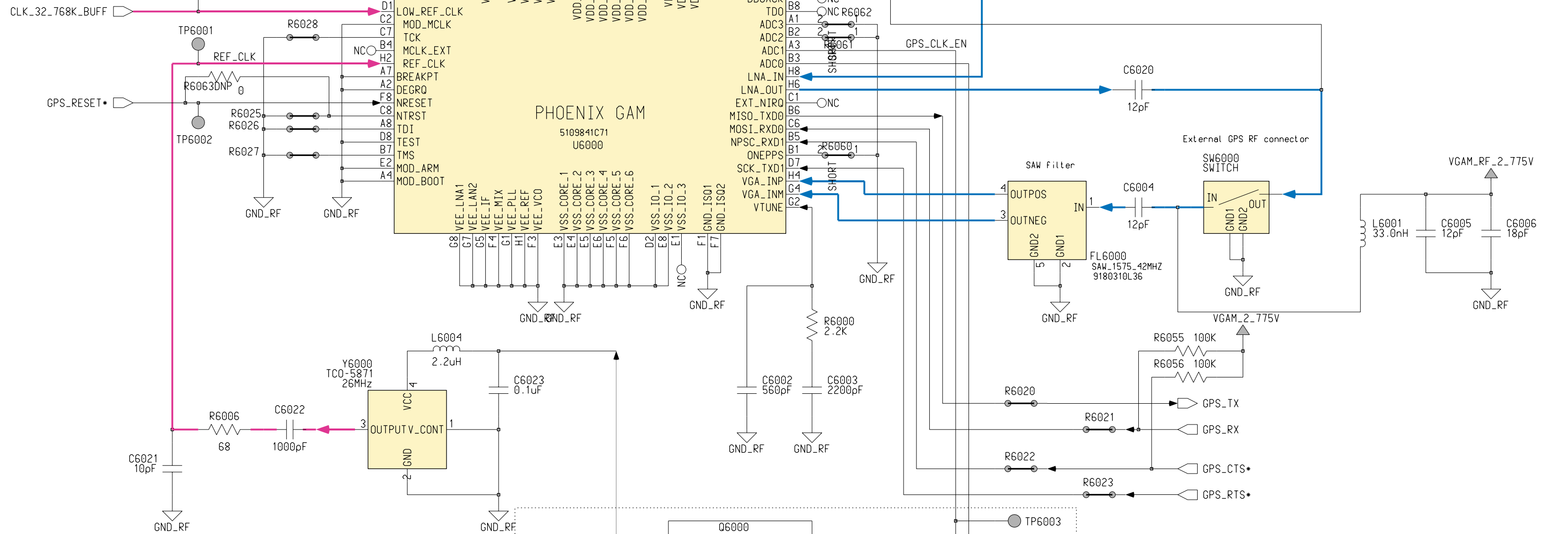
A



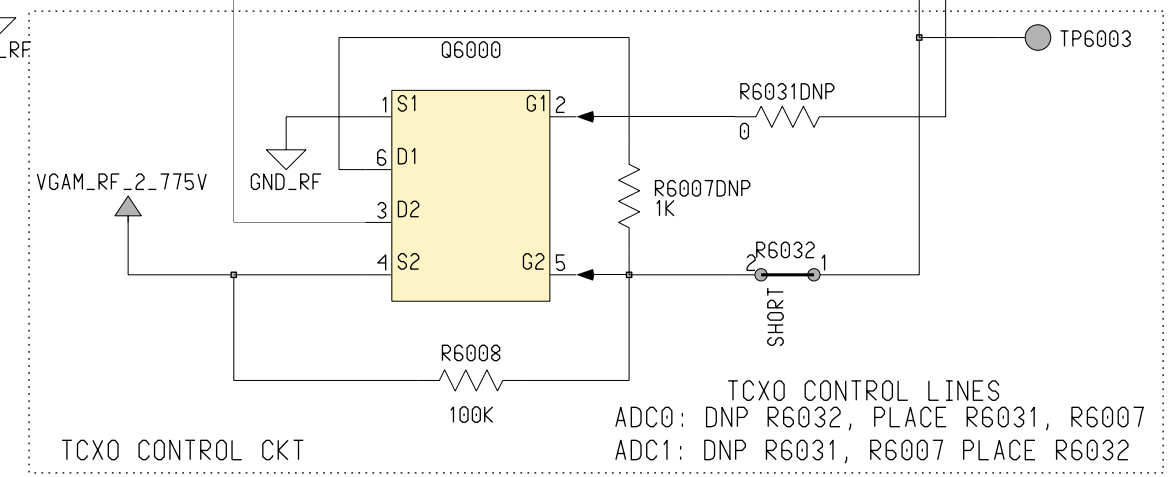
B



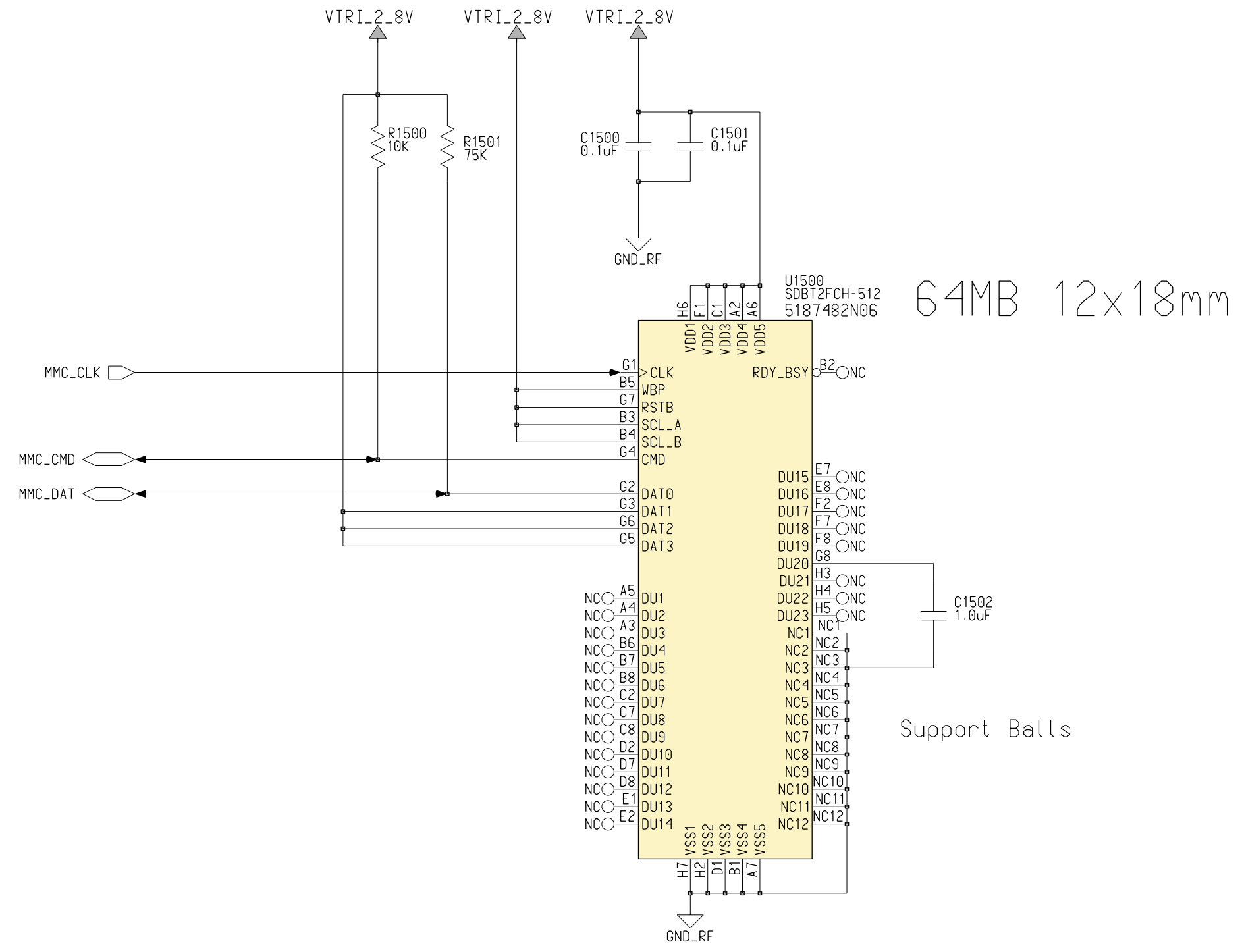
C



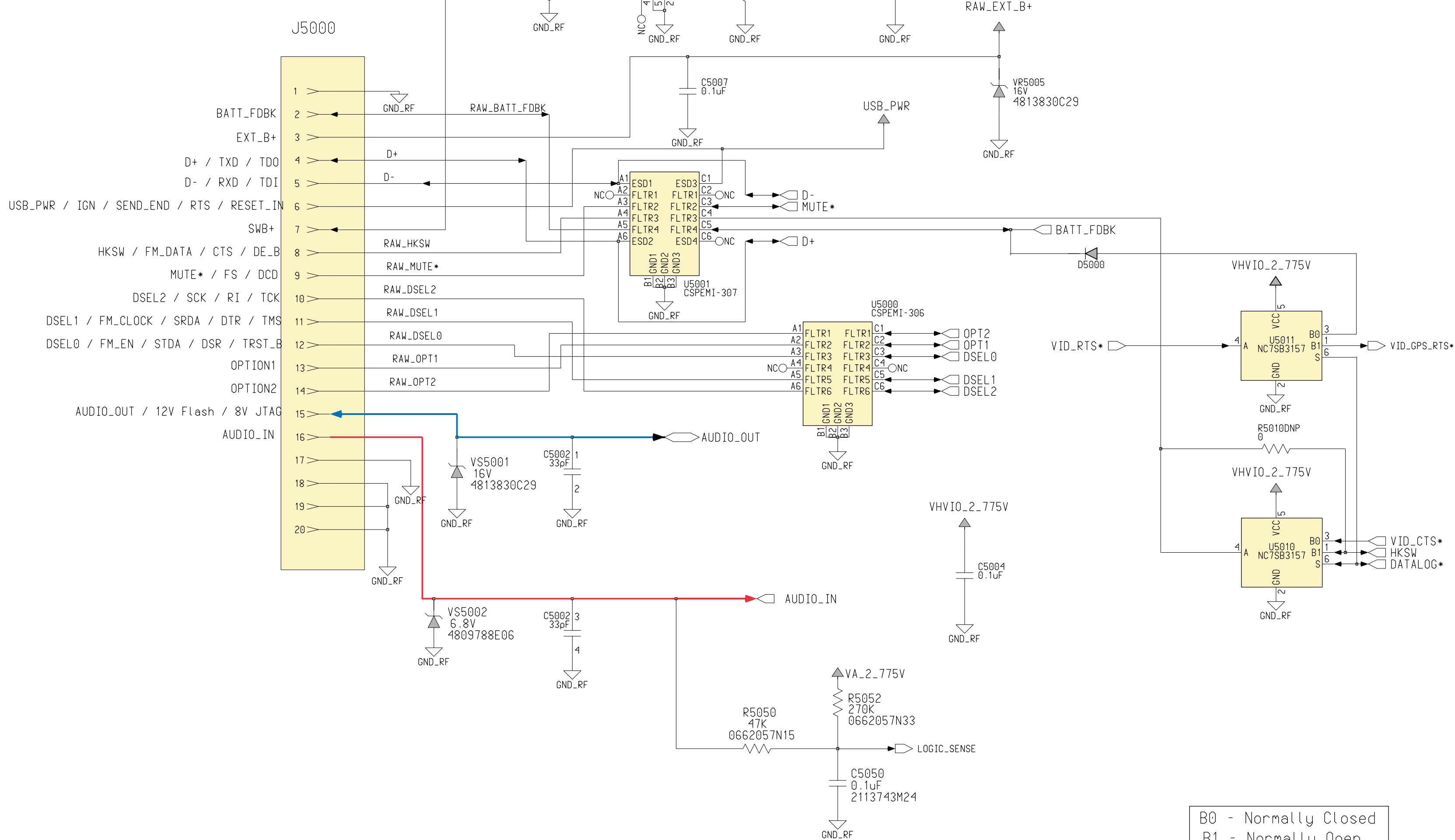
D



Engineer: Ed Naddeo			
Drawn by: Ed Naddeo			
R&D CHK:	TITLE: Talon Integrated	Size: 11x17	
DOC CTRL CHK:	Embedded SD Flash		
MFG CTRL CHK:	REV: 1.0	Drawing Number: 84888888x88	Page: 1
QA CHK:	Date: Friday, May 3, 2002	Time: 1:25:03 pm	
Changed by: Chris Pipe			



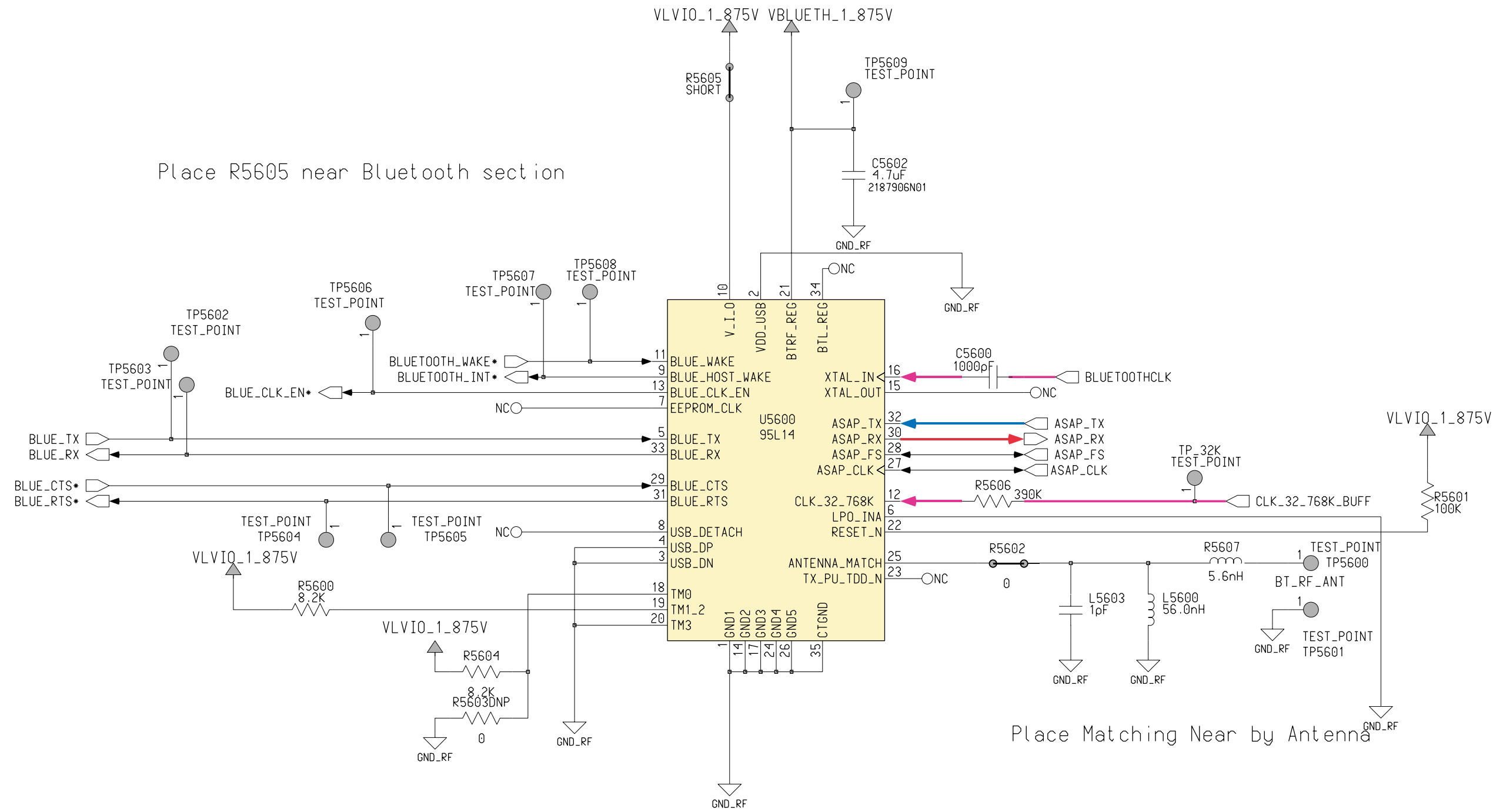
Accessory Connector CE Connector (0987636K05)



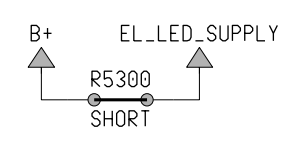
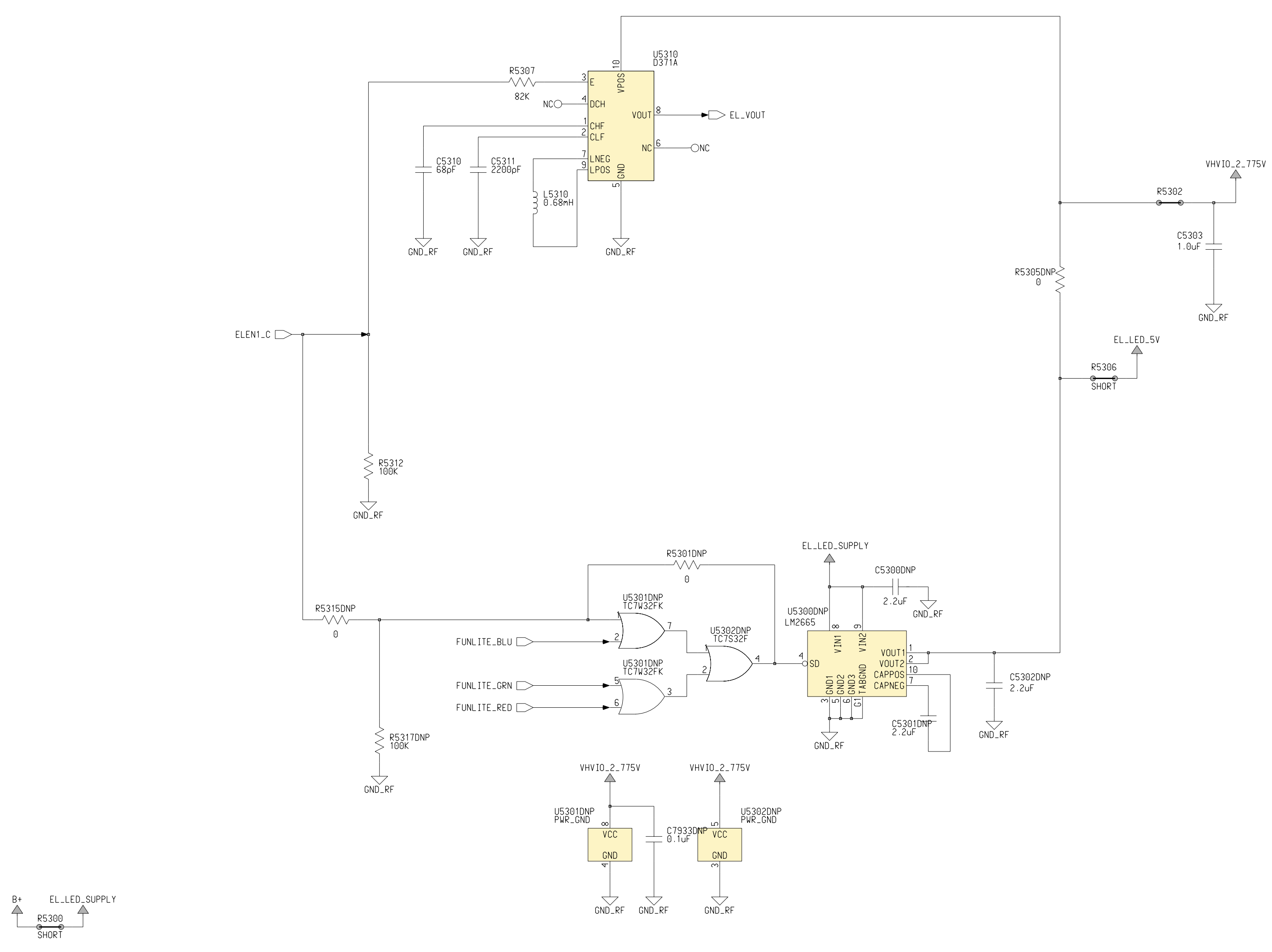
B0 - Normally Closed
 B1 - Normally Open
 S - Select
 A - Common

Engineer: Jennifer Seymour	MOTOROLA INC.		
Drawn by:			
R&D CHK:	TITLE: TALON Integrated	Size:	
DOC CTRL CHK:	CE Accessory Connector		
MFG CTRL CHK:	REV:	Drawing Number:	Page: OF:
QA CHK:	Date: 4/3/02	Time:	
Changed by: Ed Naddeo			

Engineer: Dave Stubbs	MOTOROLA INC.		
Drawn by:	TITLE:	Size:	
R&D CHK:	TALON Integrated Bluetooth		
DOC CTRL CHK:	REV:	Drawing Number:	Page:
MFG CTRL CHK:	QA CHK:	REV:	Of:
Changed by: Dave Suarez	Date: February 24, 2003	Time:	



Engineer:	MOTOROLA INC.		
Drawn by:			
R&D CHK:	TITLE:	Size:	
DOC CTRL CHK:	Funlites and EL		
MFG CTRL CHK:	REV:	Drawing Number:	Page:
QA CHK:			Of:
Changed by:	Date:	Time:	



MOTOROLA CONFIDENTIAL PROPRIETARY

4 1 3 A

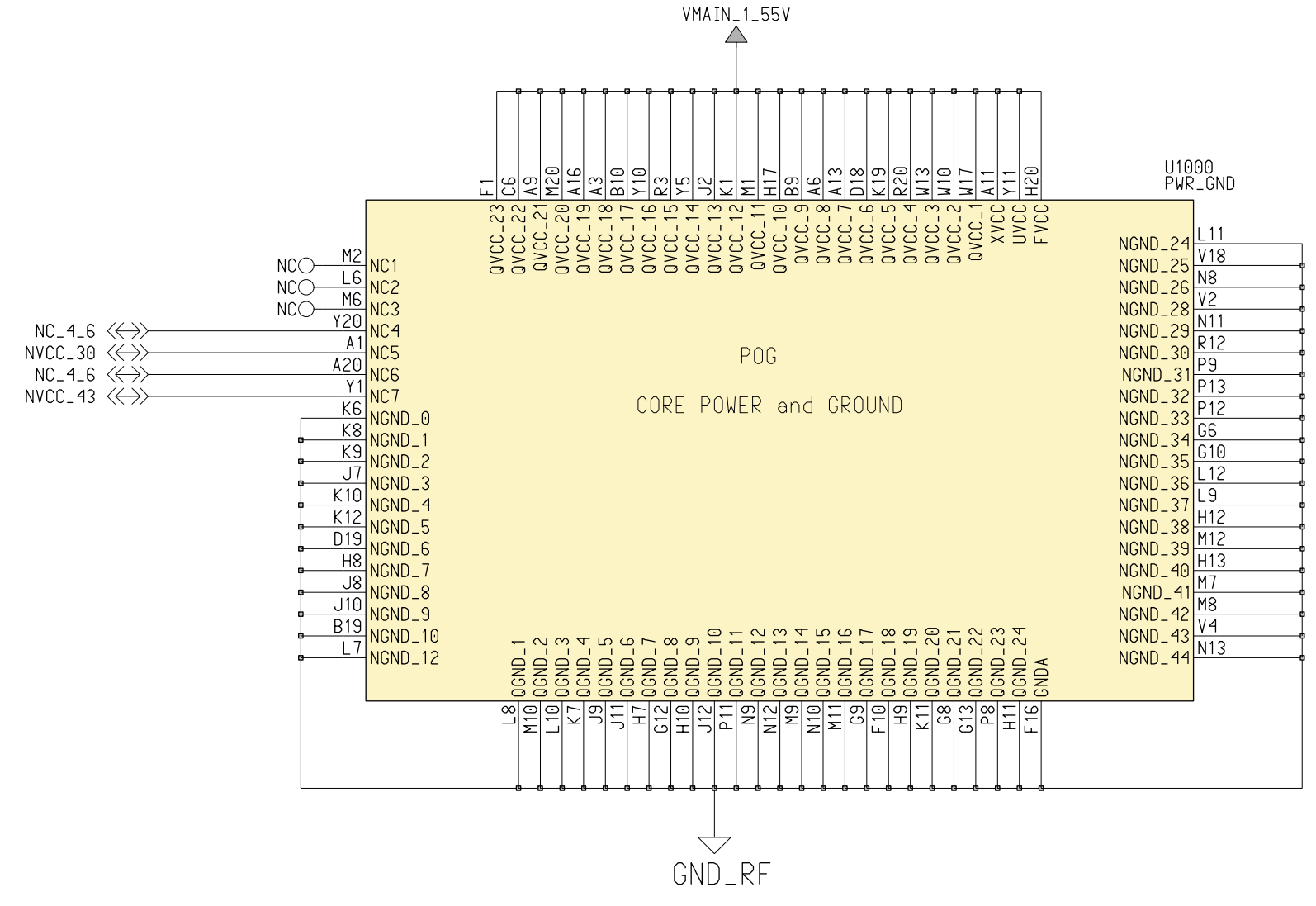
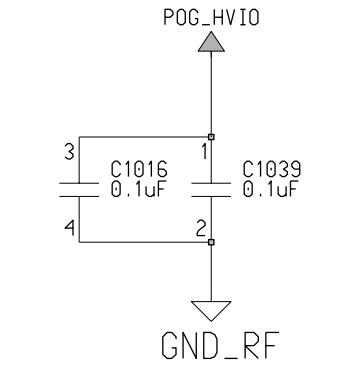
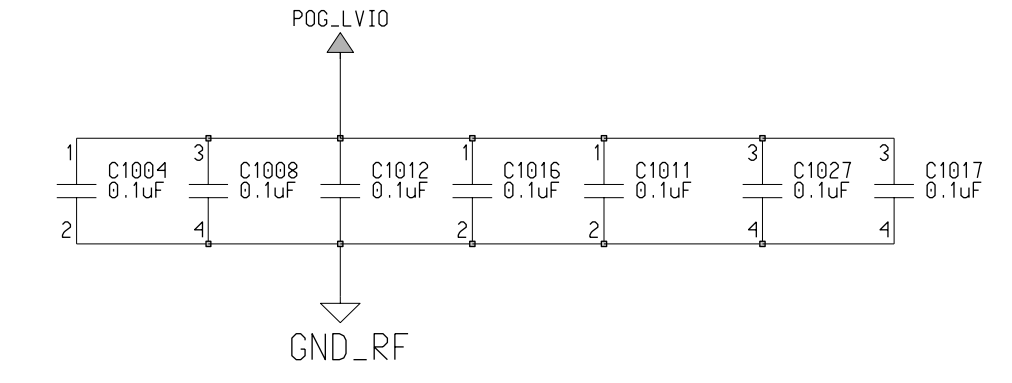
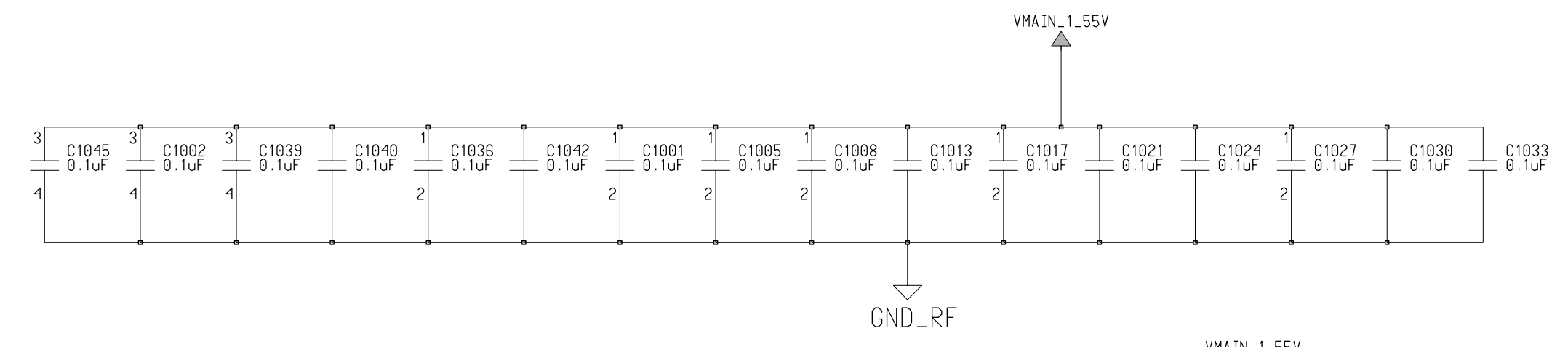
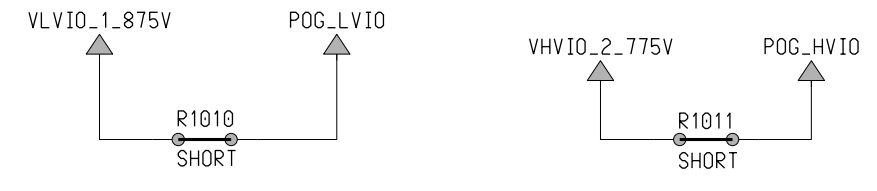
Engineer:
Ed Naddeo
Drawn by:
Ed Naddeo
R&D CHK:
DOC CTRL CHK:
MFG CTRL CHK:
QA CHK:



TITLE: Talon Integrated
POG
Power

REV: 1 Drawing Number: 8488888x88 Page: 1 Of: 4

Changed by: wlen01 Date: Tuesday, February 26, 2002 Time: 2:56:17 pm



Engineer:
Ed Naddeo
 Drawn by:
Ed Naddeo
 R&D CHK:
 DOC CTRL CHK:
 MFG CTRL CHK:
 QA CHK:

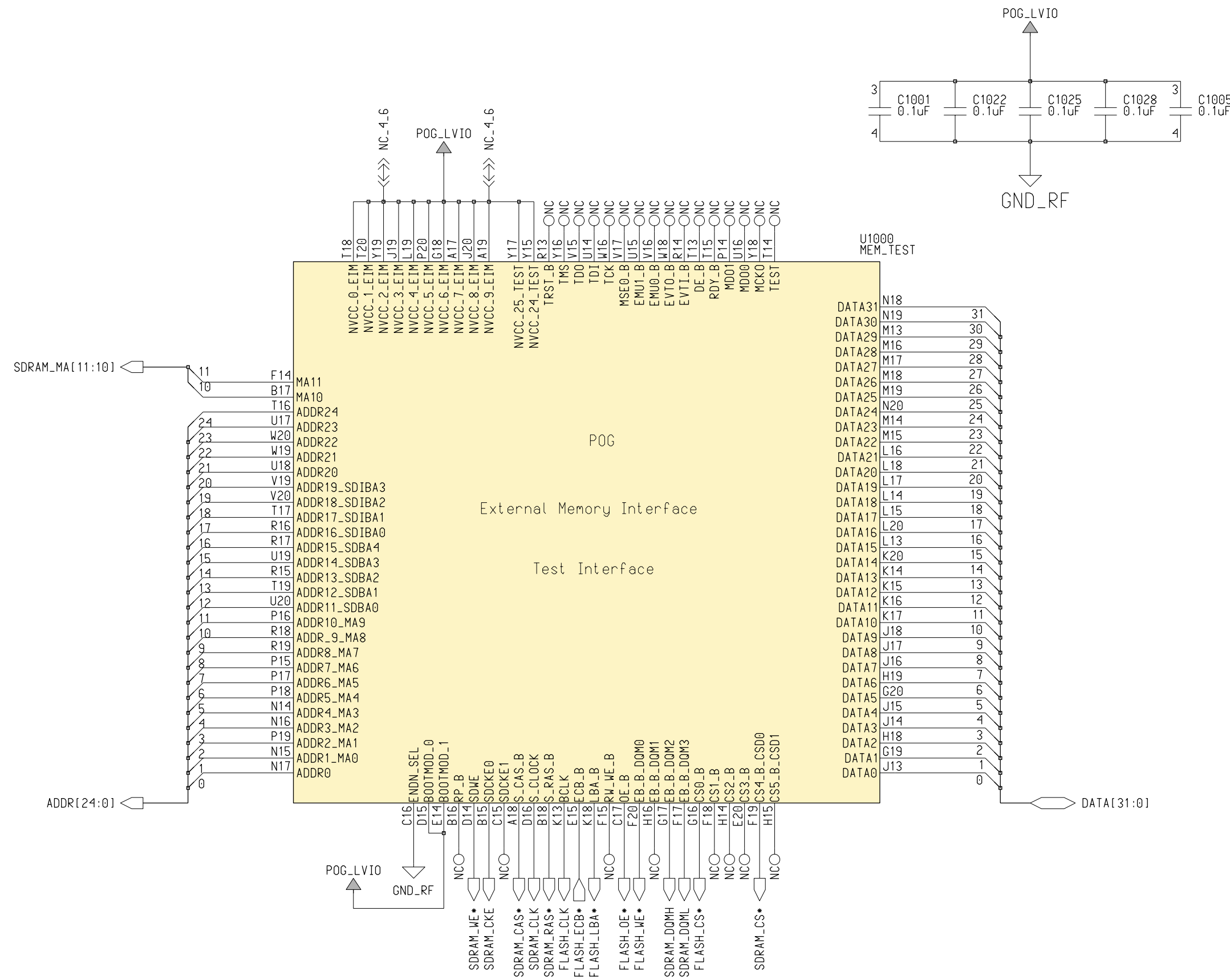


TITLE: Talon Integrated
POG
Memory and Test Interface

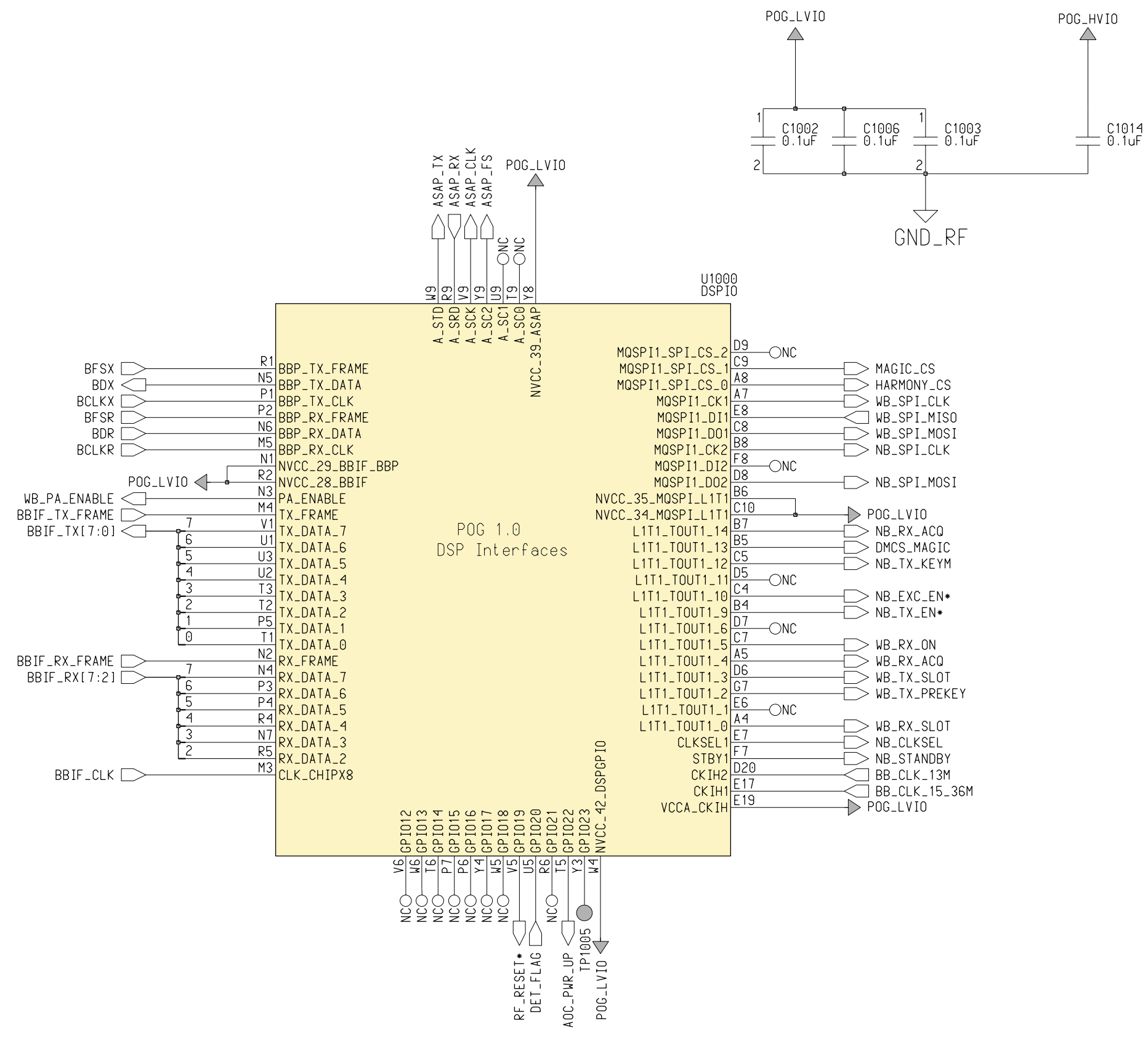
Size: 11x17

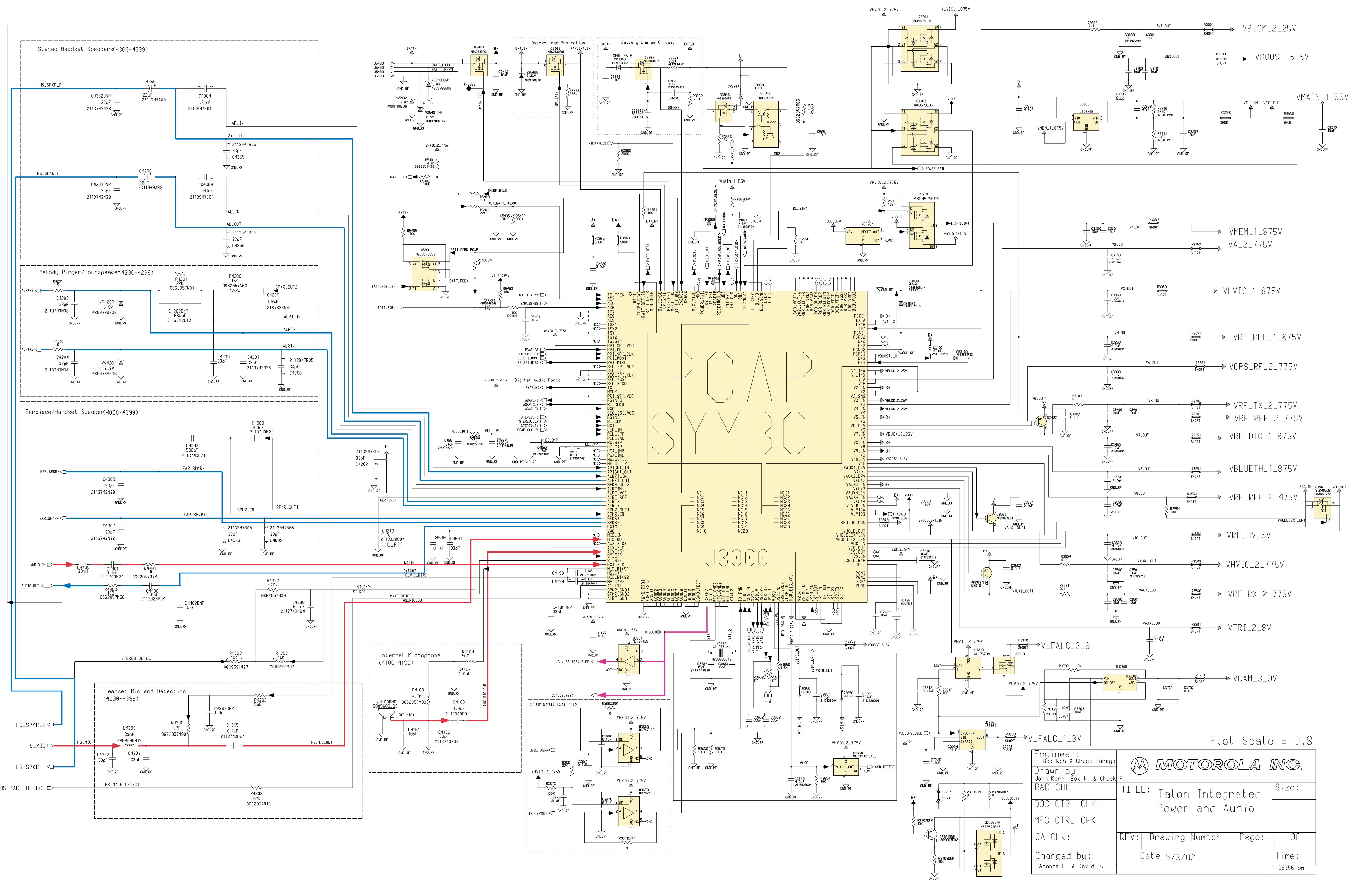
REV: 1.0 Drawing Number: 848888x88 Page: 3 Of: 4

Changed by: wlen01 Date: Tuesday, February 26, 2002 Time: 6:15:09 pm



Engineer: Ed Naddeo			
Drawn by: Ed Naddeo			
R&D CHK:	TITLE: Talon Integrated	Size: 11x17	
DOC CTRL CHK:	POG		
MFG CTRL CHK:	DSP Interface		
QA CHK:	REV: 1.0	Drawing Number: 848888x88	Page: 4
Changed by: wlen01	Date: Tuesday, February 26, 2002	Time: 6:16:44 pm	





Engineer: Bok Koh & Chuck Farago	MOTOROLA INC.		
Drawn by: John Kerr, Bok K. & Chuck F.			
R&D CHK:	TITLE: Talon Integrated Power and Audio	Size:	
DOC CTRL CHK:	REV: Drawing Number:	Page:	Of:
MFG CTRL CHK:	Date: 5/3/02	Time:	
QA CHK:			
Changed by: Amanda H. & David D.			1:36:56 pm