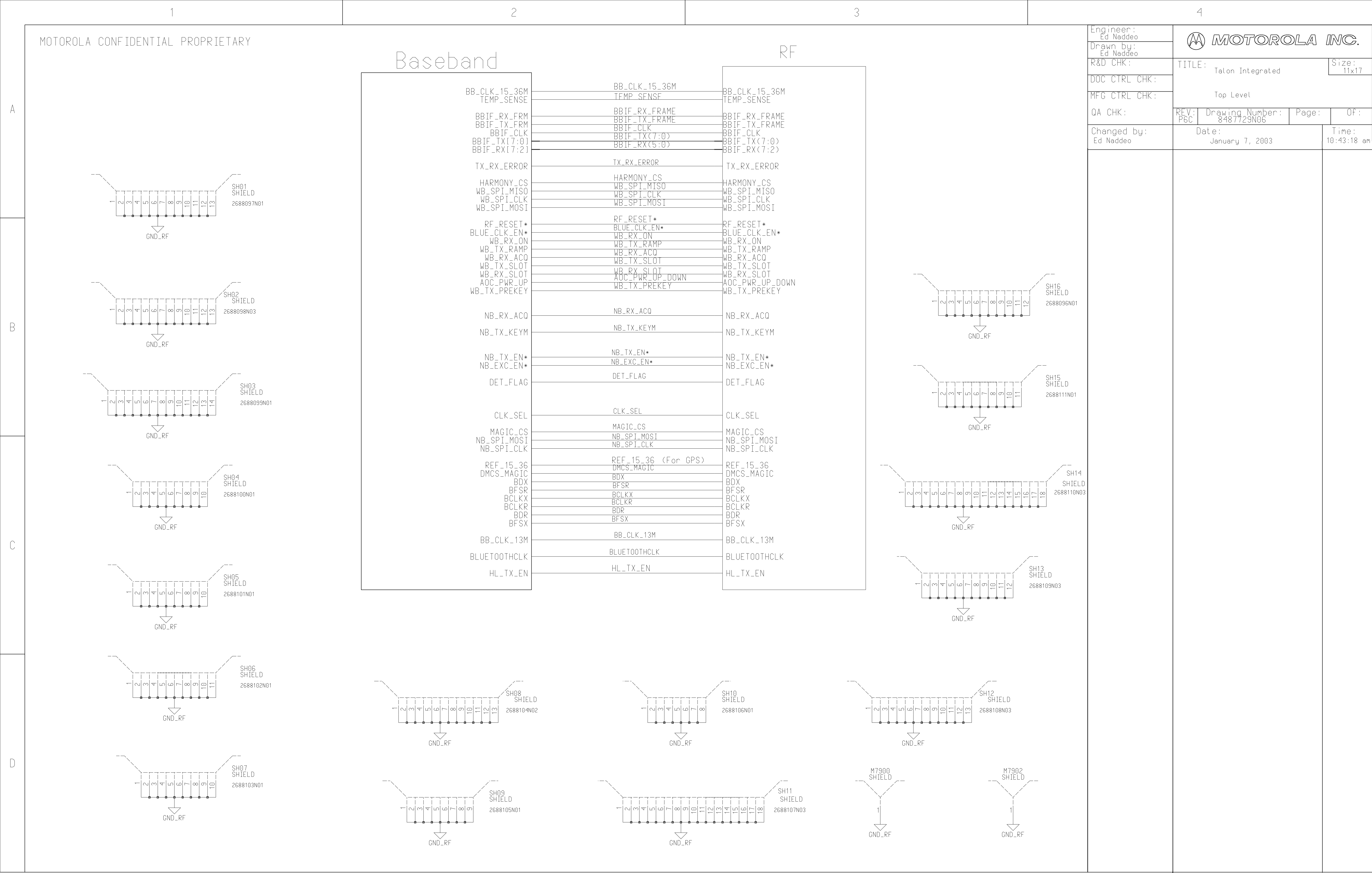




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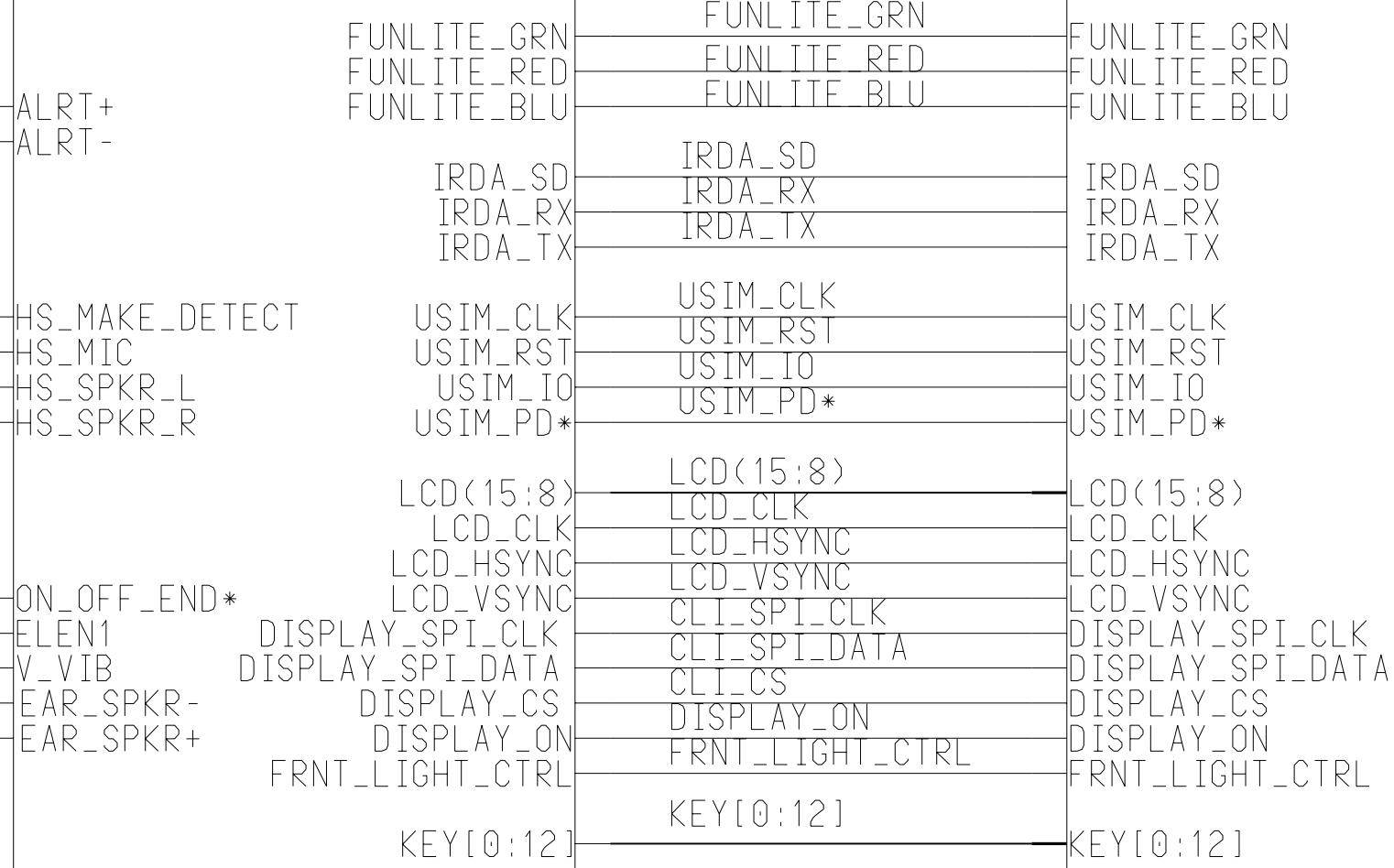
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Misc Connectors

Display Connector USIM
Keypad Connector IrDA
Headset Jack Vibrator
Fun Lights



CORE LOGIC BLOCK

POG
Memories
Video
ACPS
Embedded SD

PCAP_CLK_IN

VID_RTS*
LOGIC_SENSE
VID_CTS*

SWB+_EN
HKSW
MUTE*
DSEL2
DSEL1
DSEL0
OPT1
OPT2
DATALOG*

BLUETOOTH_WAKE*
BLUETOOTH_INT*
BLUE_TX
BLUE_CTS*
BLUE_RTS*
BLUE_RX

WB_SPI_CLK
WB_SPI_MOSI
WB_SPI_MISO
HARMONY_CS

CLK_32_768K_BUFF

ASAP_FS
ASAP_CLK
ASAP_RX
ASAP_TX
STEREO_FS
STEREO_CLK
STEREO_TX
VSIMC_EN

MUXCTL
BATT_IO
RXD.VMIN
TXD.VPOUT
RTS*.XRXD
USB_TXEN*
USB_VMOUT
USB_VPIN
USB_DETECT
BB_SPI_CLK
BB_SPI_MISO
BB_SPI_MOSI
PCAP_CS

PCAP_INT
PCAP_RESET*
USER_OFF
POWER_FAIL
PCAP_MCU_RESET*
WATCHDOG

CLK_32_768K
NB_STANDBY
VID_GPSb_SEL
MIDRATE_1
MIDRATE_2
NB_TX_KEYM
BATT_FDBK_SW
BATT_DETB

REF_15_36

NB_TX_KEYM

EAR_SPKR+
EAR_SPKR-
V_VIB
ELEN1
ON_OFF_END*
HS_SPKR_R
HS_SPKR_L
HS_MIC
HS_MAKE_DETECT

ALRT-
ALRT+

MOTOROLA CONFIDENTIAL PROPRIETARY

CE Bus Connector

VID_RTS*
LOGIC_SENSE
VID_CTS*

SWB+_EN
HKSW
MUTE*
DSEL2
DSEL1
DSEL0
OPT1
OPT2
DATALOG*

BATT_FDBK
D+
D-
AUDIO_IN
AUDIO_OUT

Bluetooth

BLUETOOTH_WAKE*
BLUETOOTH_INT*
BLUE_TX
BLUE_CTS*
BLUE_RTS*
BLUE_RX
ASAP_FS
ASAP_CLK
ASAP_RX
ASAP_TX

PCAP (Power/Audio)

ASAP_FS
ASAP_CLK
ASAP_RX
ASAP_TX
STEREO_FS
STEREO_CLK
STEREO_TX
VSIMC_EN

MUXCTL
BATT_IO
RXD.VMIN
TXD.VPOUT
RTS*.XRXD
USB_TXEN*
USB_VMOUT
USB_VPIN
USB_DETECT
BB_SPI_CLK
BB_SPI_MISO
BB_SPI_MOSI
PCAP_CS

PCAP_INT
PCAP_RESET*
USER_OFF
POWER_FAIL
PCAP_MCU_RESET*
WATCHDOG

CLK_32_768K
NB_STANDBY
VID_GPSb_SEL
MIDRATE_1
MIDRATE_2
NB_TX_KEYM
BATT_FDBK_SW
BATT_DETB

BATT_FDBK
D+
D-
AUDIO_IN
AUDIO_OUT

BATT_FDBK
D+
D-
AUDIO_IN
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BATT_FDBK
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AUDIO_IN
AUDIO_OUT

Engineer:
John S. Kerr
Drawn by:
Todd Roester
R&D CHK:
DOC CTRL CHK:
MFG CTRL CHK:
QA CHK:

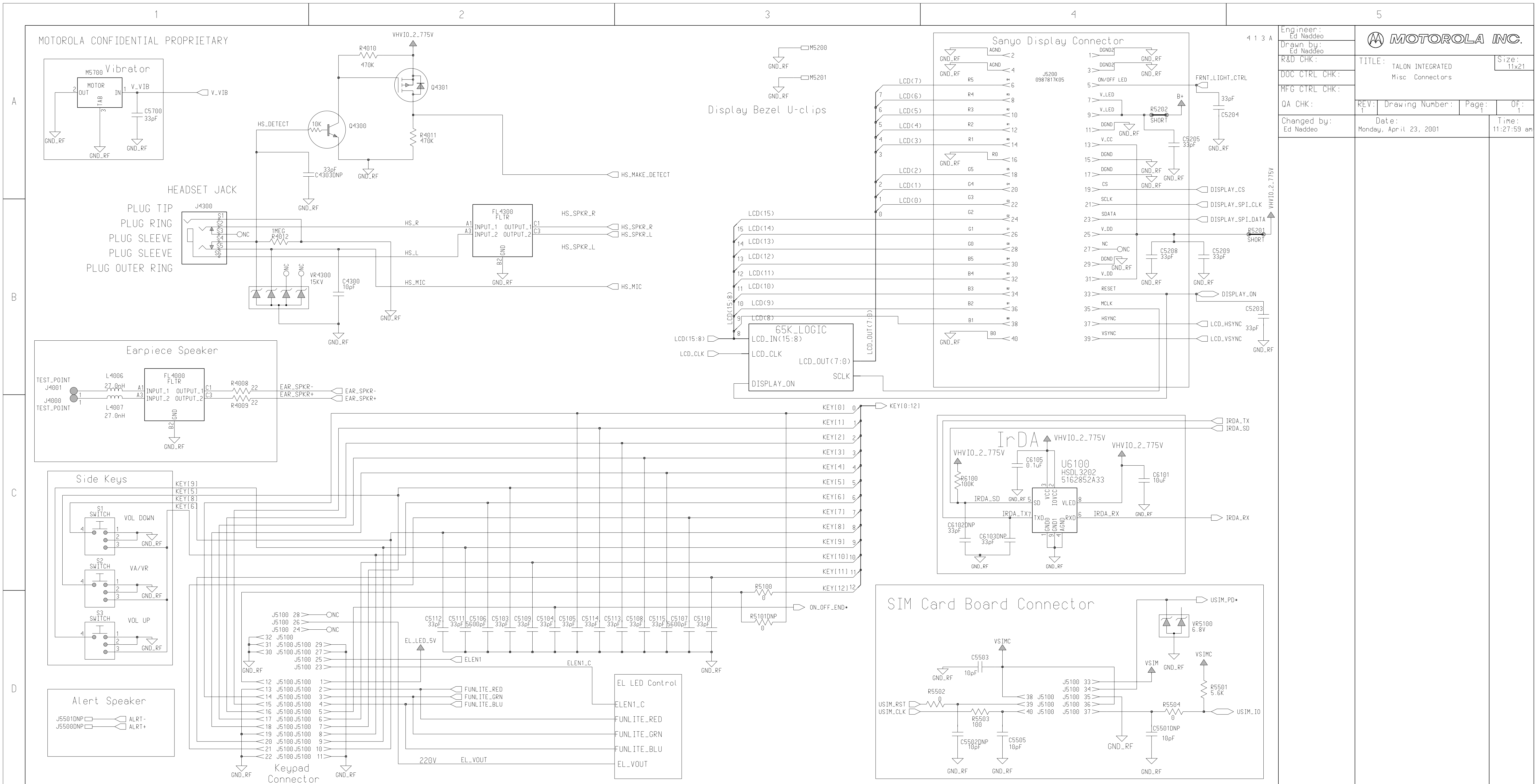


TITLE: Talon Integrated
Baseband Top Level

Size:
11x17

REV: Drawing Number: Page: Of:

Changed by:
Ed Naddeo
Date:
Monday, March 4, 2002
Time:
10:39:56 am



MOTOROLA CONFIDENTIAL PROPRIETARY

Engineer :

Drawn by:

R&D CHK:

DOC CTRL CHK:

MFG CTRL CHK:

QA CHK:

 **MOTOROLA INC.**

TITLE:	Size:
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Funlites and EL

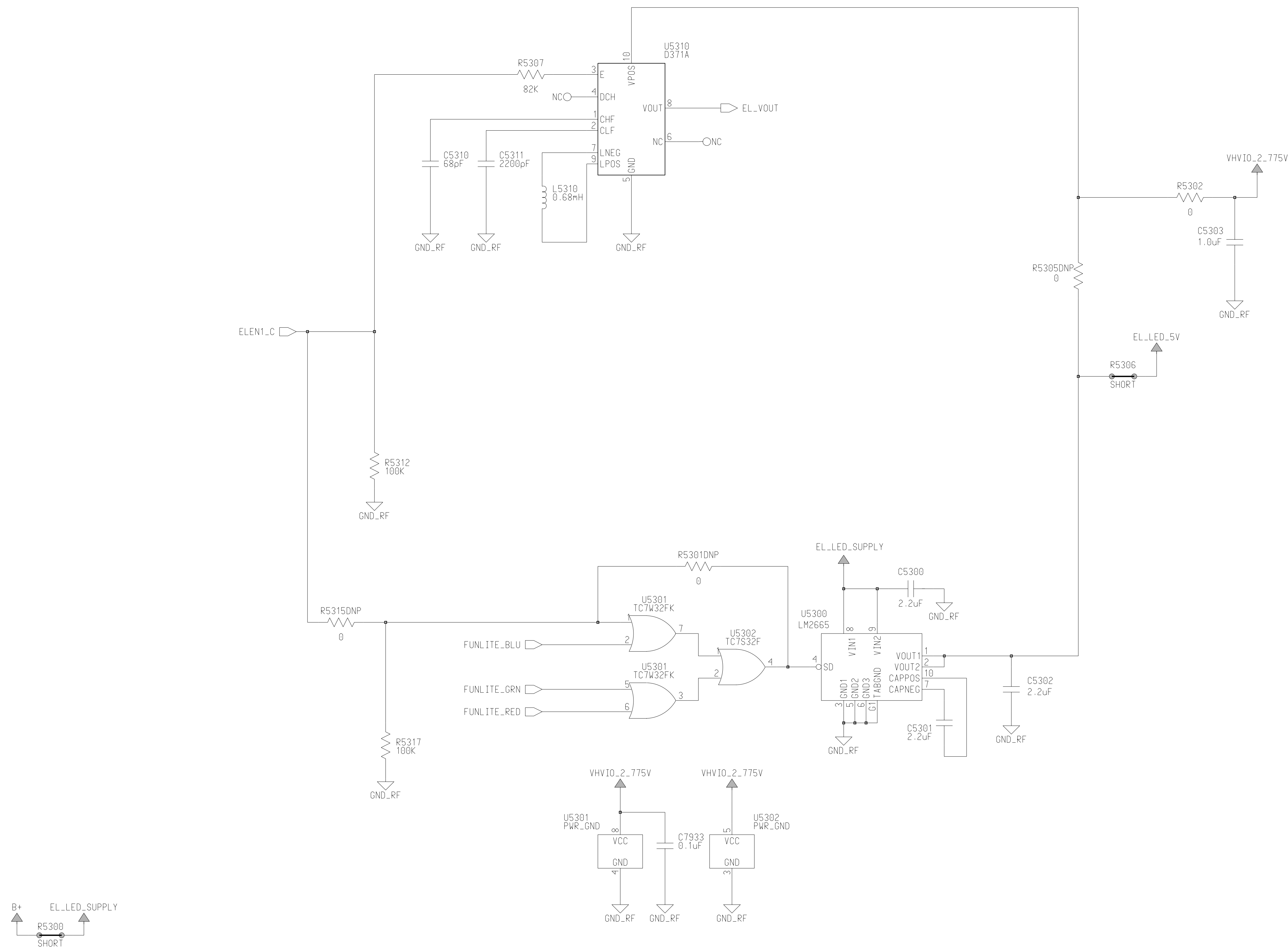
REV:	Drawing Number:	Page:	Of:
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Changed by:

Date:	Time:
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Date:	Time:
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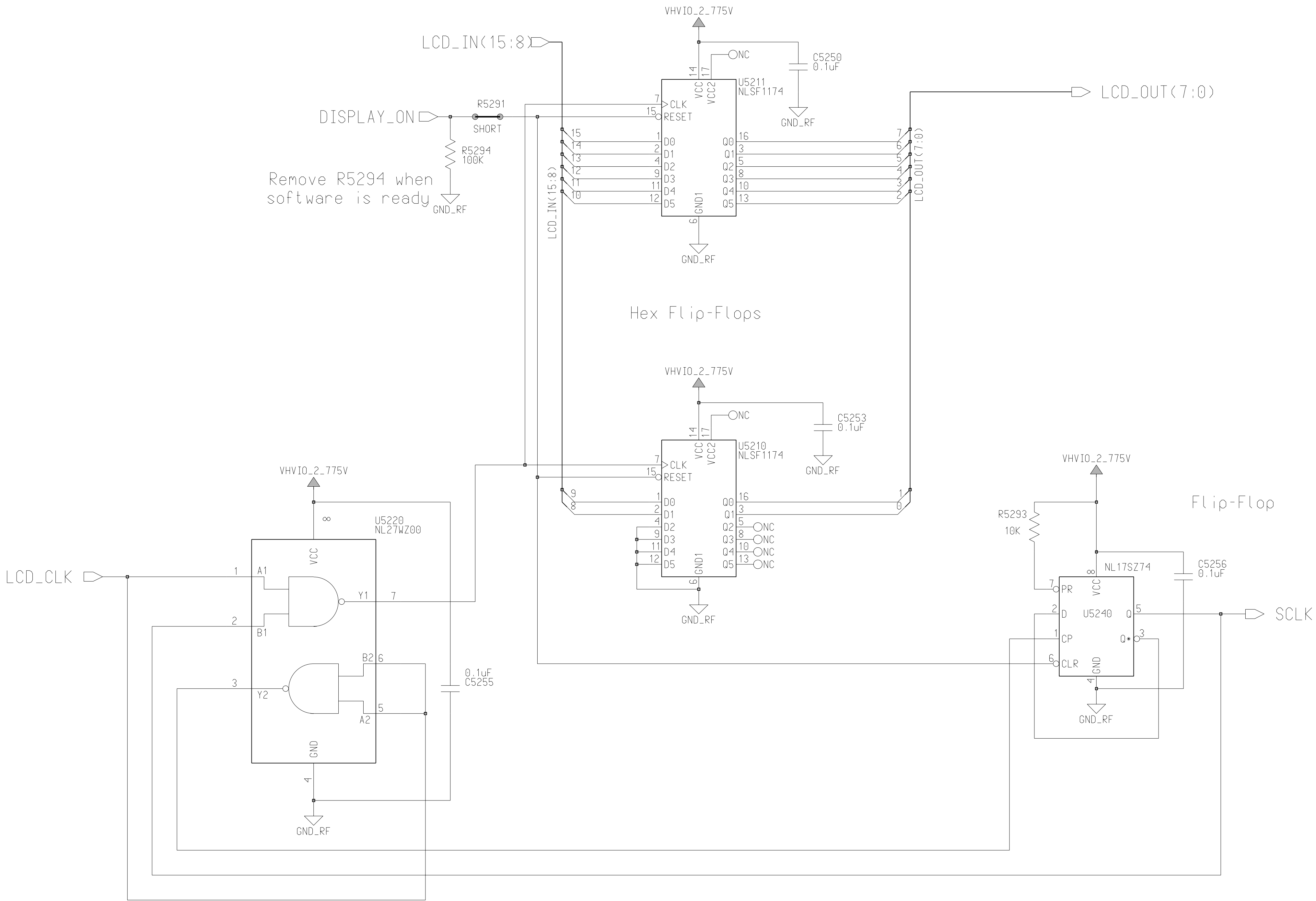
Date:	Time:
-------	-------

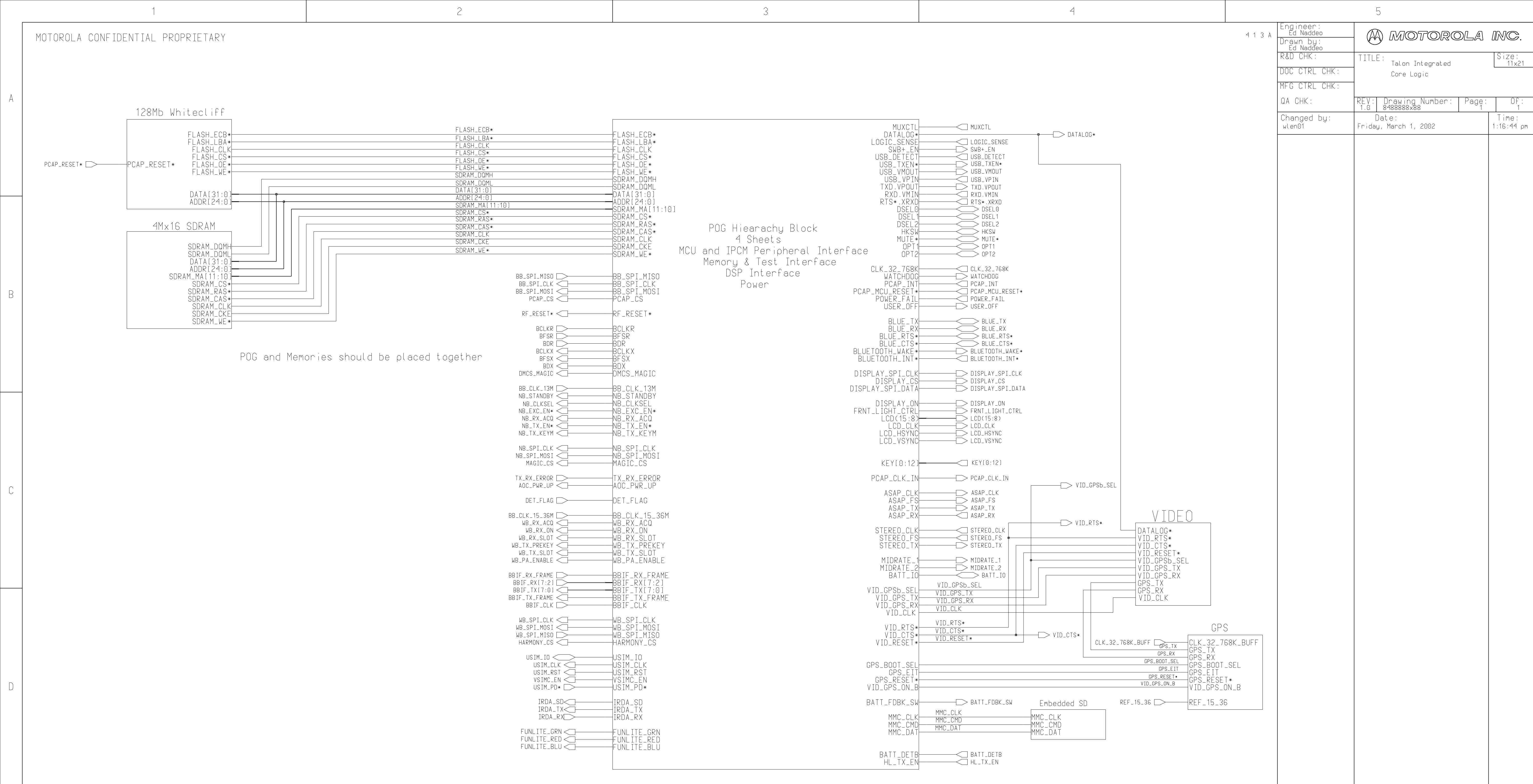


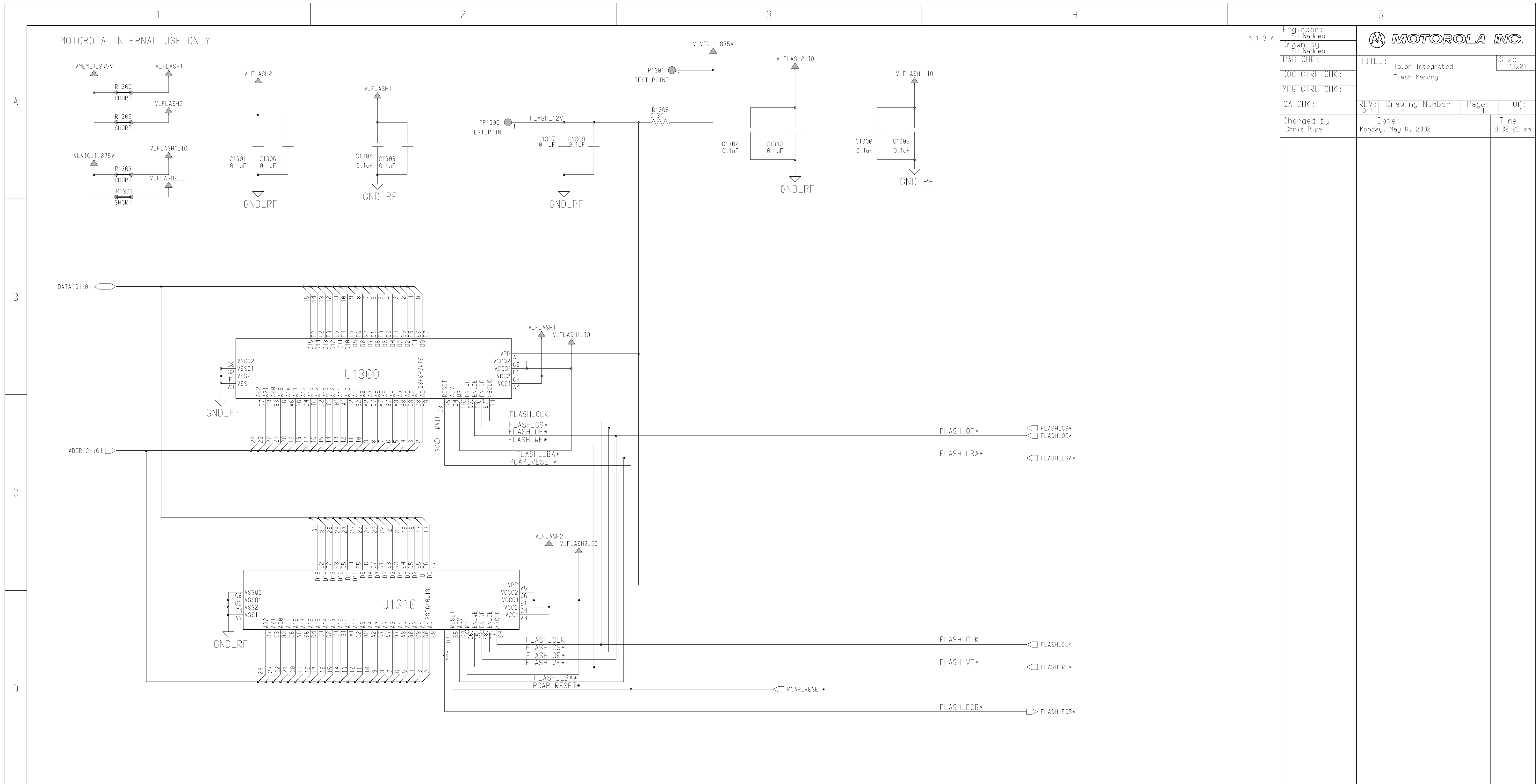
MOTOROLA CONFIDENTIAL PROPRIETARY

4 1 3 A

Engineer: Mike Lanfalus	 MOTOROLA INC.		
Drawn by: Mike Lanfalus			
R&D CHK:	TITLE: 65K_LOGIC		Size: 11x17
DOC CTRL CHK:			
MFG CTRL CHK:			
QA CHK:	REV:	Drawing Number:	Page: 1
Changed by: w16808	Date: Thursday, May 2, 2002		Time: 2:04:10 pm



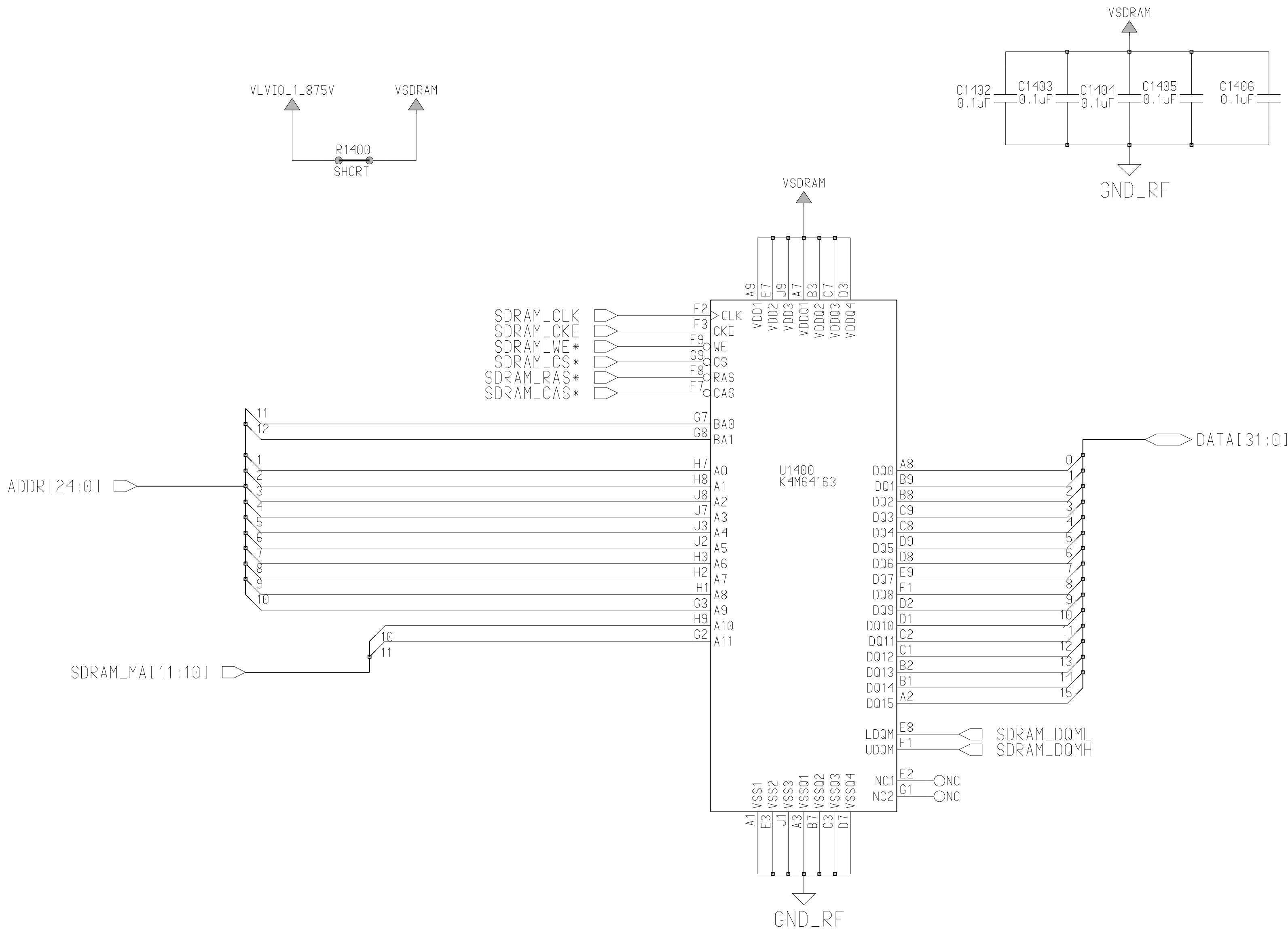




MOTOROLA CONFIDENTIAL PROPRIETARY

4 1 3 A

Engineer: Ed Naddeo	 MOTOROLA INC. TITLE: Talon Integrated 4Mx16 SDRAM Size: 11x17		
Drawn by: Ed Naddeo			
R&D CHK:			
DOC CTRL CHK:			
MFG CTRL CHK:			
QA CHK:	REV: 1.0	Drawing Number: 84888888x88	Page: 1
Changed by: wlen01	Date: Friday, March 1, 2002		Time: 3:37:06 pm



MOTOROLA CONFIDENTIAL PROPRIETARY

4 1 3 A

Engineer :

Ed Naddeo
Drawn by: Ed Naddeo

R&D CHK:

DOC CTRL CHK:

MEG CTR1 CHK:

QA CLK :

QA CHK:

Changed by:

 **MOTOROLA INC.**

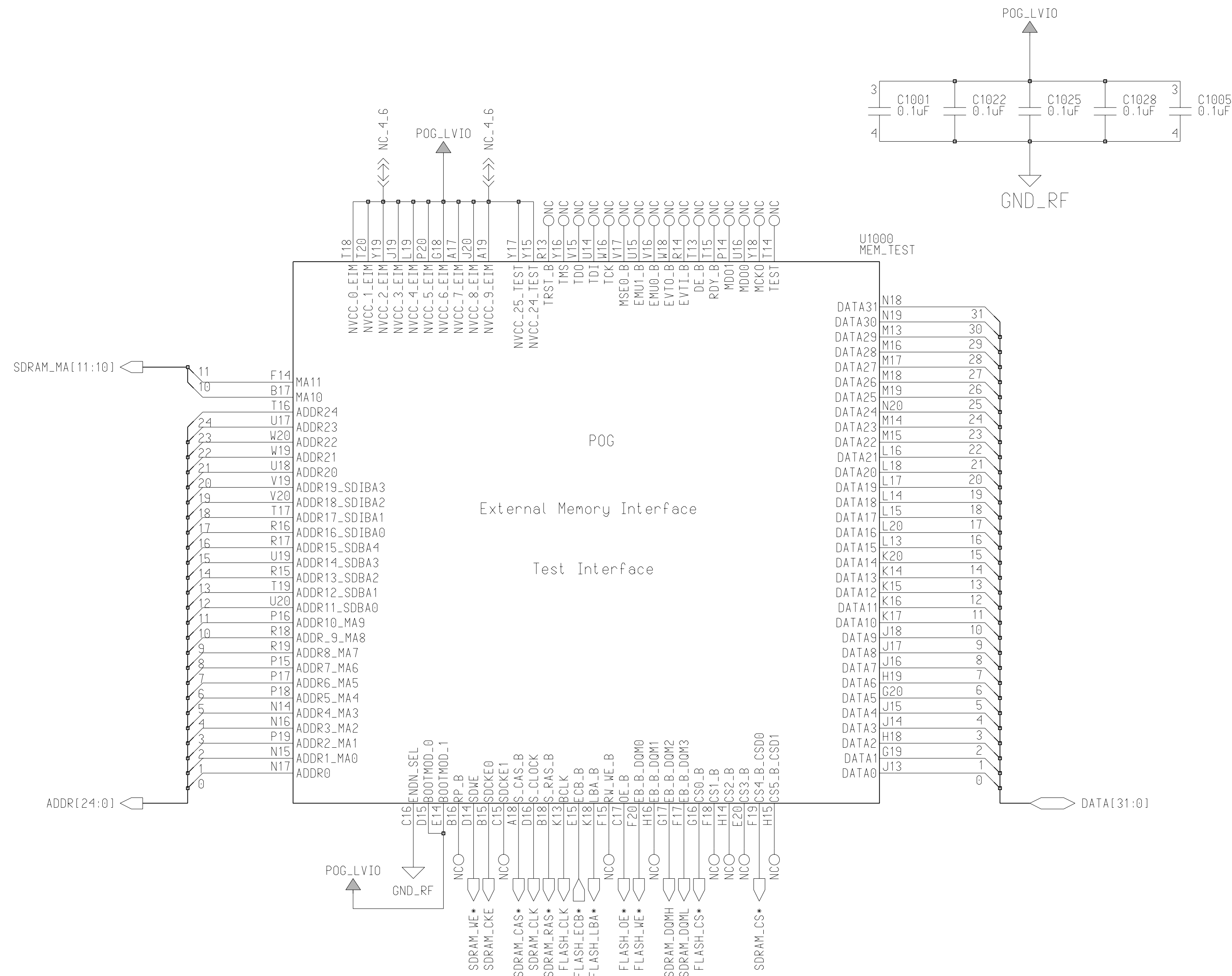
TITLE:	Talon Integrated POG Memory and Test Interface
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Size:	11x17
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REV: 1.0	Drawing Number: 8488888x88	Page: 3	Of: 4
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Date:
Tuesday, February 26, 2002

Time:
6:15:09 pm



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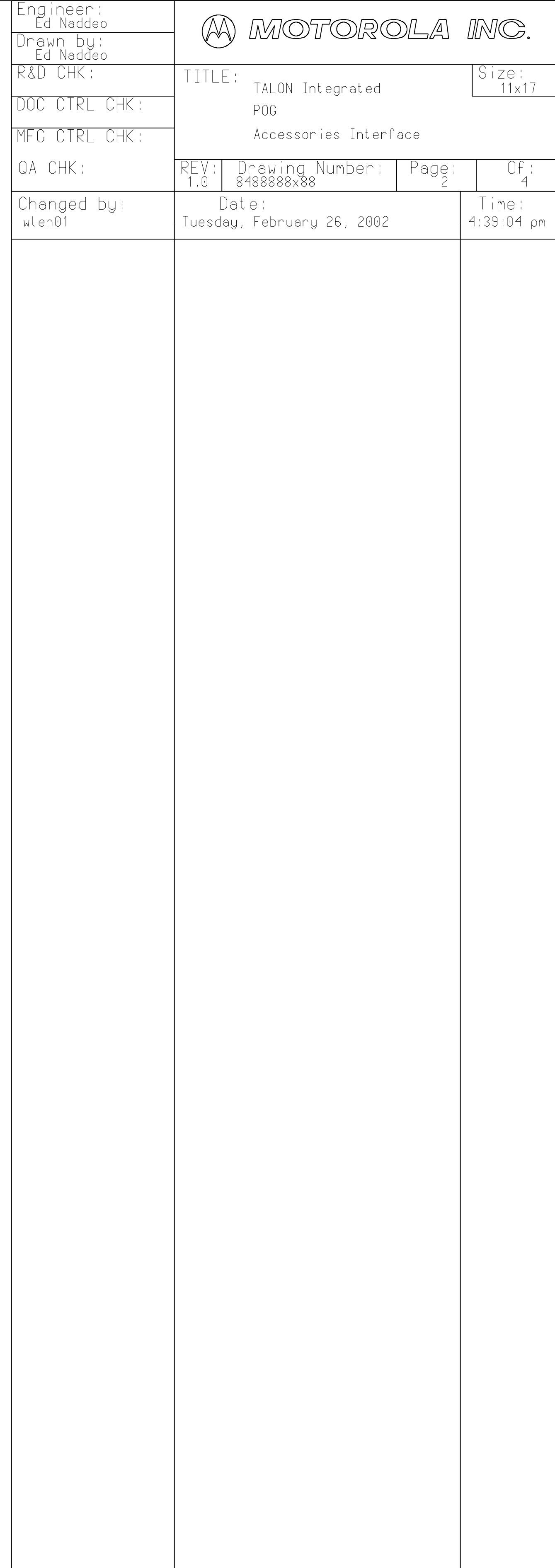
C

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 MOTOROLA INC.	
TITLE: TALON Integrated POG Accessories Interface	Size: 11x17
REV: 1.0 Drawing Number: 8488888x88 Date: Tuesday, February 26, 2002	Page: 2 Of: 4 Time: 4:39:04 pm

 MOTOROLA INC.	
TITLE: TALON Integrated POG Accessories Interface	Size: 11x17
REV: 1.0 Drawing Number: 8488888x88 Date: Tuesday, February 26, 2002	Page: 2 Of: 4 Time: 4:39:04 pm

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MOTOROLA CONFIDENTIAL PROPRIETARY

4 1 3 A

Engineer:

Ed Naddeo
Group buy:

Ed Nadeo

R&D CHK:

DOC CTRL CHK:

MEG CTRL QUK

MFG CTRL CHK:

QA CHK:

O	I

Changed by:
ulon@1

 **MOTOROLA INC.**

TITLE: Tolson, Integrated

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Bayer

REV

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Tues

Tuesday, February 20, 2002

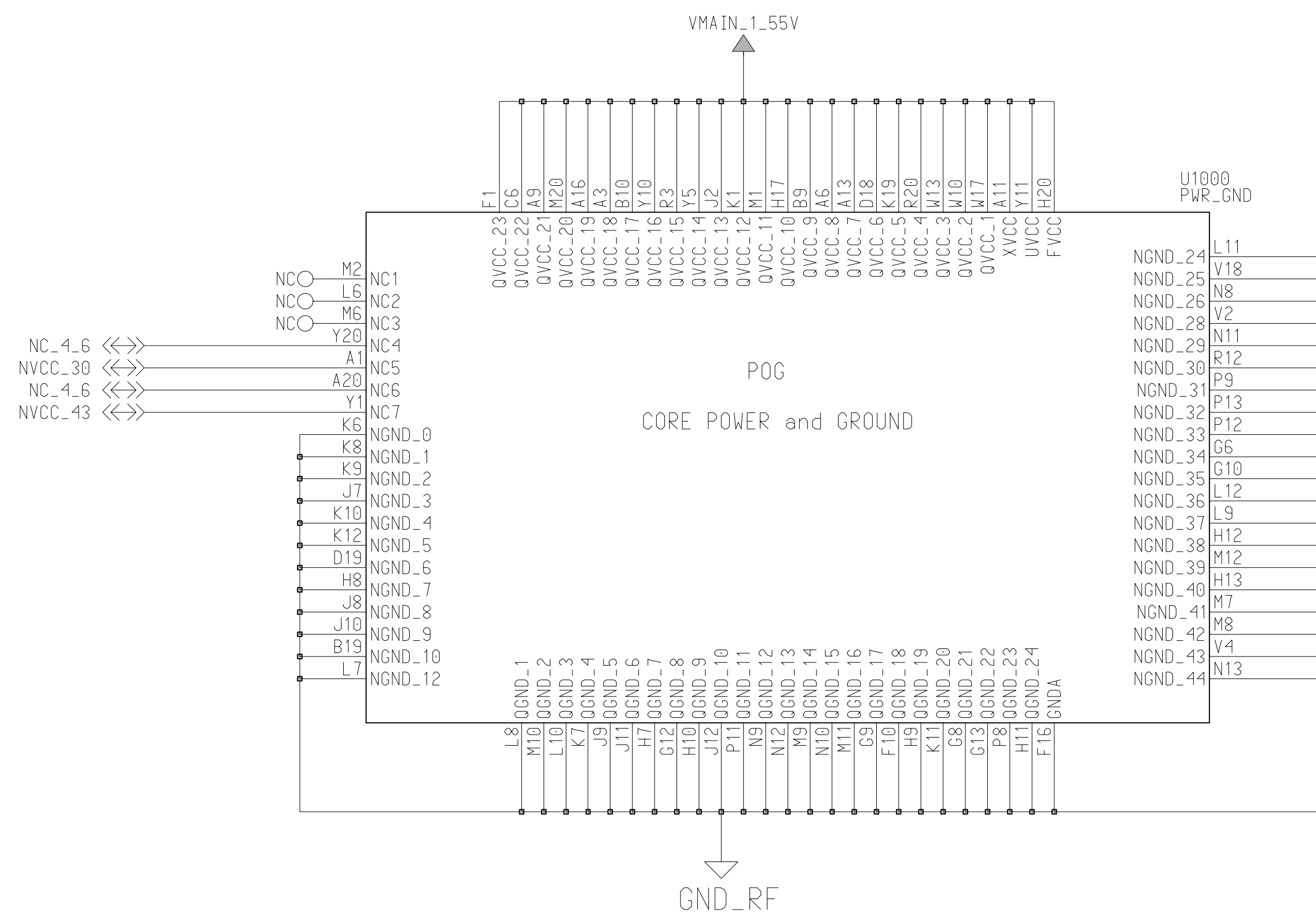
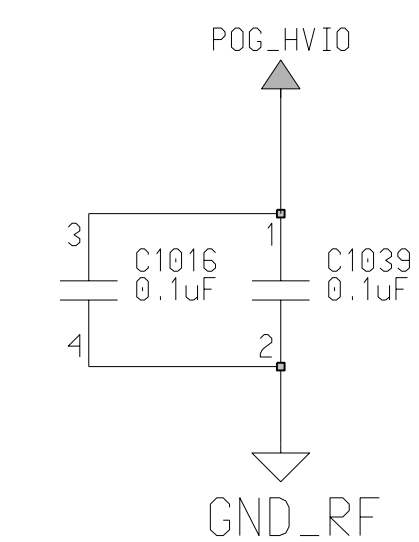
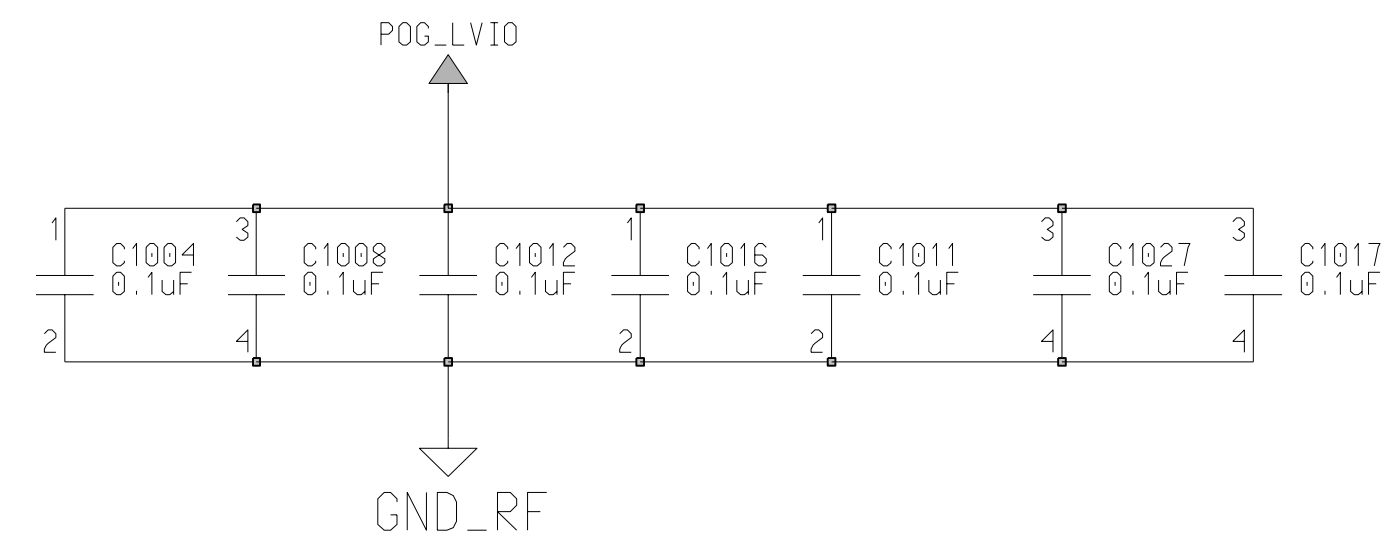
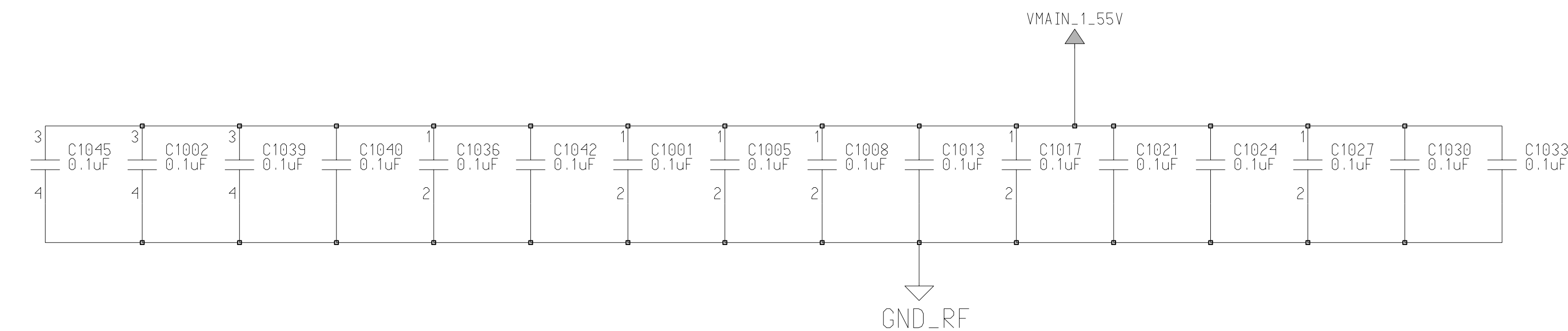
Size:	11x17
-------	-------

11X11

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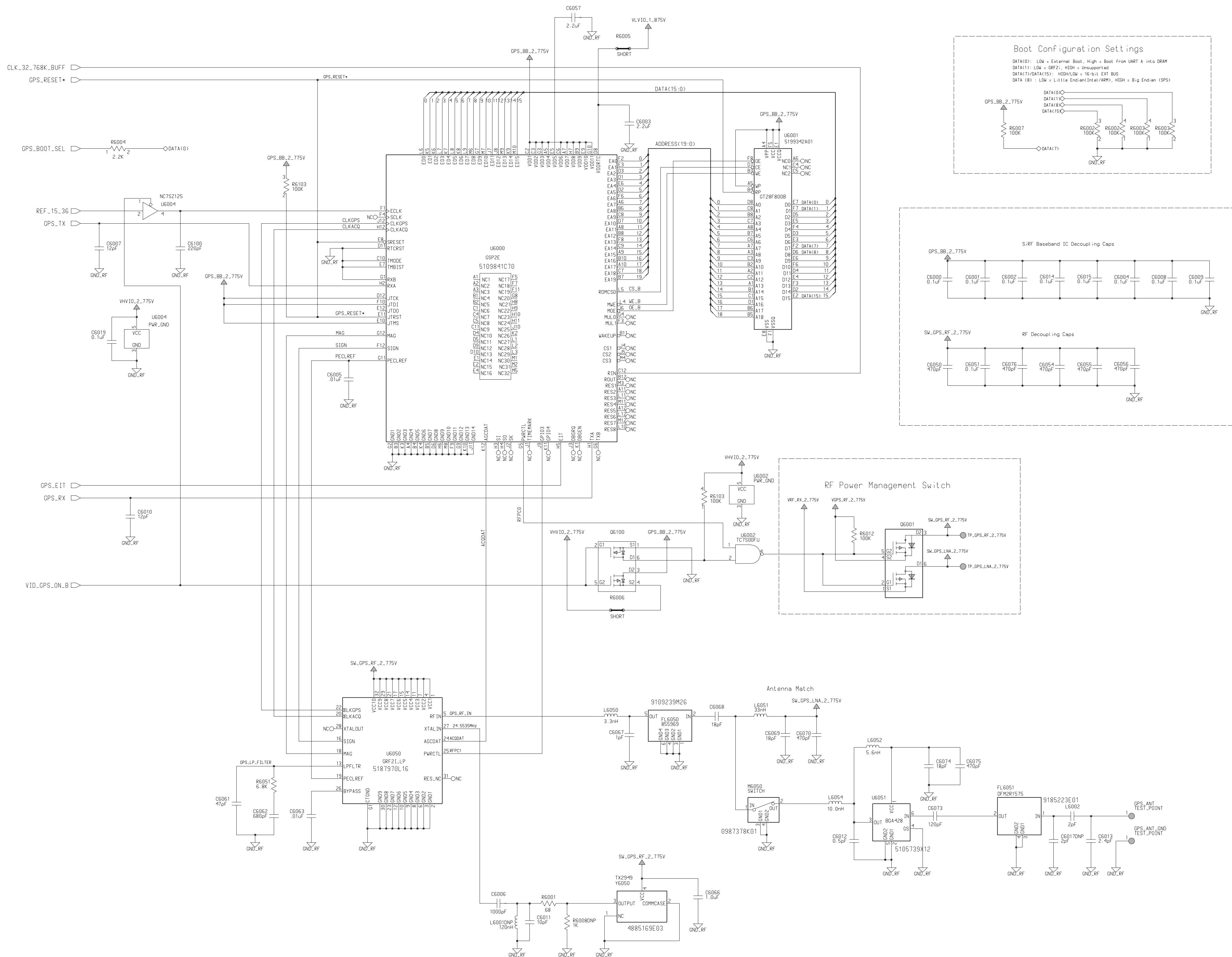


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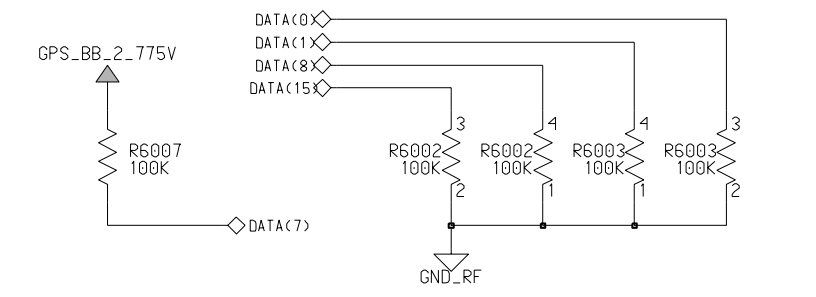
C

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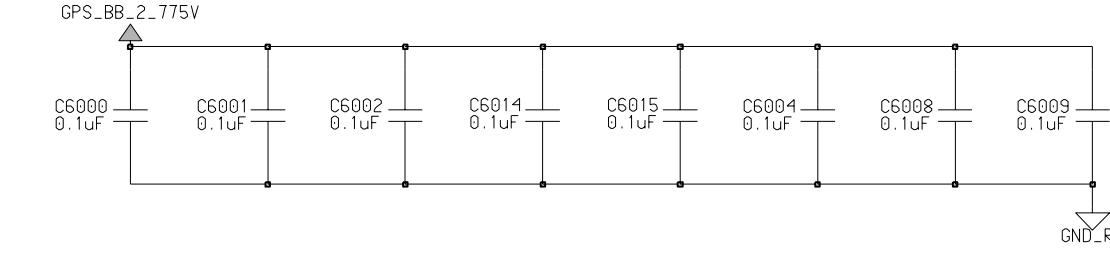


Boot Configuration Settings

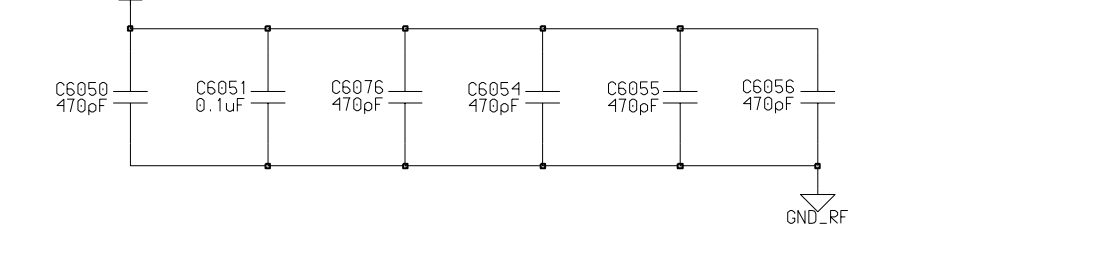
DATA(0): LOW = External Boot, High = Boot from UART A into DRAM
DATA(1): LOW = GRF2i, HIGH = Unsupported
DATA(7)/DATA(15): HIGH/LOW = 16-bit EXT BUS
DATA (8) : LOW = Little Endian(Intel/ARM), HIGH = Big Endian (SPS).



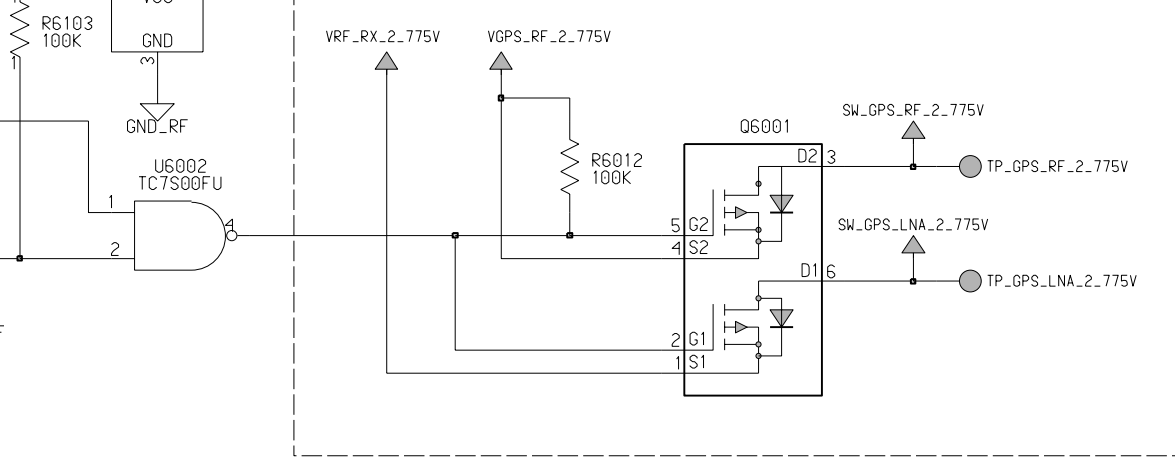
SiRF Baseband IC Decoupling Caps



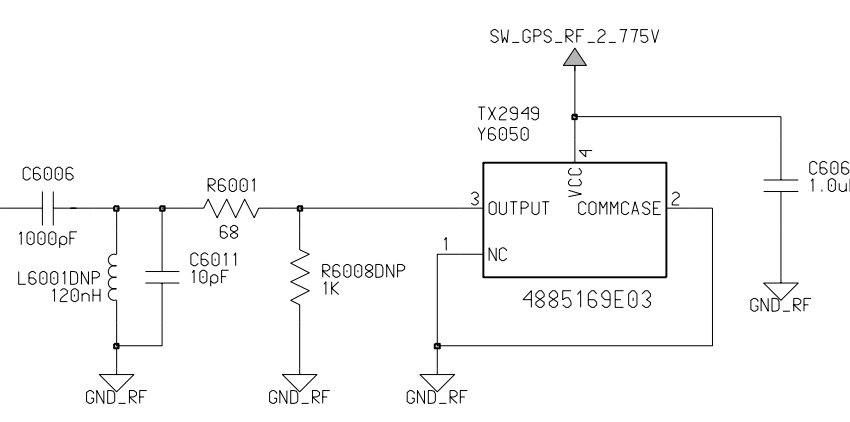
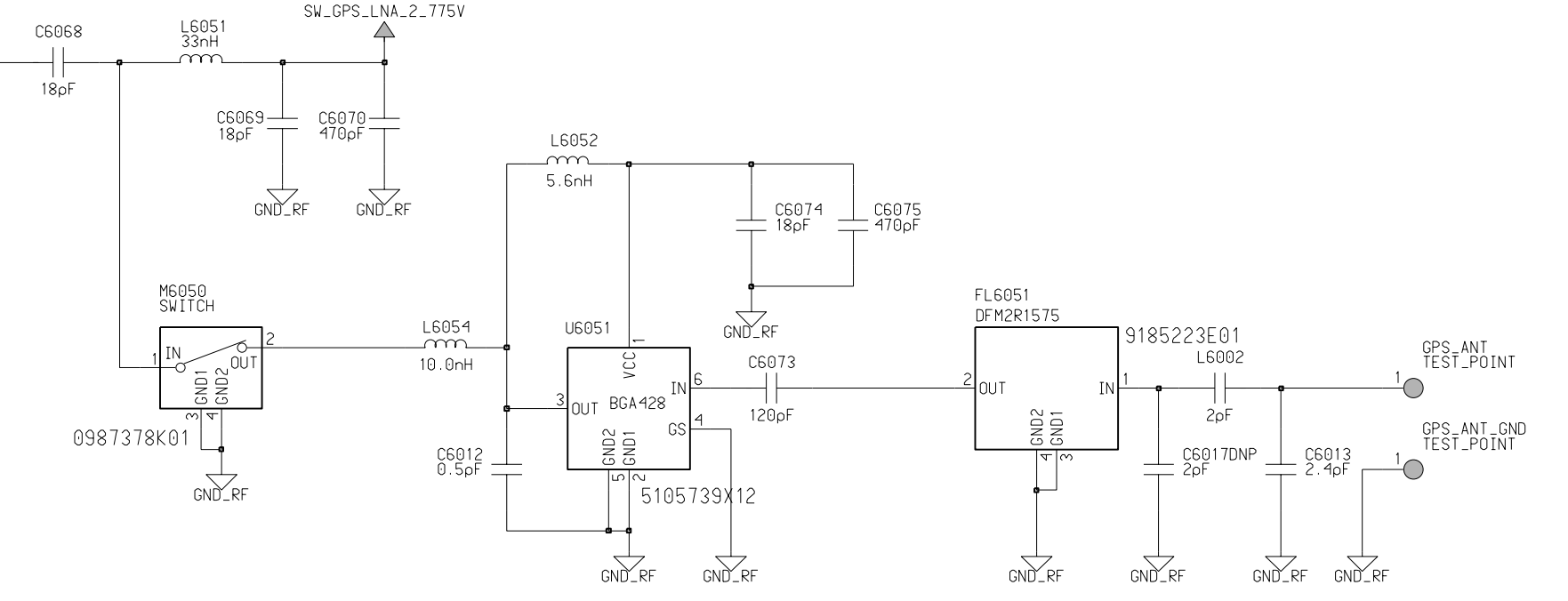
RF Decoupling Cap



RF Power Management Switch



Antenna Match



MOTOROLA CONFIDENTIAL PROPRIETARY

4 1 3 A

Engineer :

Ed Naddeo
Drawn by:

Ed Naddeo
DAD QUIZ

R&D	CHK:
DOC	CTRL CHK:

MFG CTRL CHK:

QA CHK :

Changed by: Chris Pipe

 **MOTOROLA INC.**

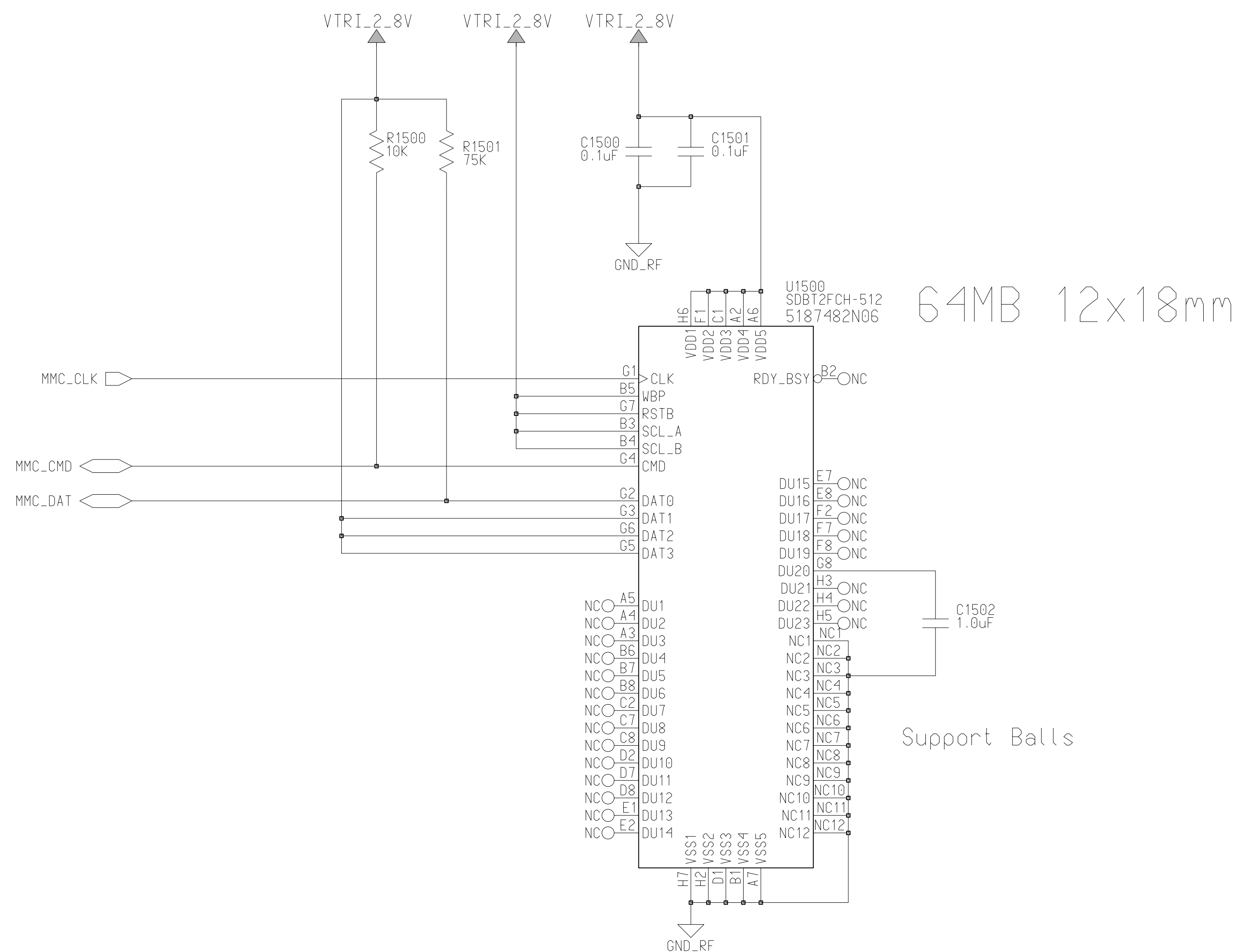
TITLE:	Talon Integrated Embedded SD Flash
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Size:	11x17
-------	-------

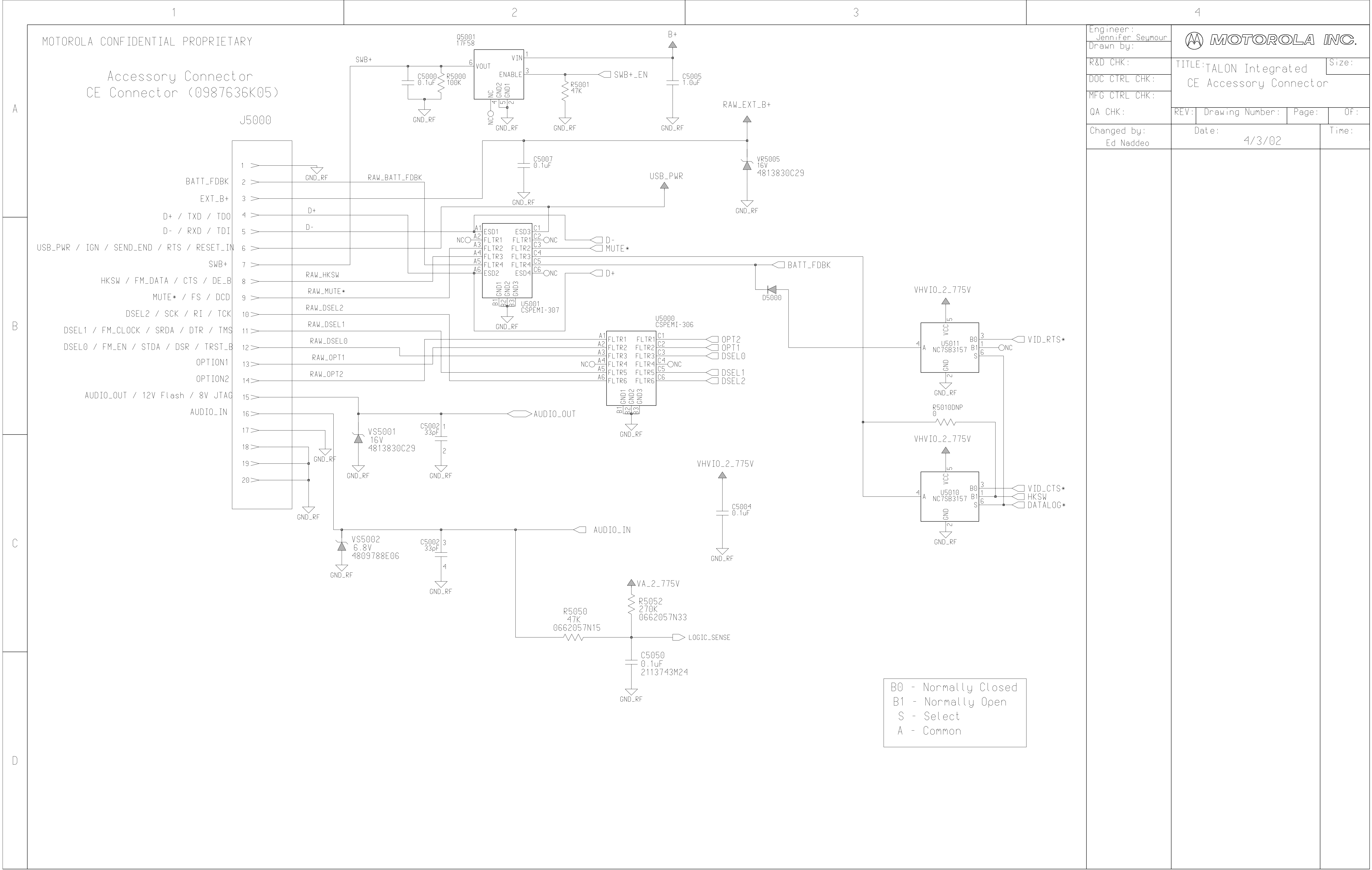
REV: 1.0	Drawing Number: 848888888x88	Page: 1	Of: 1
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Date:
Friday, May 3, 2002

Time:
1:25:03 p



Support Balls



Engineer:
Jennifer Seymour

Drawn by:

R&D CHK:

DOC CTRL CHK:

MFG CTRL CHK:

QA CHK:

Changed by:
Ed Naddeo

MOTOROLA INC.

TITLE: TALON Integrated
CE Accessory Connector

Size:

REV:

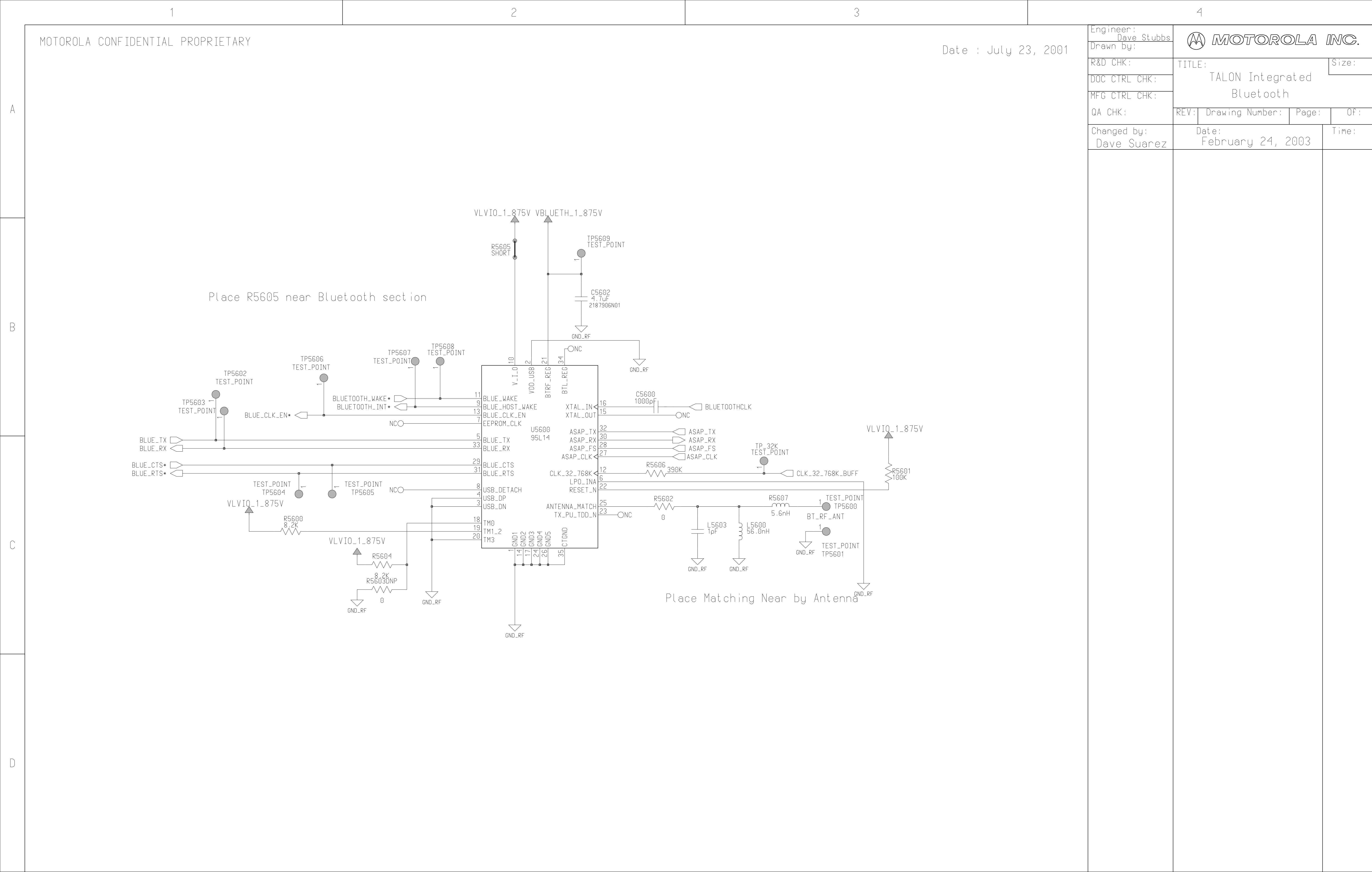
Drawing Number:

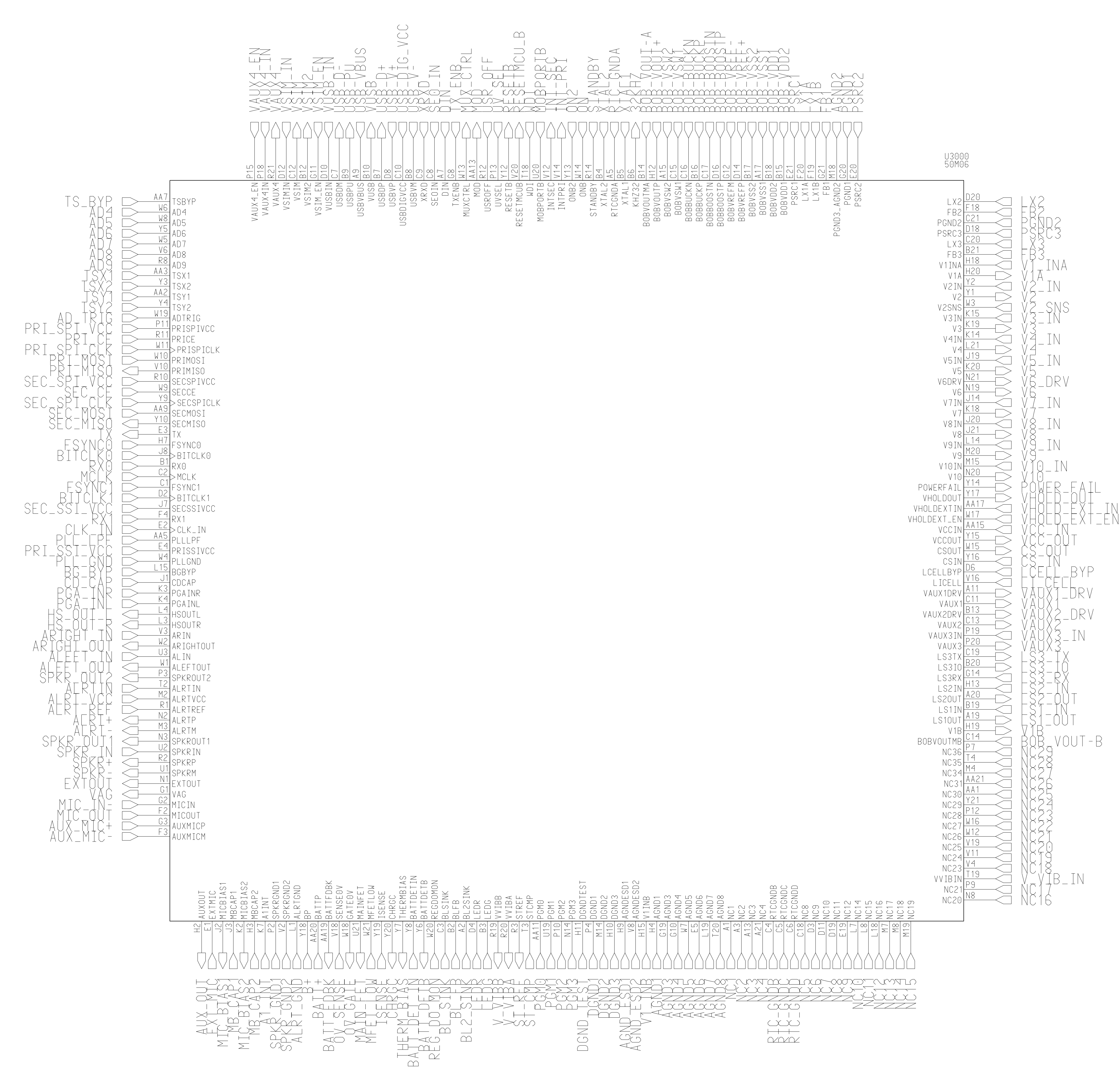
Page:

Of:

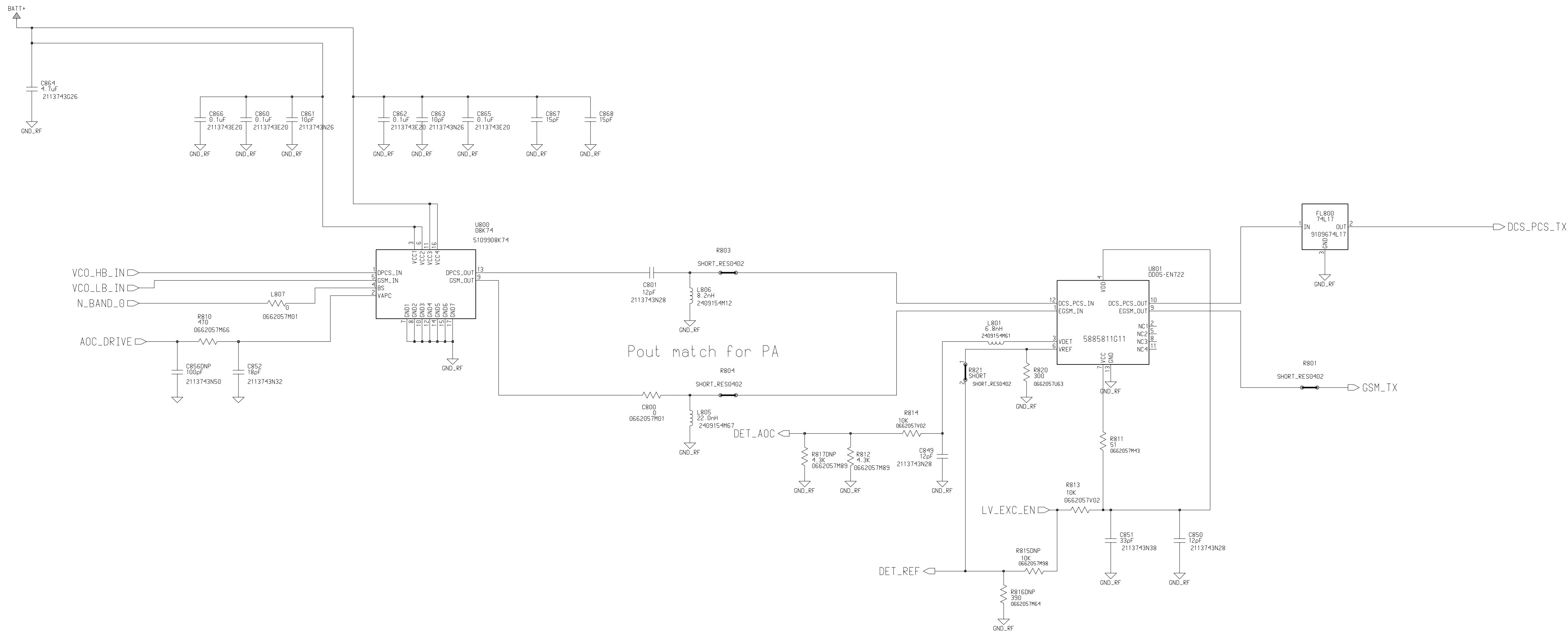
Date:
4/3/02

Time:





GSM Transmitter

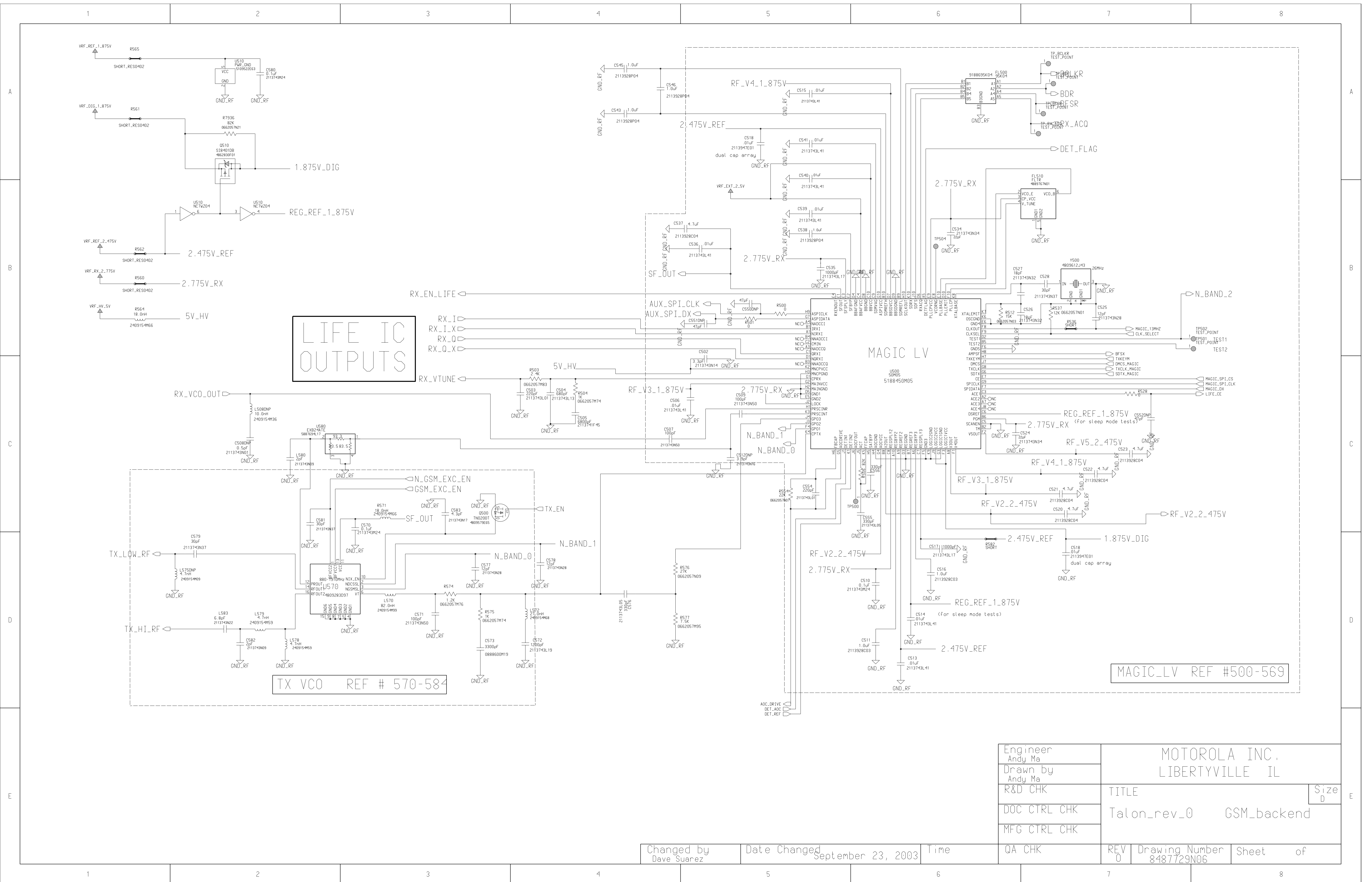


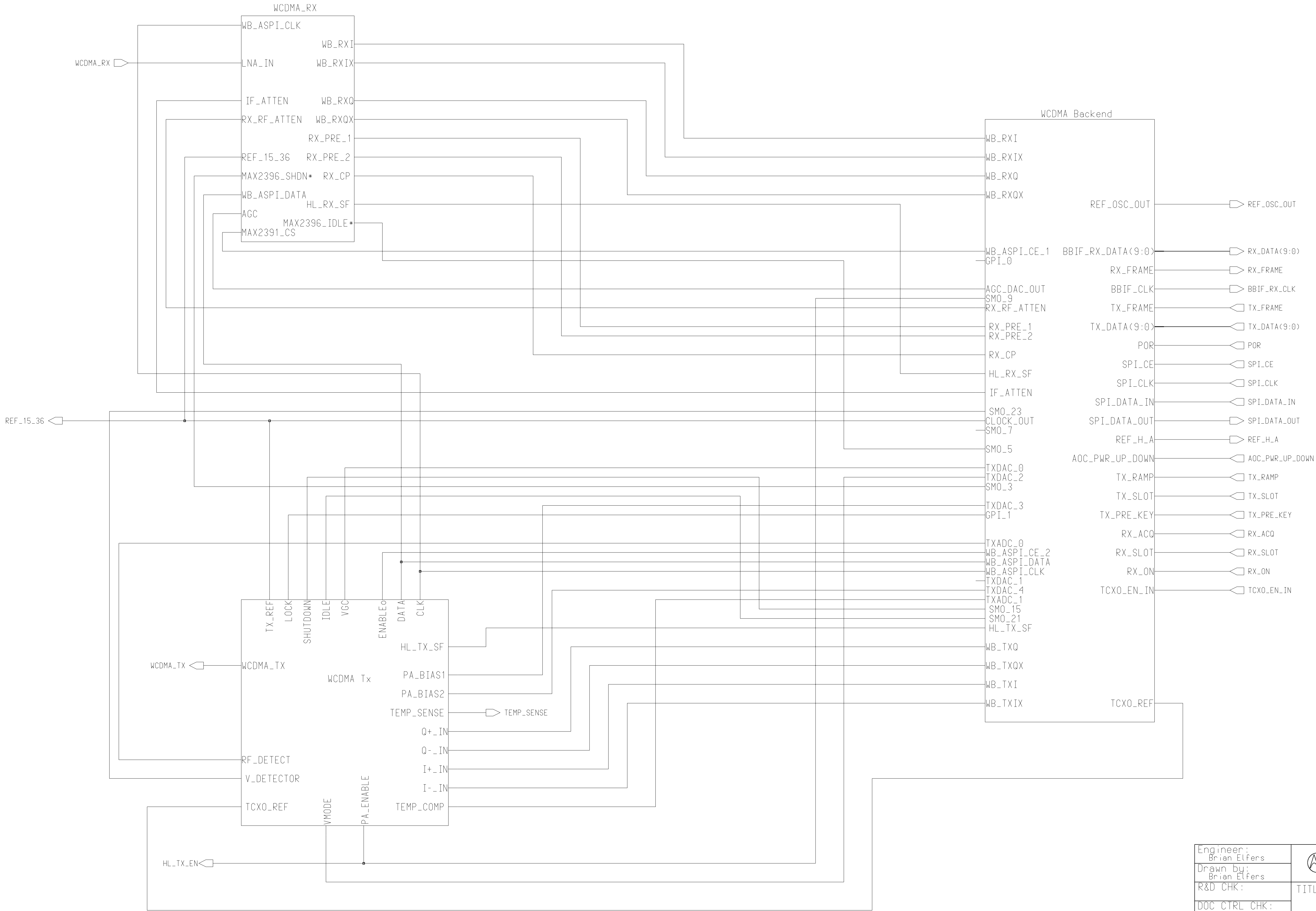
Engineer Paul Manente	COMPANY NAME Address City		
Drawn by Paul Manente			
R&D CHK	TITLE		Size D
DOC CTRL CHK	GSM_TX_D		
MFG CTRL CHK	GSM_Transmitter		
QA CHK	REV 0	Drawing Number 8487729N06	Sheet of

Changed by
Dave Suarez

Date Changed
September 23, 2003

Time

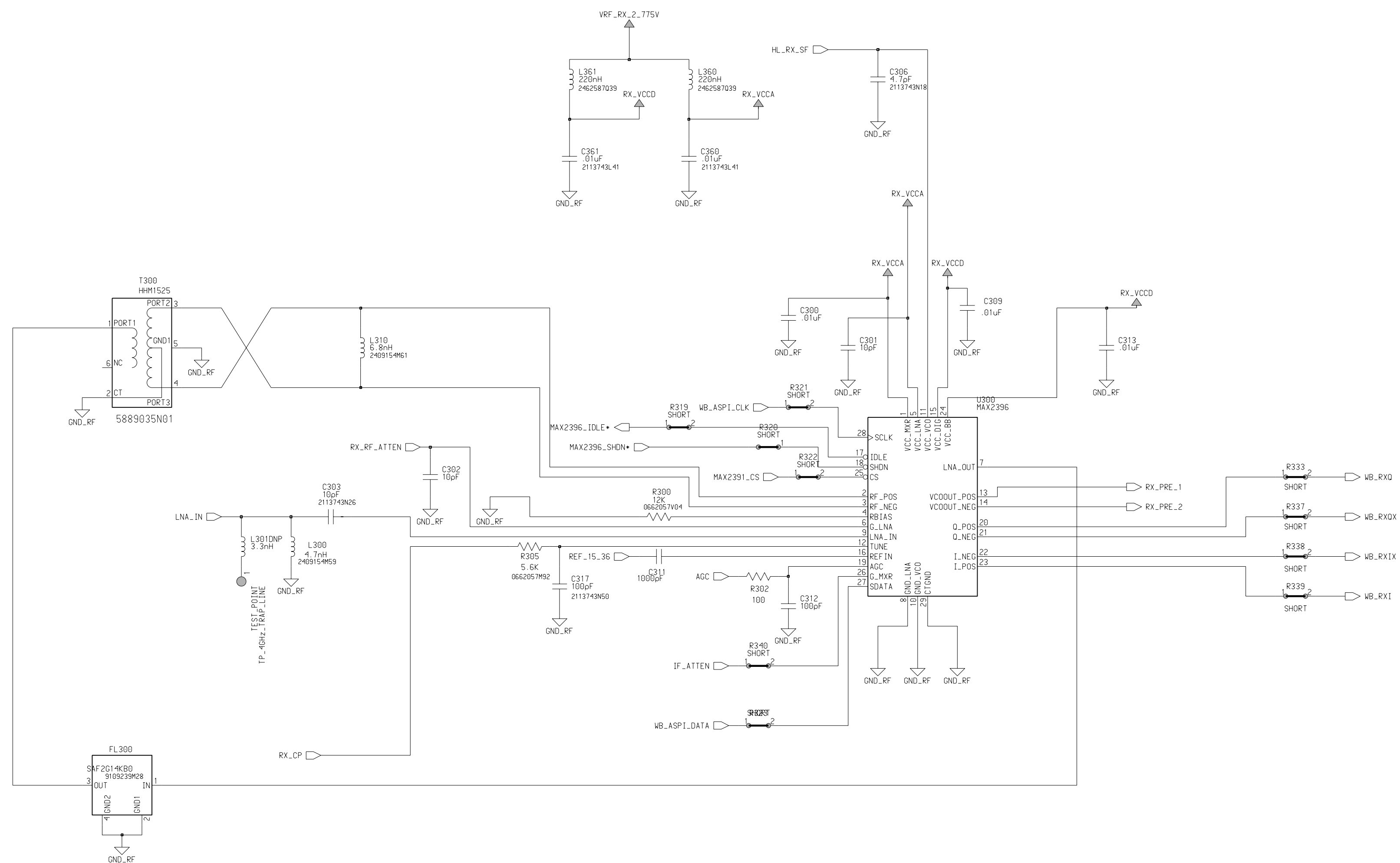




Engineer: Brian Elfers	 MOTOROLA INC.		
Drawn by: Brian Elfers			
R&D CHK:	TITLE: Talon_rev_0		Size: 11x17
DOC CTRL CHK:			
MFG CTRL CHK:			
QA CHK:	REV: P3	Drawing Number: 8487729N03	Page: Of:
Changed by: Dave Suarez	Date: Friday, April 20, 2001		Time: 4:07:28 pm

1

MOTOROLA INTERNAL USE ONLY



Engineer: Randy Wiessner	 MOTOROLA INC		
Drawn by: Randy Wiessner			
R&D CHK:	TITLE:	Size:	
DOC CTRL CHK:	UCDMA_Rx	B	
MFG CTRL CHK:			
QA CHK:	REV:	Drawing Number:	Page:
B	0	2487729N06	Of:
Changed by: Dave Suarez	Date:	Time:	
	September 23, 2003	4:33:16 pm	

