

5

4

3

2

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D

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C

C

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B

A

A



Title <Title>		
Size A	Document Number <Doc>	Rev <RevCode>
Date:	Wednesday, September 26, 2018	Sheet 1 of 47

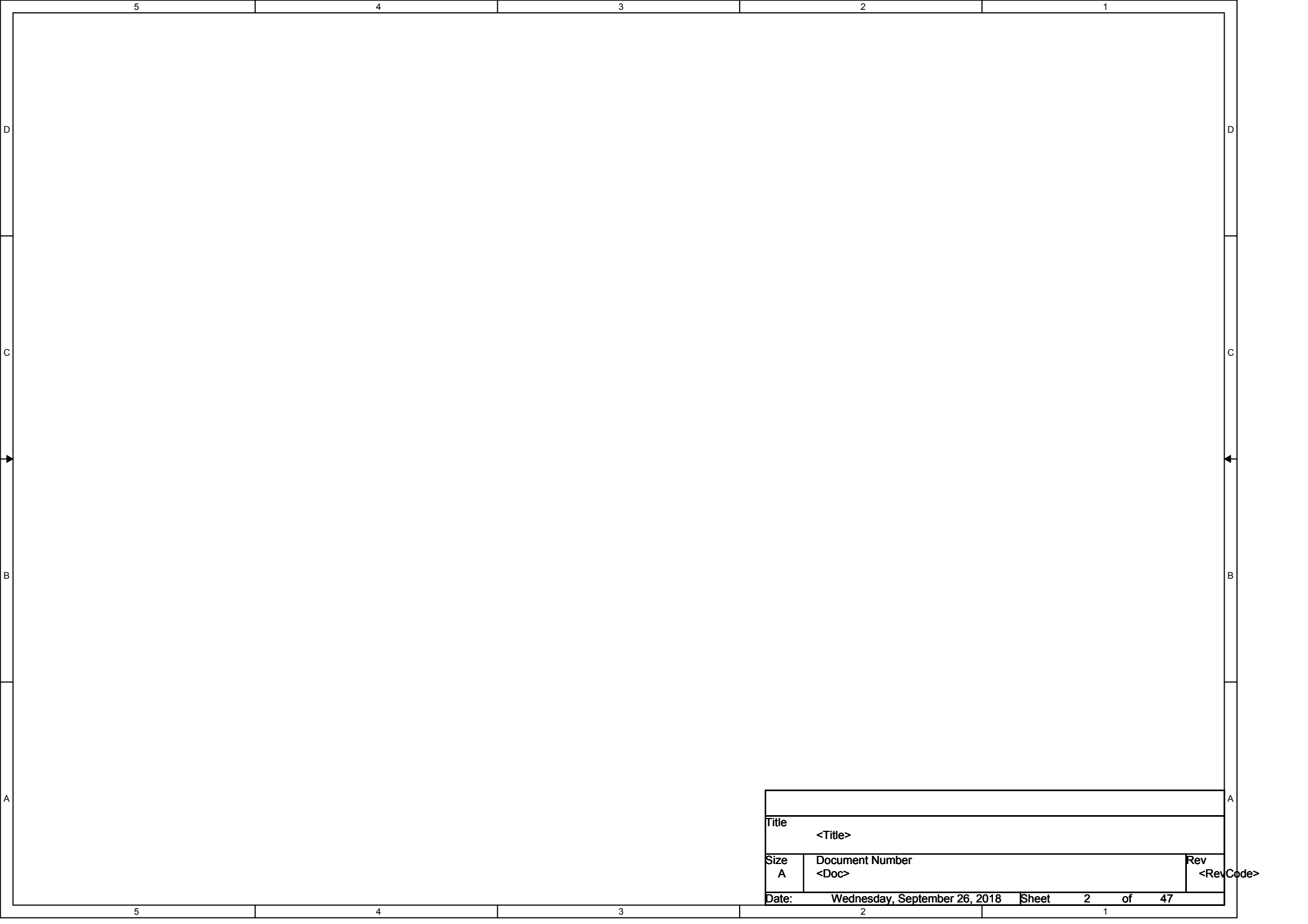
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4

3

2

1



			A
Title			<Title>
Size	Document Number	Rev	<RevCode>
A	<Doc>		
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5

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C

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B

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A



Title <Title>		
Size A	Document Number <Doc>	Rev <RevCode>
Date:	Wednesday, September 26, 2018	Sheet 3 of 47

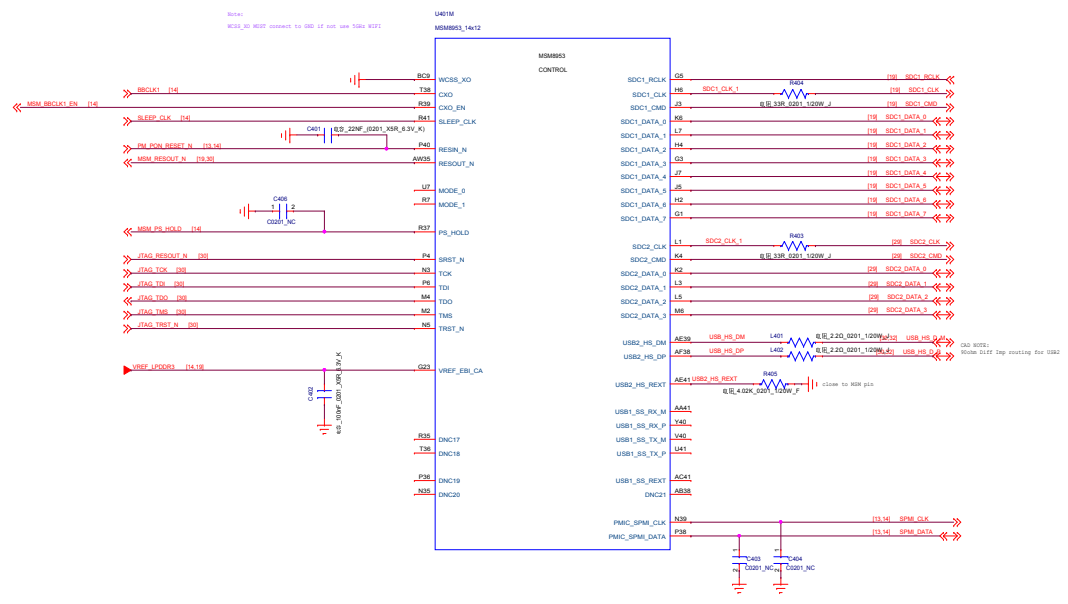
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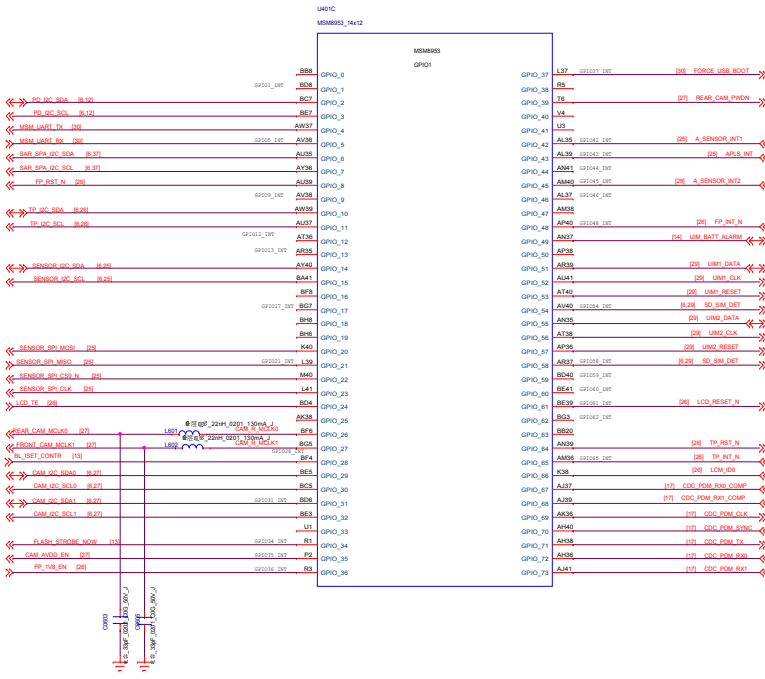


MSM8953 Control



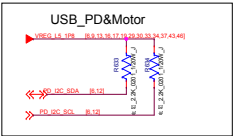
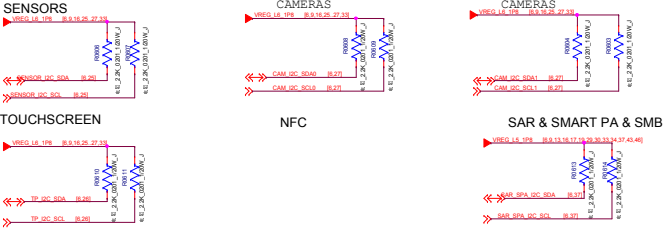
MSM8953 EBI

NOTE:
Asterisks (*) indicate modem power management (MPM) wake-up pins

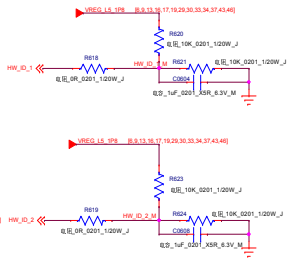


APPS_BOOT_FROM_ROM FOR DEVELOPMENT

I2C PULL-UP



HW Board ID



GPIO_37	FRX32A_USB_BOOT
GPIO_106	MEM231MEM24
GPIO_108	APPS_BOOT_FROM_ROM

BOOT_CONFIG(1)	BOOT_CONFIG0
0x000	0x01 + 0x02 -> 0x02,0
0x001	0x02 + 0x01 -> 0x02,0
0x010	0x01 + 0x02 -> 0
0x011	0x02,0

Default boot config (0x001) is 0x01(0x02)

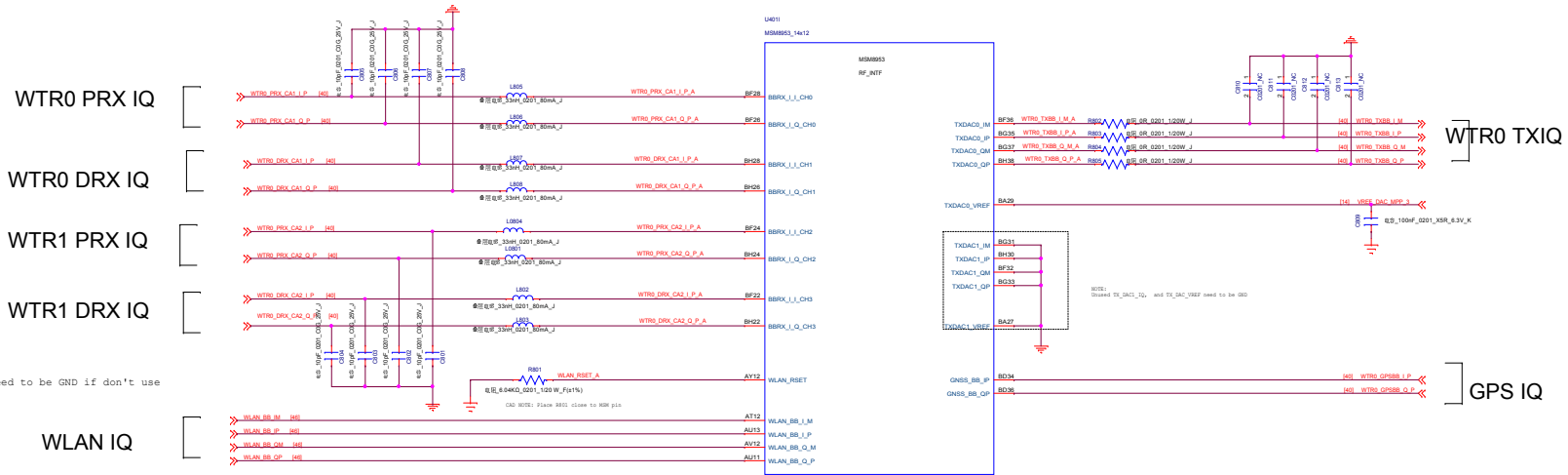
MSM8953 GPIO

NOTE:
Ensure SW sets these GPIOs (Sensor, CTP and Camera I2C bus) to inout pull down when the peripherals are powered off to eliminate leakage.

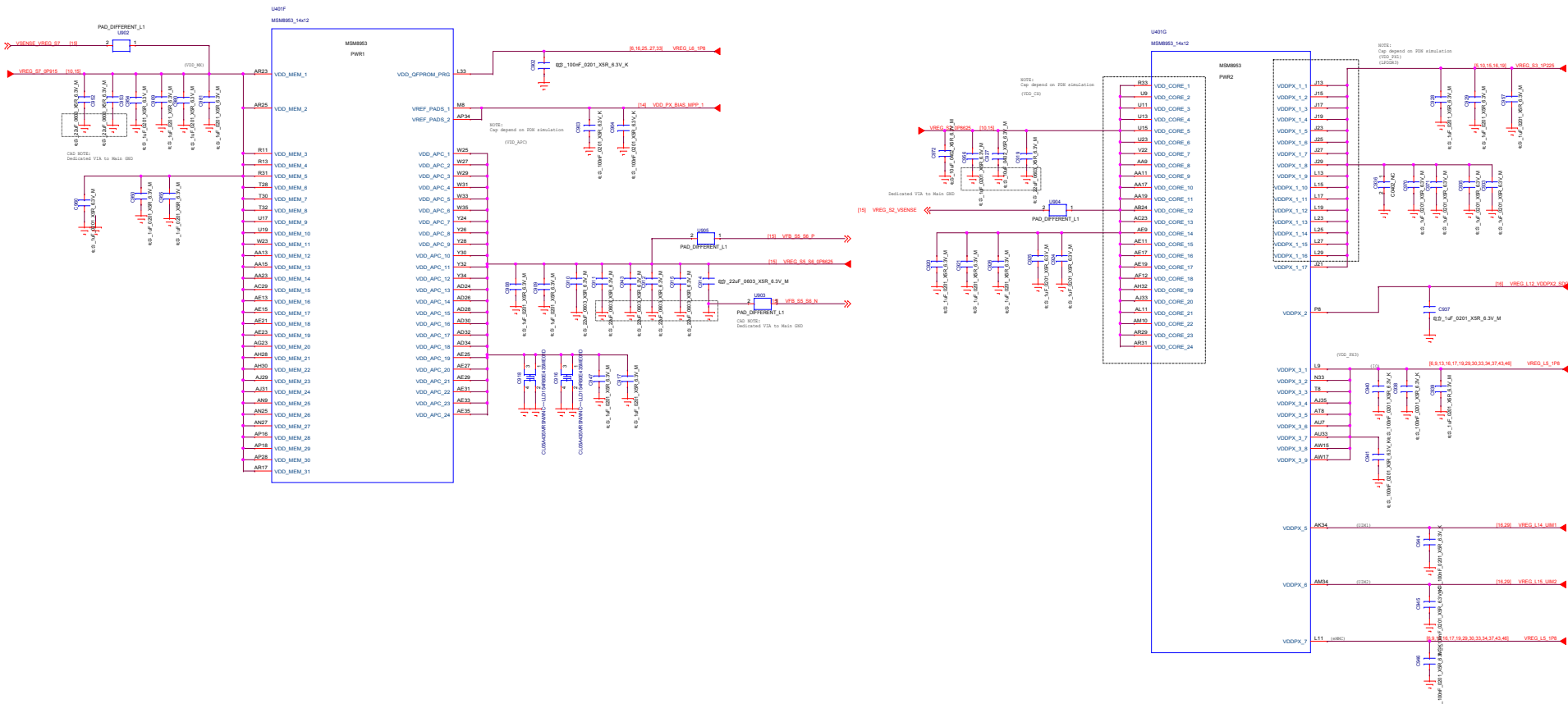


MSM8953 MIPI

NOTE:
Unused BBRx_IQ need to be GND if don't use

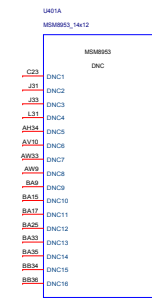
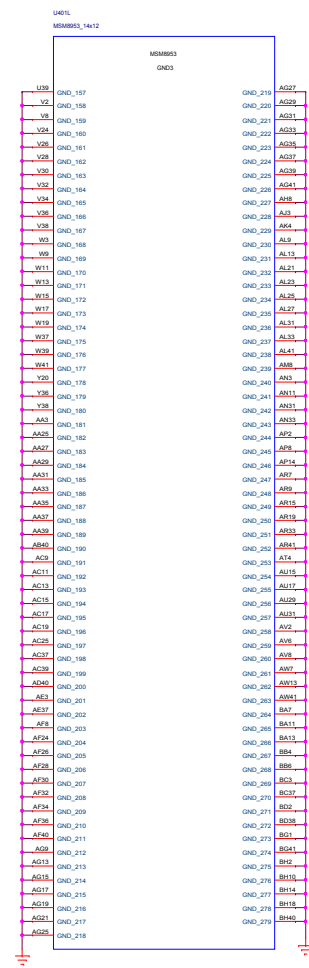
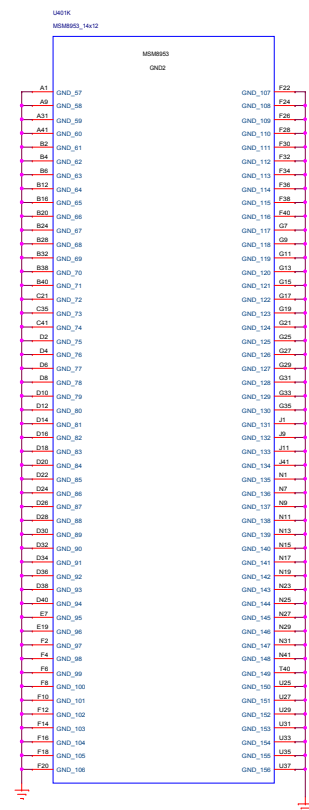
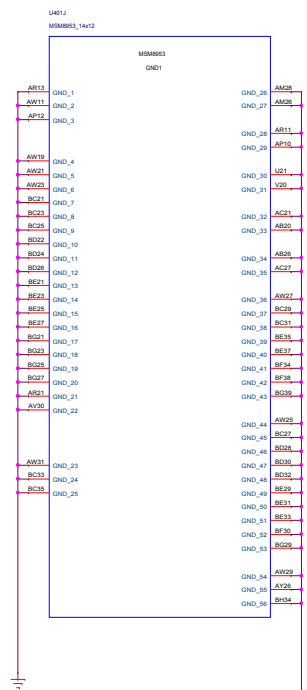


MSM8953 RF



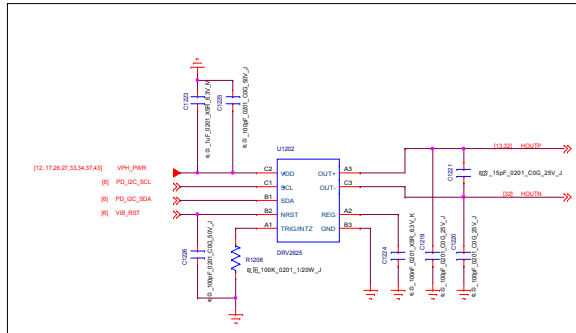
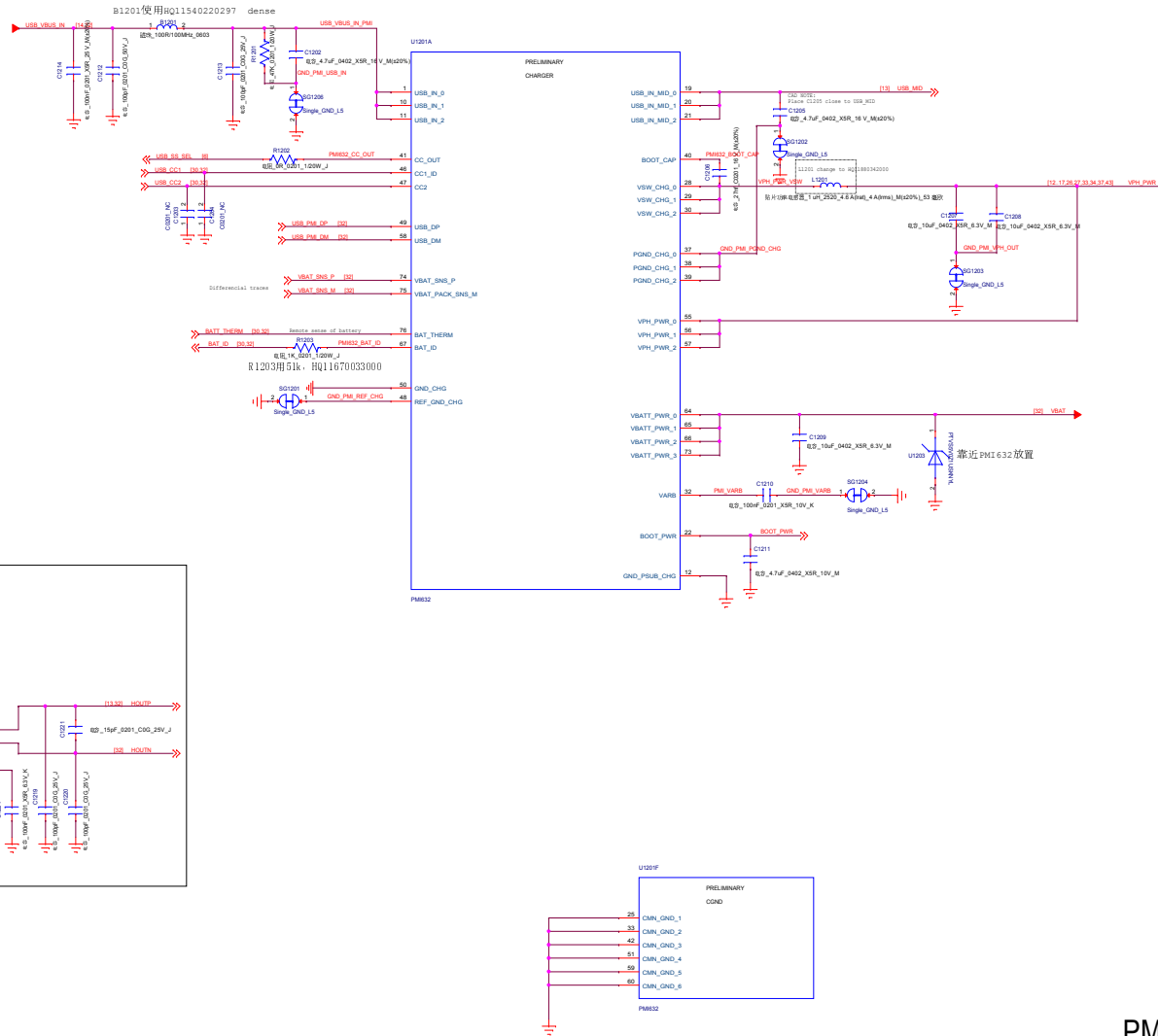
MSM8953 POWER1

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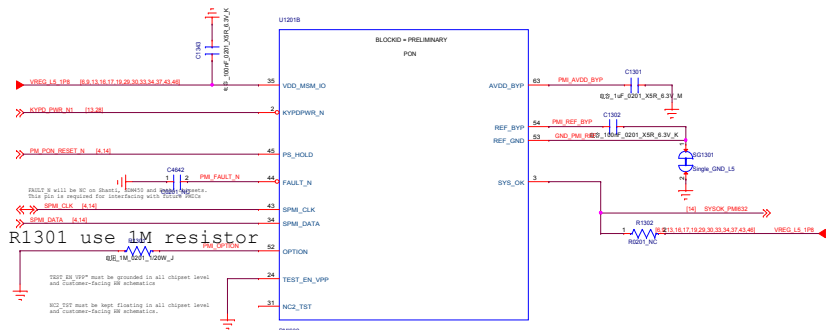


MSM8953
GND

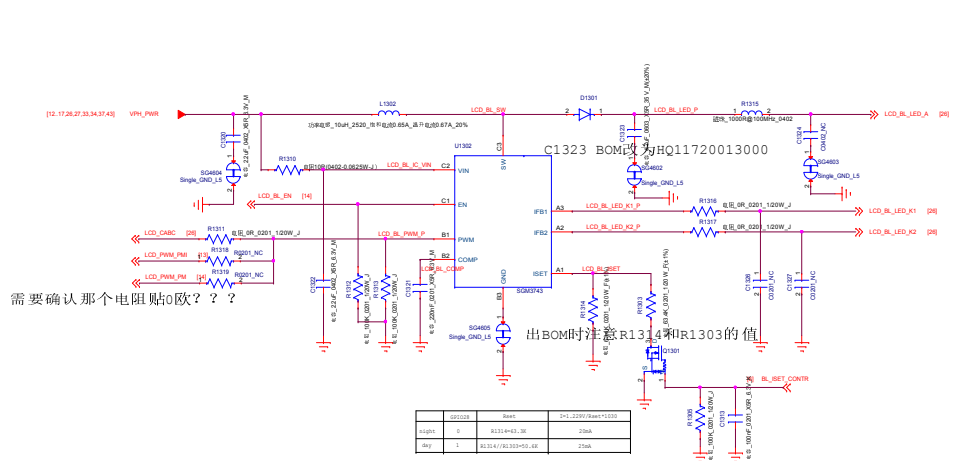
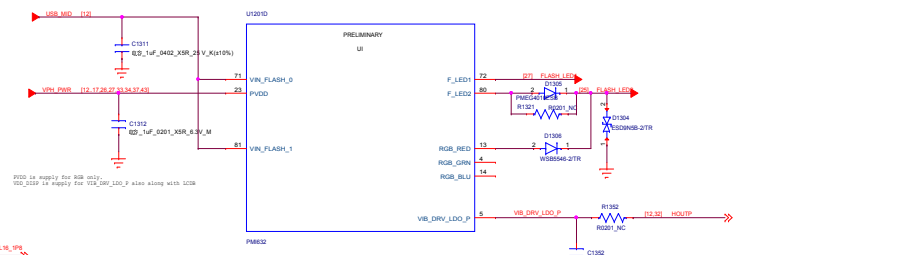
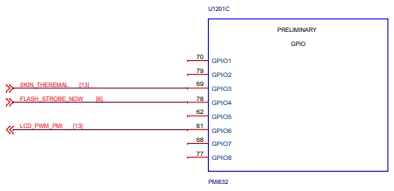
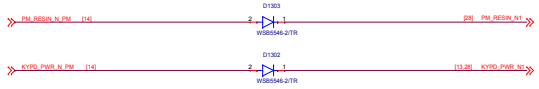
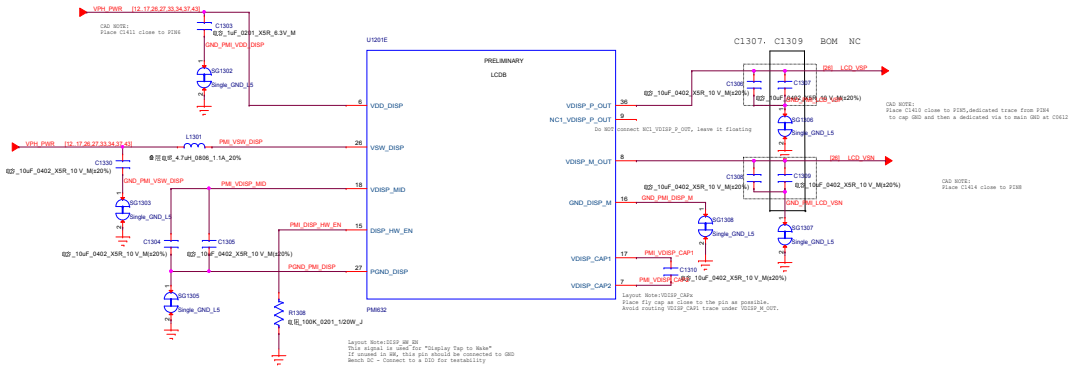
RF012	connection to device without keypad	connection to device with keypad
CU5_08	pull up to VDD_CDP with 10kohm	trunk 990 040_04
DF22_03	pull down to GND with 30k	trunk 990 002_04
RF013	to VDD_CDP	trunk 990 002_04
RF014	for 500mA (charge) Connects to GND for preliminary charging; Connects to VDD_CDP for normal charging; Connects to GND for full charge; Connects to GND for VDD_CDP	trunk 990 002_04



PMI632 Charger

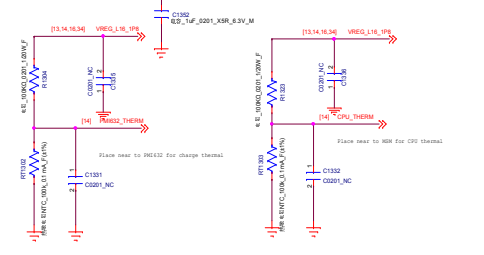
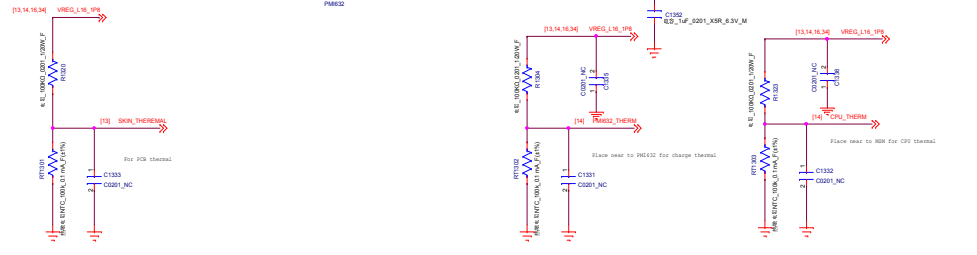


OPTION	OPTION Configuration	OPTION Resistance
1	OPTION_1	20K
2	OPTION_2	20K
3	OPTION_3	100K
4	OPTION_4	100K

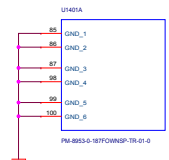
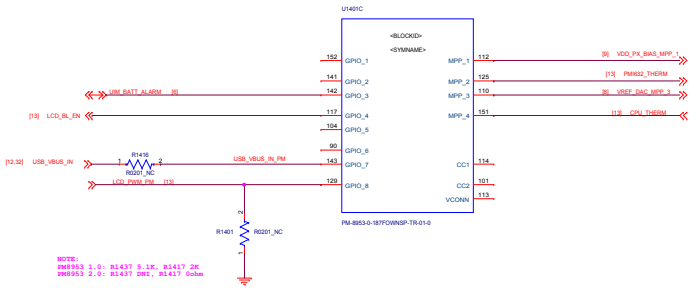
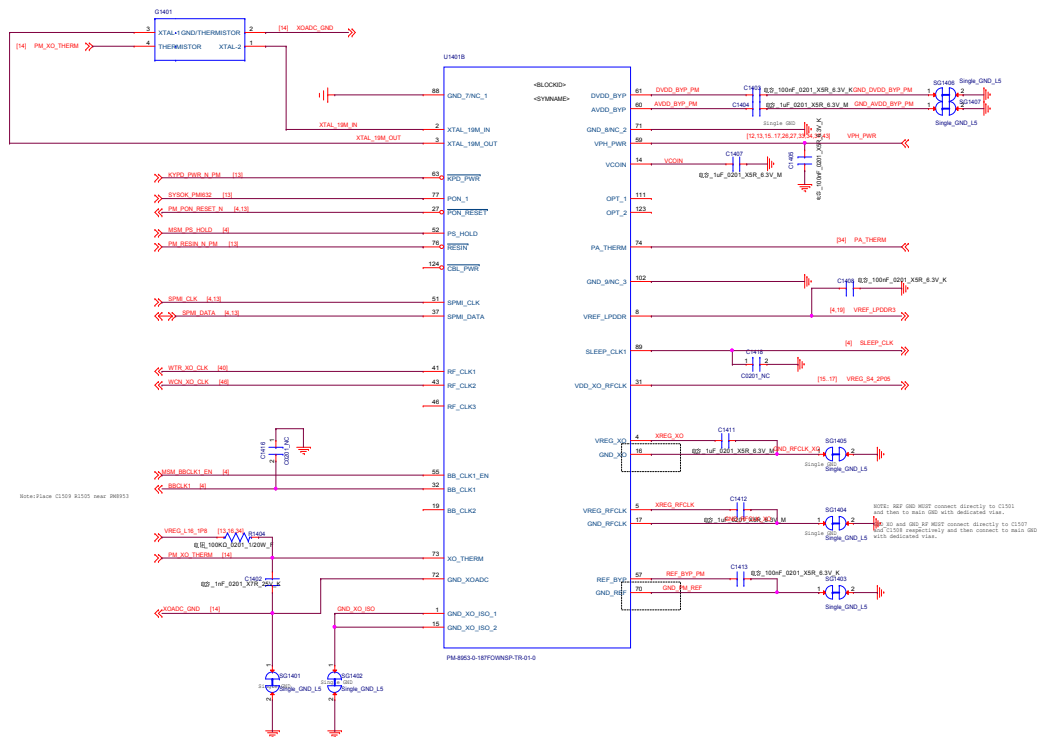


OPTION	OPTION	OPTION Resistance
1	OPTION_1	20K
2	OPTION_2	20K

需要确认那个电阻贴0欧??



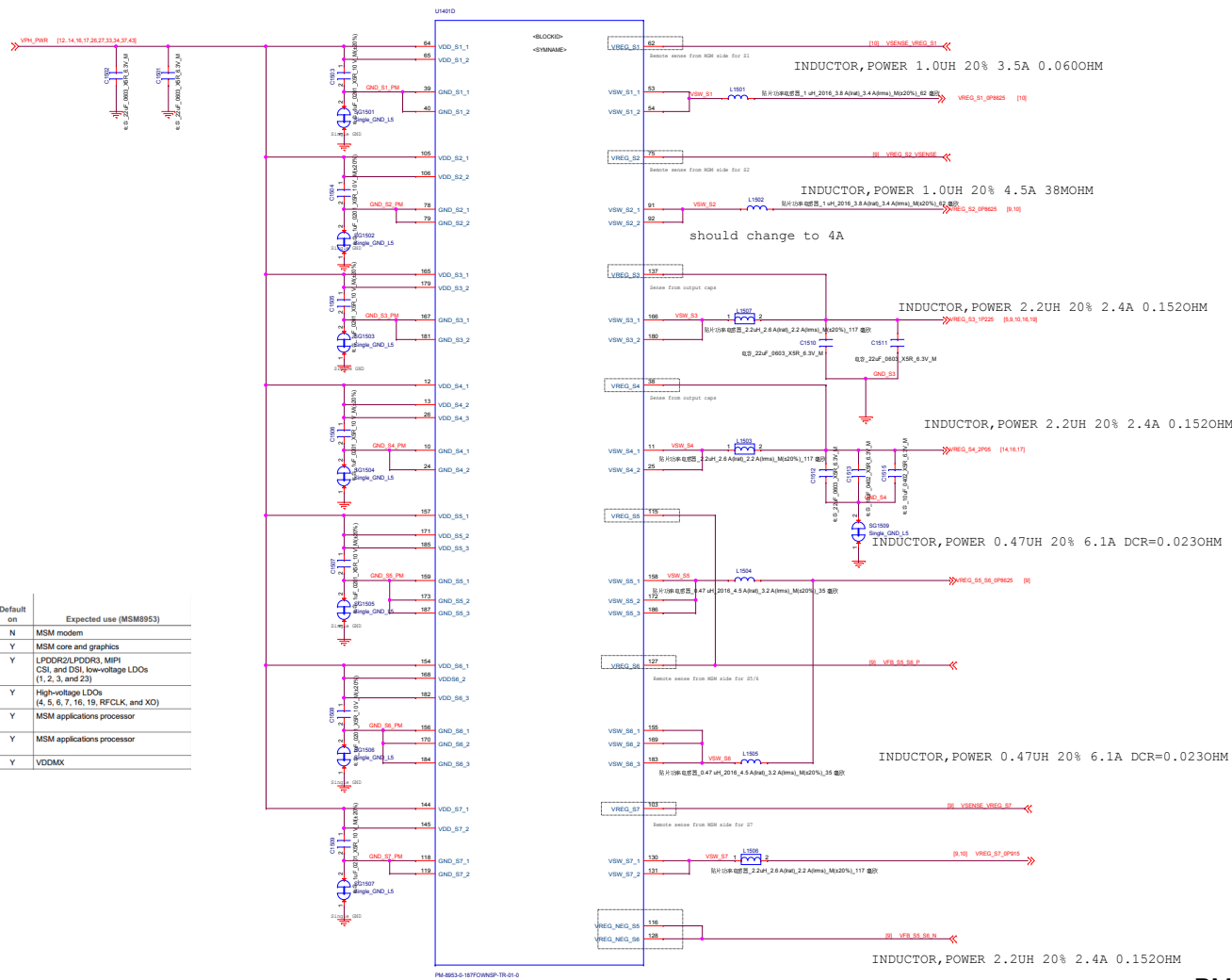
NOTE: All GND should connect to XNDC_GND pin
 All GND pins should connect to XNDC_GND pin
 All GND pins should connect to XNDC_GND pin
 All GND pins should connect to XNDC_GND pin



PM8953 Control/Interface

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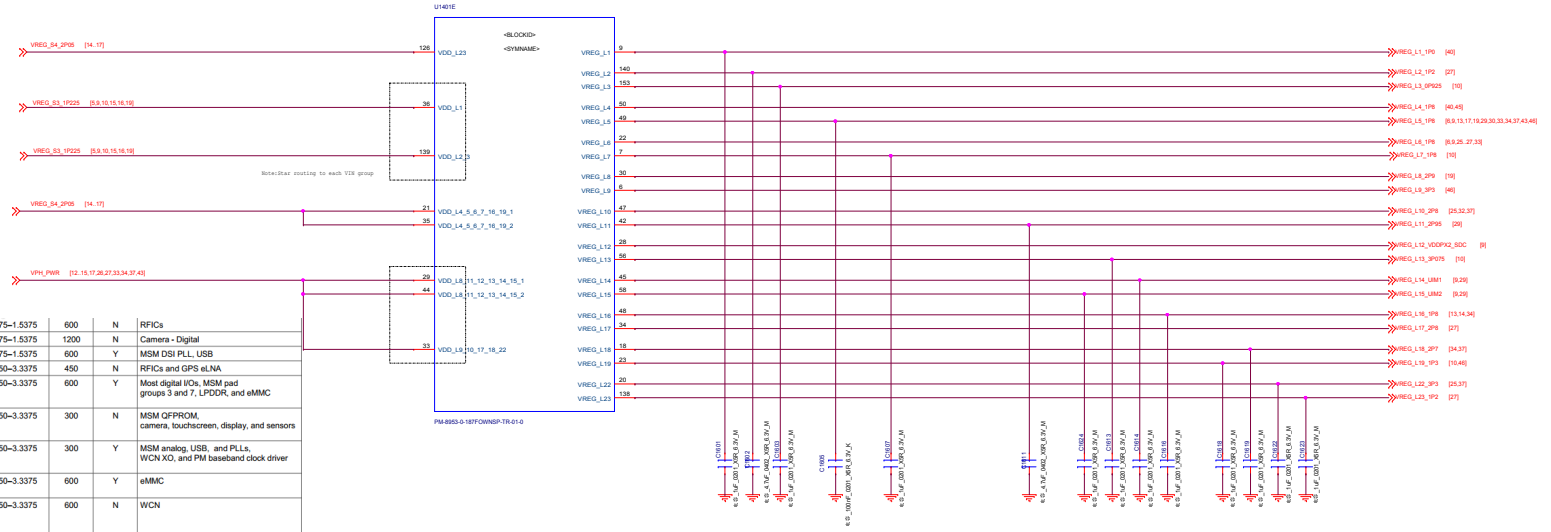
Function	Circuit type	Default voltage (V)	Specified range (V) (MSM8953)	Programmable range (V)	Rated current (mA)	Default on	Expected use (MSM8953)
S1	SMPS	0.87	0.4-1.14	0.32-2.04	3000	N	MSM modem
S2	SMPS	0.87	0.4-1.14	0.32-2.04	4000	Y	MSM core and graphics
S3	SMPS	1.225	1.2-1.25	0.32-2.04	2000	Y	LPDDR2/LPDDR3, MIPI CSI, and DSI, low-voltage LDOs (1, 2, 3, and 23)
S4	SMPS	2.04	1.8-2.04	0.32-2.04	2000	Y	High-voltage LDOs (4, 5, 6, 7, 16, 19, RFCLK, and XO)
S5	SMPS	0.87	0.4-1.14	0.35-1.355	3750	Y	MSM applications processor
S6	SMPS	0.87	0.4-1.14	0.35-1.355	3750	Y	MSM applications processor
S7	SMPS	0.915	0.4-1.14	0.32-2.04	2000	Y	VDDMX



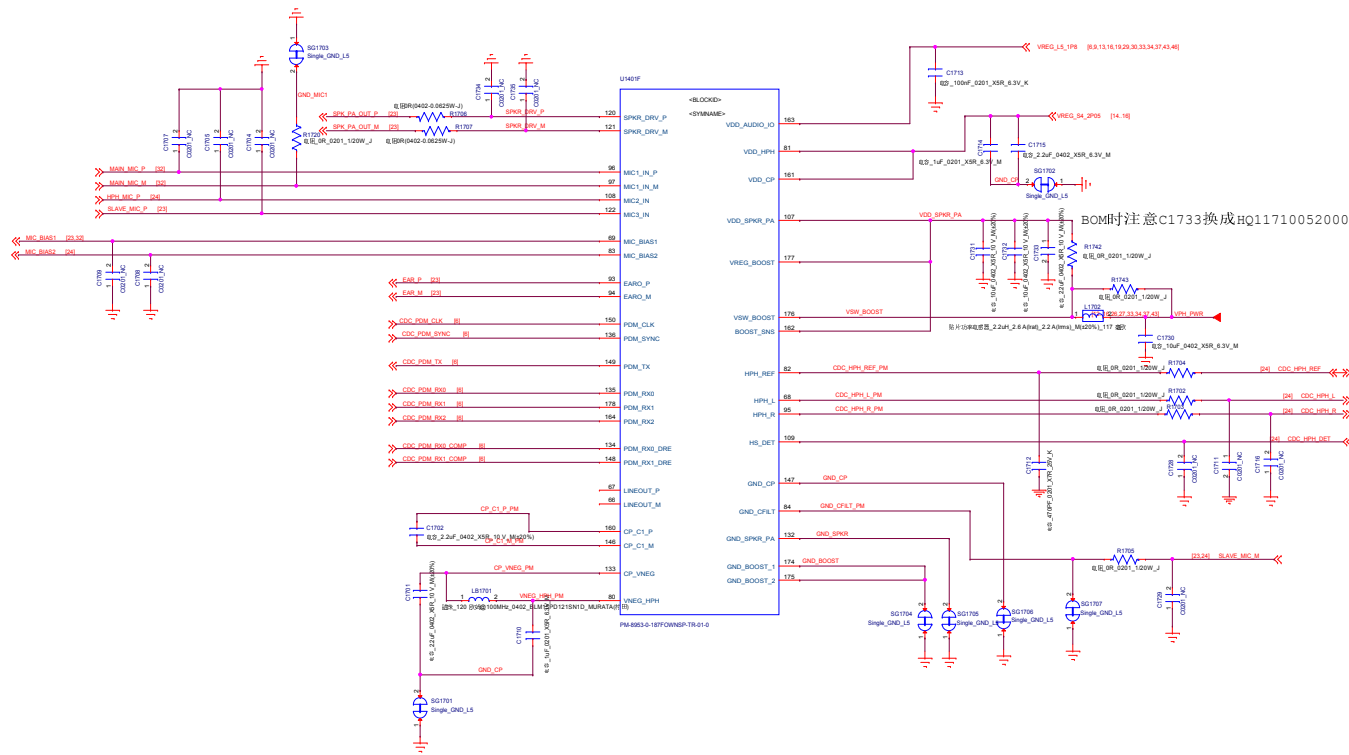
PM8953
SMPS

L1	NMOS LDO	1.0	1.0	0.375-1.5375	600	N	RFICs	
L2	NMOS LDO	1.100	1.100	0.375-1.5375	1200	N	Camera - Digital	
L3	NMOS LDO	0.925	0.925	0.375-1.5375	600	Y	MSM DSI PLL, USB	
L4	PMOS LDO	1.800	1.800	1.750-3.3375	450	N	RFICs and GPS eLNA	
L5	PMOS LDO	1.800	1.800	1.750-3.3375	600	Y	Most digital ICs, MSM pad groups 3 and 7, LFSR, and eMMC	
L6	PMOS LDO	1.800	1.800	1.750-3.3375	300	N	MSM QPPROM, camera, touchscreen, display, and sensors	
L7	PMOS LDO	1.800	1.800	1.750-3.3375	300	Y	MSM analog, USB, and PLLs, WCN XCI, and PM baseband clock driver	
L8	PMOS LDO	2.900	2.900	1.750-3.3375	600	Y	eMMC	
L9	PMOS LDO	Vout = 3.3 V for VBAT > 3.575 V; Vout = 3 V for VBAT < 3.575 V		3.000-3.300	1.750-3.3375	600	N	WCN

Function	Circuit type	Default V (V)	Specified range (V) (MSM8953)	Programmable range (V)	Rated current (mA)	Default ON	Expected use (MSM8953)
L10	PMOS LDO	3.0	3.0	1.750-3.3375	150	N	Sensors and touchscreen
L11	PMOS LDO	2.950	2.950	1.750-3.3375	800	Y	Micro SD
L12	PMOS LDO	2.950	1.800/2.950	1.750-3.3375	50	Y	MSM pad group 2 and SDIC
L13	PMOS LDO	3.125	3.125	1.750-3.3375	150	Y	MSM USB type C and PMIC and external codec audio
L14	PMOS LDO	1.800	1.800/3	1.750-3.3375	50	N	MSM pad group 5, dual-voltage UIM1, and NFC
L15	PMOS LDO	1.800	1.800/3	1.750-3.3375	50	N	MSM pad group 6 and dual-voltage UIM2
L16	PMOS LDO	1.800	1.800	1.750-3.3375	5	N	PMIC HKADC
L17	PMOS LDO	2.850	2.850	1.750-3.3375	300	N	Camera and display
L18	PMOS LDO	2.700	2.700	1.750-3.3375	150	N	QTI RF front-end
L19	NMOS LDO	1.350	1.350	0.375-1.5375	600	N	MSM analog, WCN, and WGR
L20	Low-noise LDO	1.74	1.74	1.74-3.3375	5	Y	PMIC XO circuits
L21	Low-noise LDO	1.74	1.74	1.74-3.3375	5	N	PMIC RF clock buffers
L22	PMOS LDO	2.800	2.800	1.750-3.3375	150	N	Camera - analog
L23	NMOS LDO	1.15	1.15	0.375-1.5375	600	N	Camera - digital



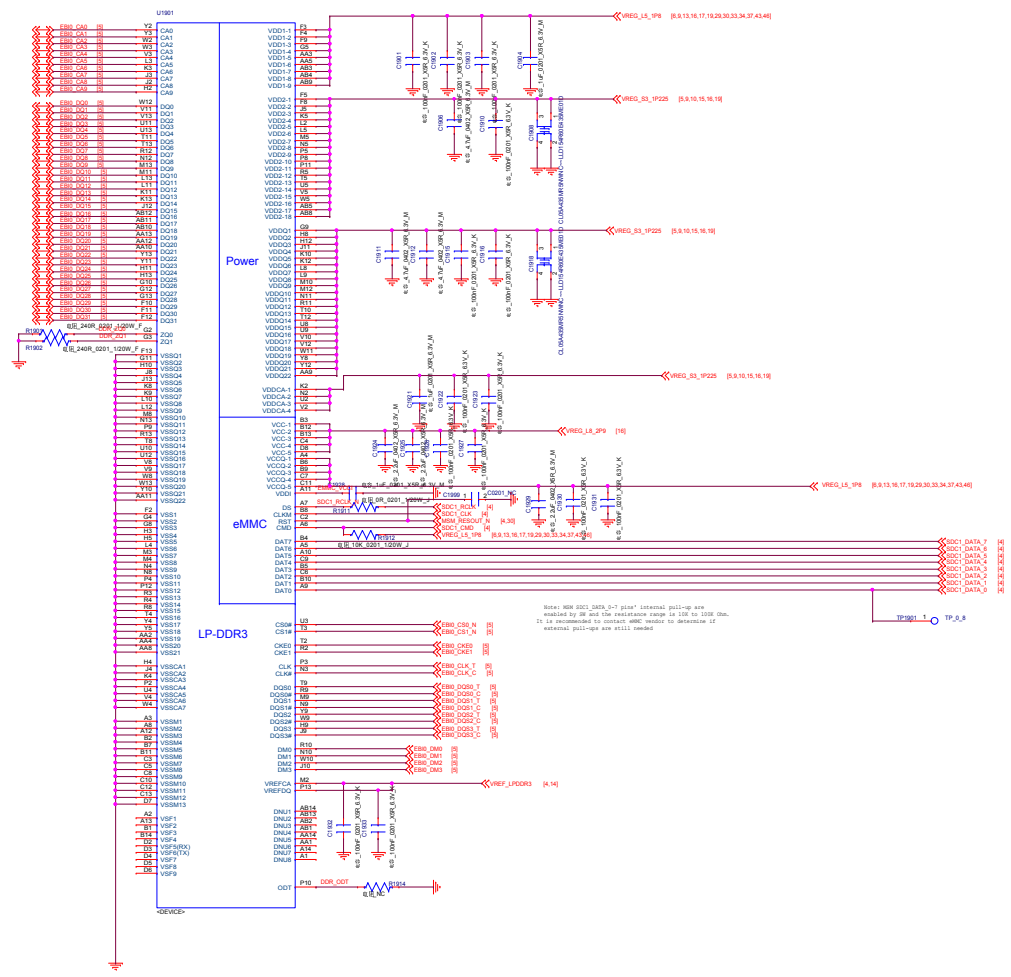
PM8953 LDO



BOM时注意C1733换成HQ11710052000, R1742、R1743如何选择? ?

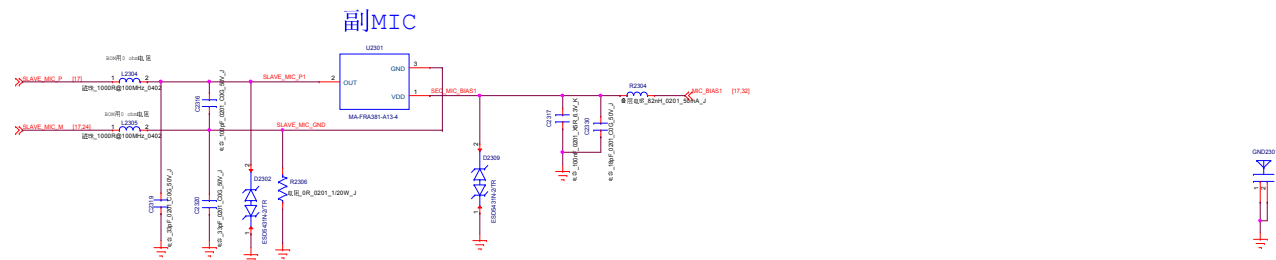
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Date	Wednesday, September 26, 2018 17:07:47

Company: Huesen		
Block Title	Test Points	
Project	Task	Rev 1.0
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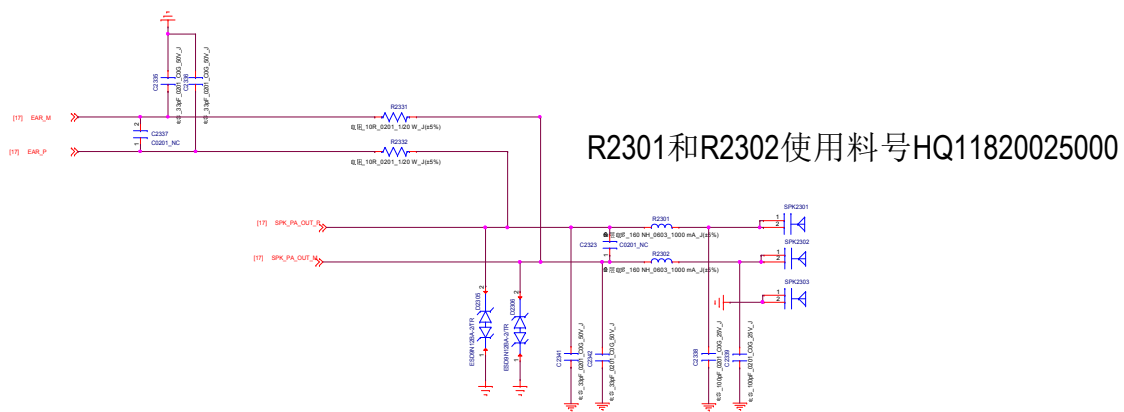


MEMORY(LPDDR3+EMMC V5.1)

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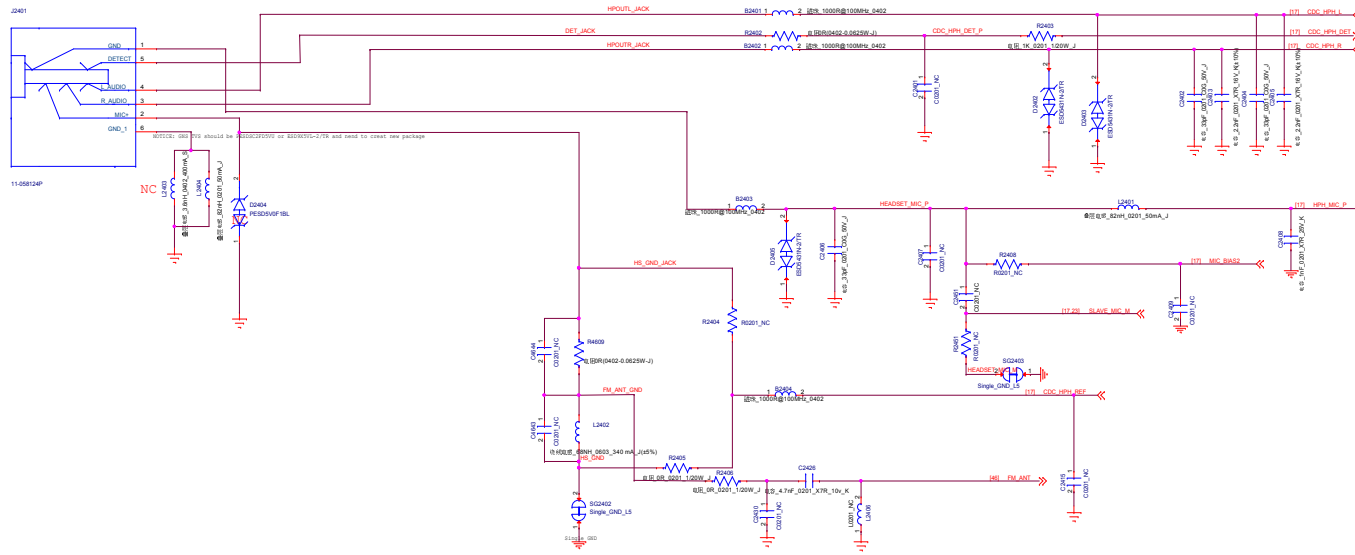


REV & Speaker

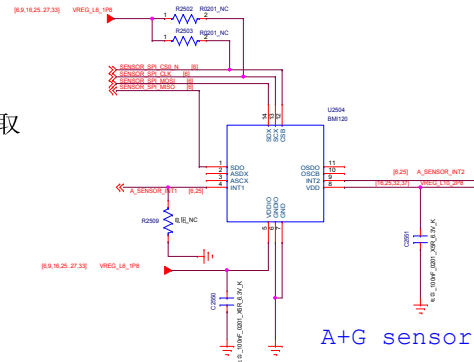
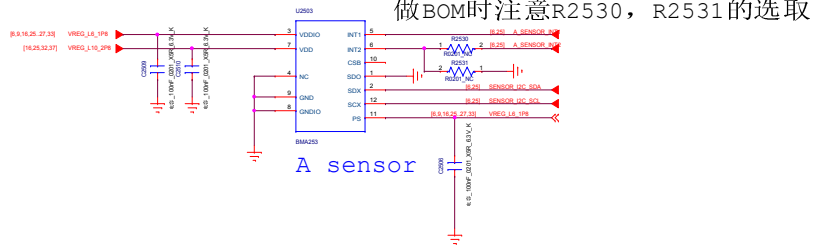


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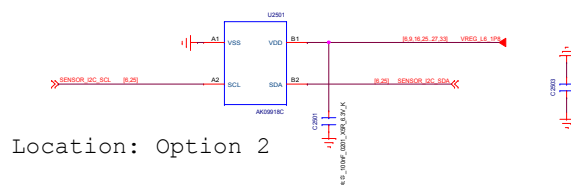
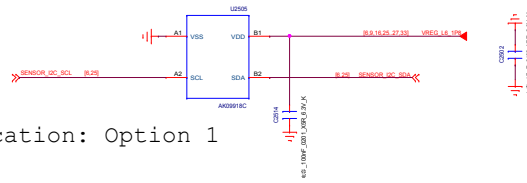
AUDIO JACK



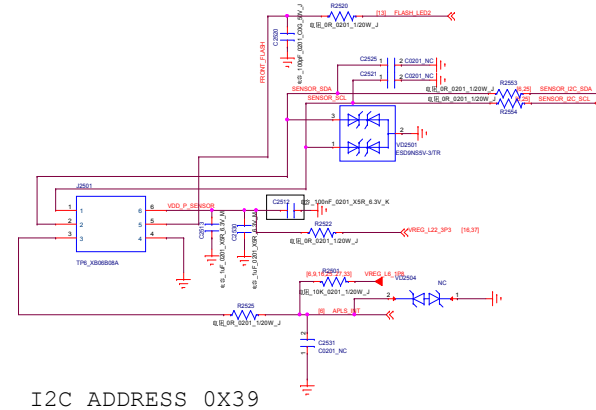
ACCELEROMETER



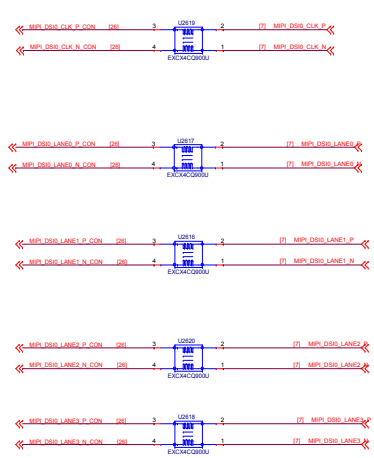
address of AK09918 is 0Ch



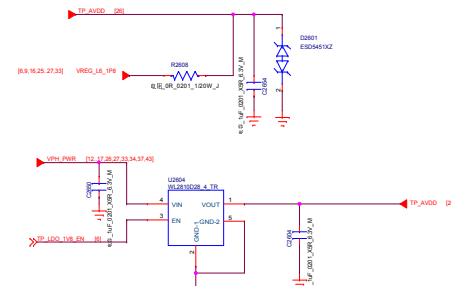
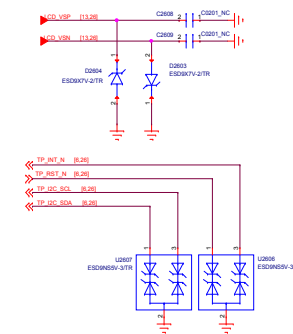
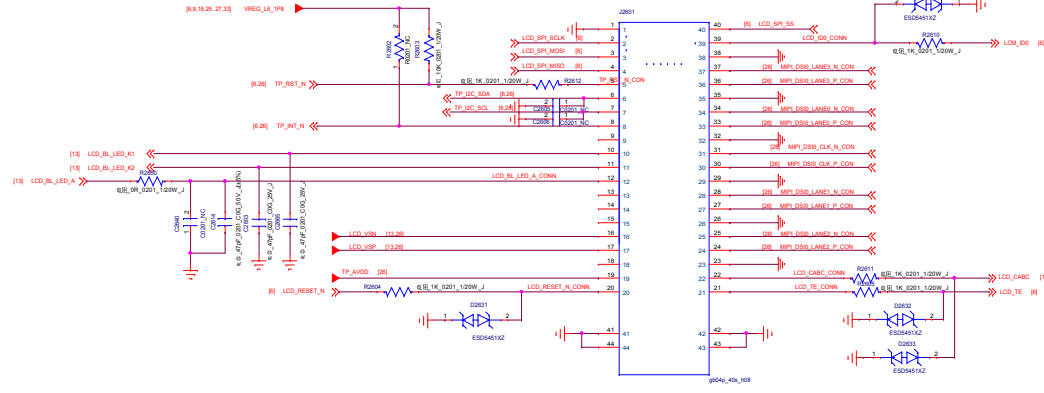
ALS/P SENSOR+Front Flash+Charge LED CONN



LCD MIPI Common Mode Filter

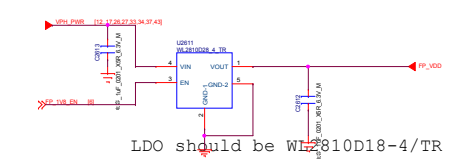
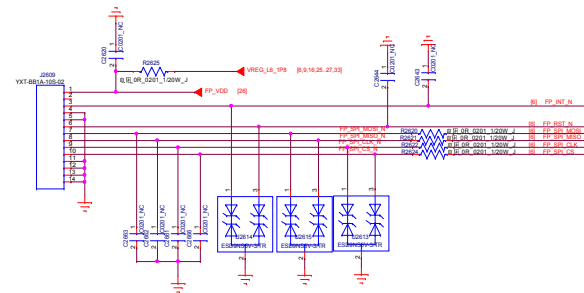


LCD/TP-Connector

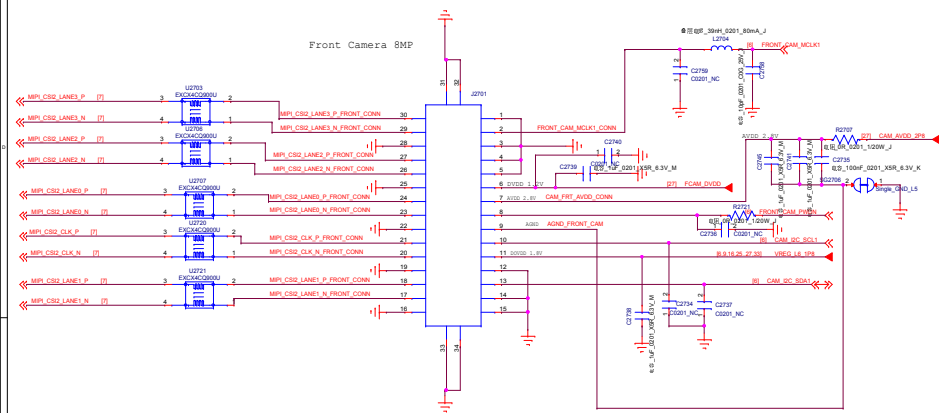


LDO should be WL2810D18-4/TR

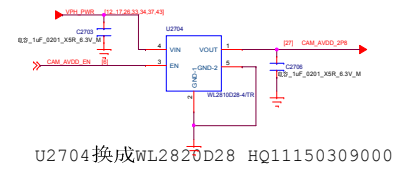
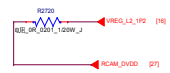
FPS-Connector



LDO should be WL2810D18-4/TR



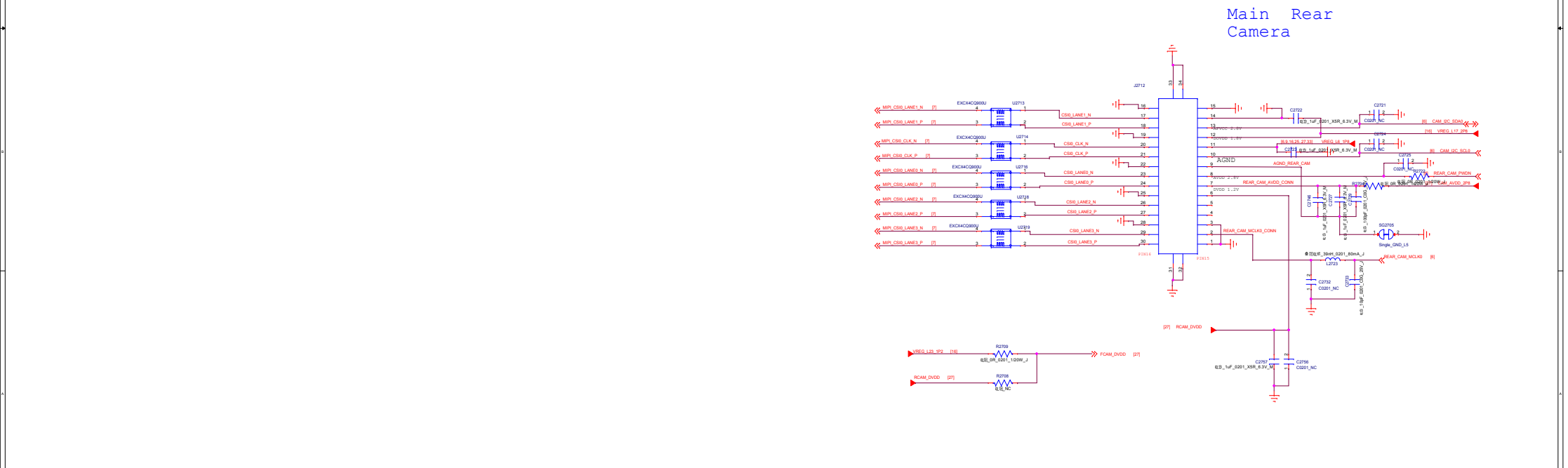
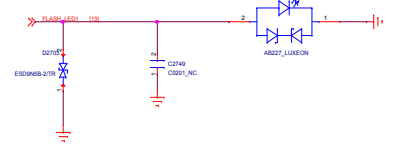
LDO



前闪

后闪

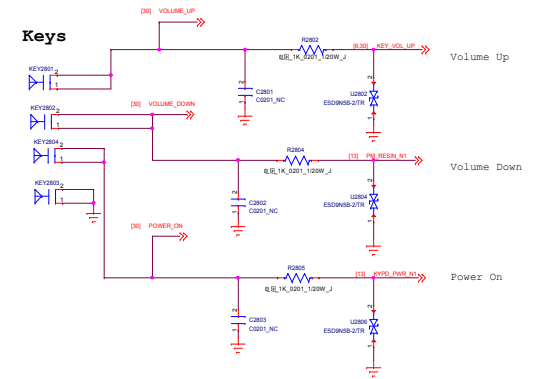
出BOM时注意使用OSLO-M评估的后闪光灯

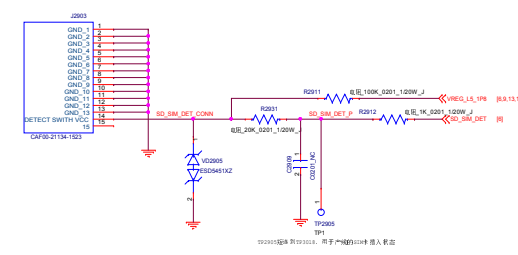
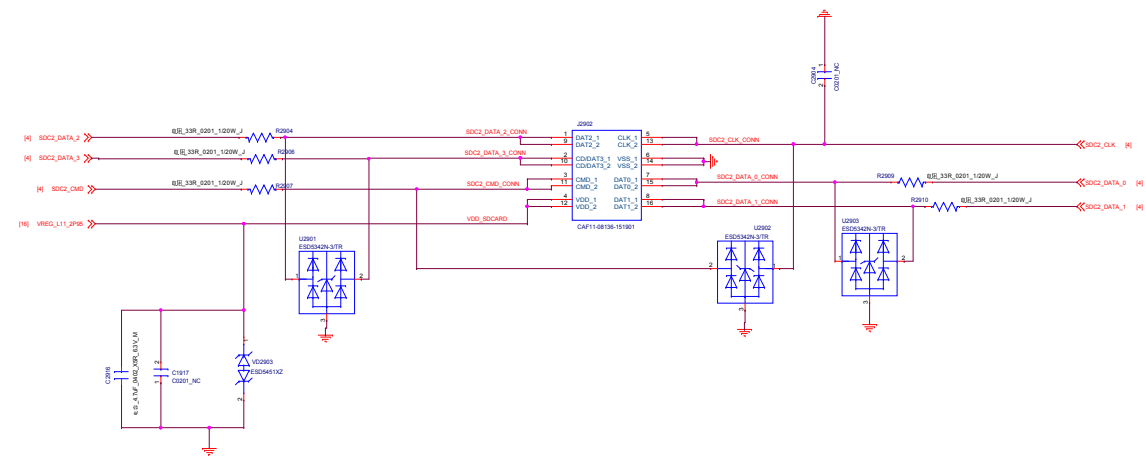
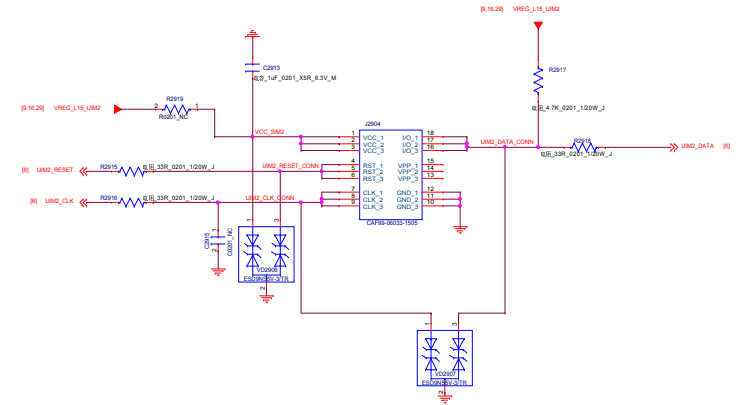
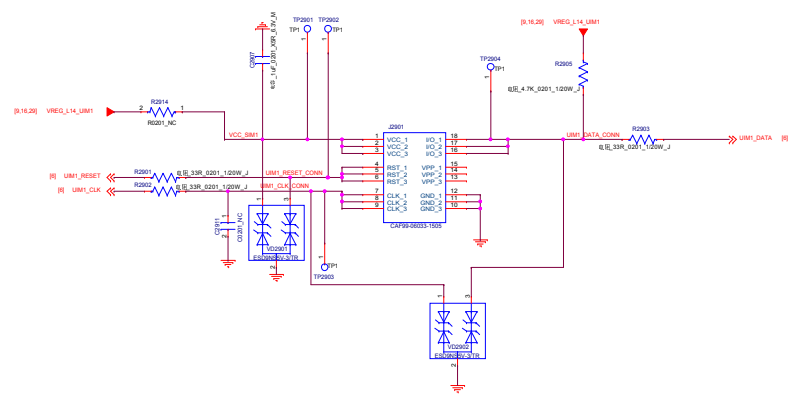


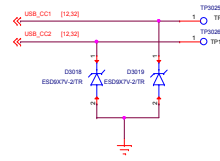
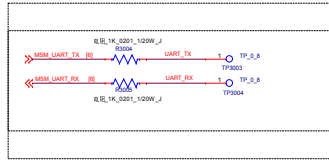
.Main/Slave Camera and Flash

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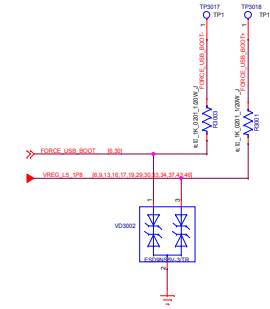
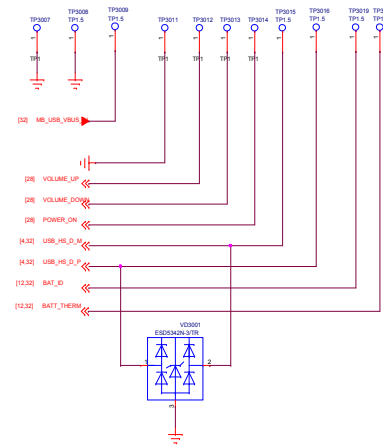
音量键&开关机键



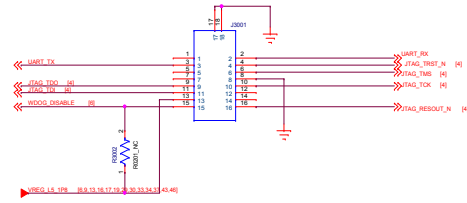




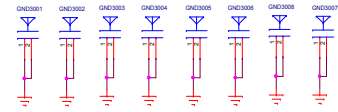
PCBA AUTOMATIC TEST point



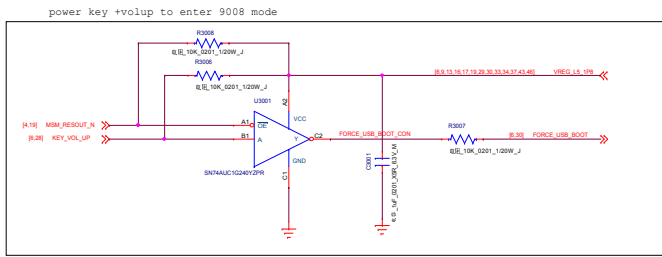
JTAG CONNECTOR



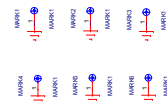
接地弹片 TOP



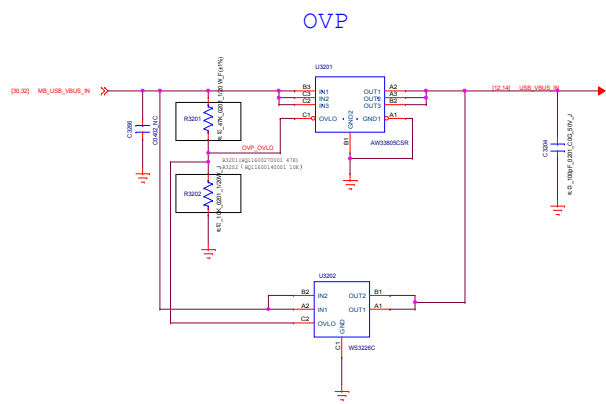
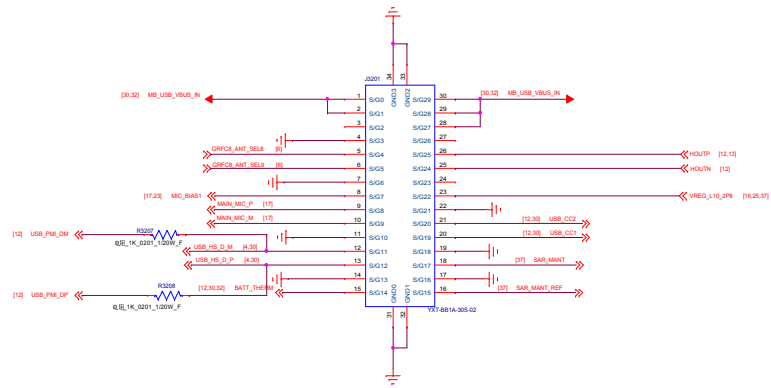
接地弹片 BOT



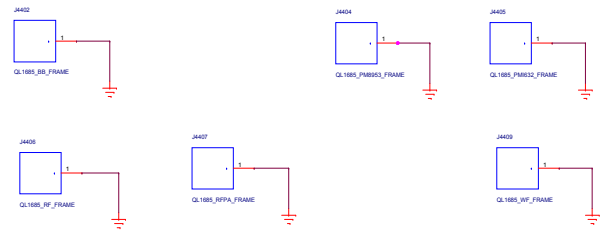
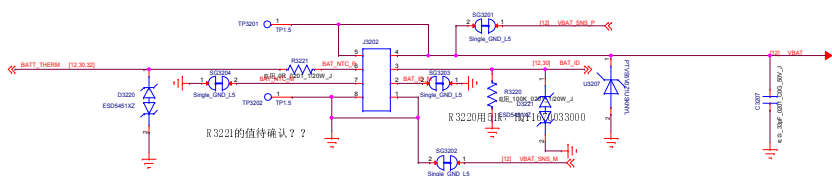
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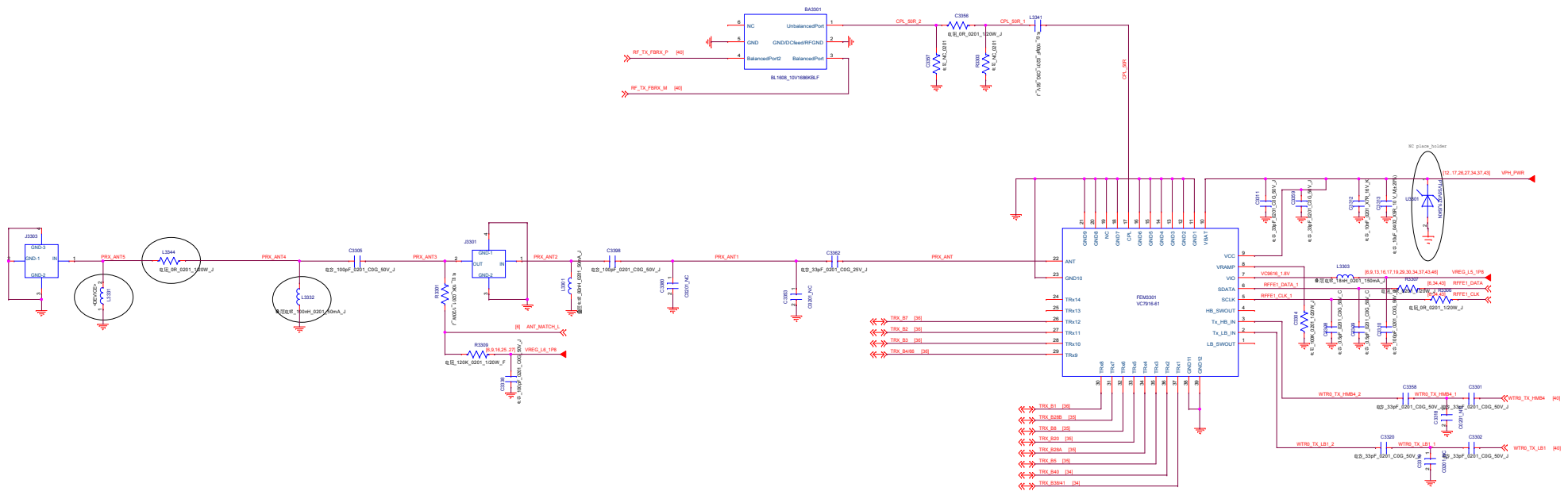


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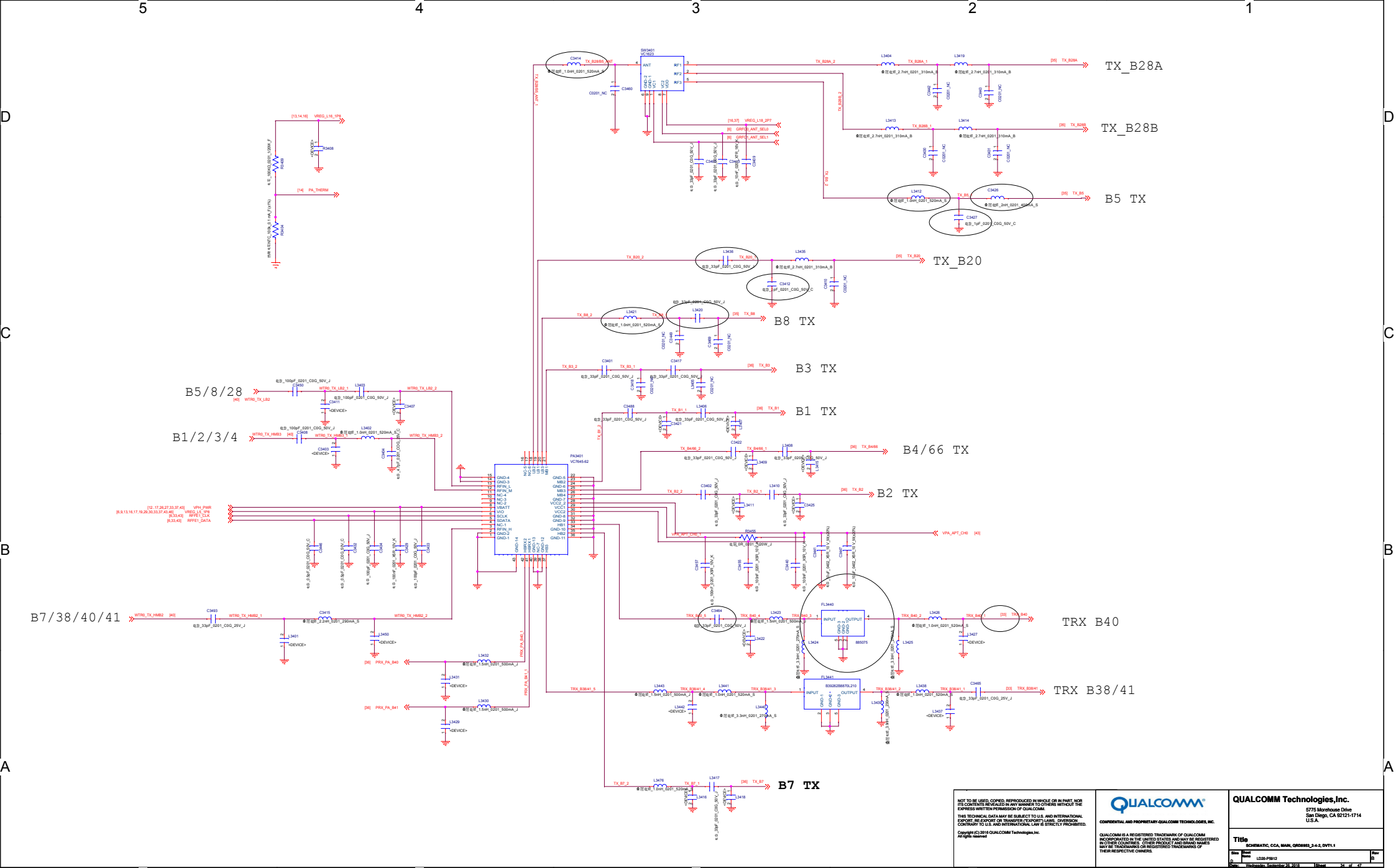
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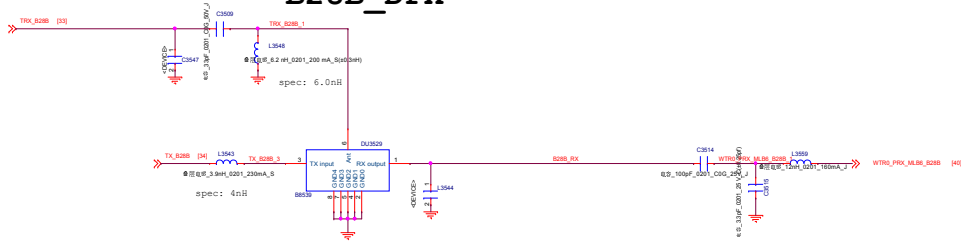
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 5775 Morehouse Drive
 San Diego, CA 92121-1714
 U.S.A.

Title
 SCHEMATIC, CCA, MARK, QRD9813_3-4-3, DVFL1

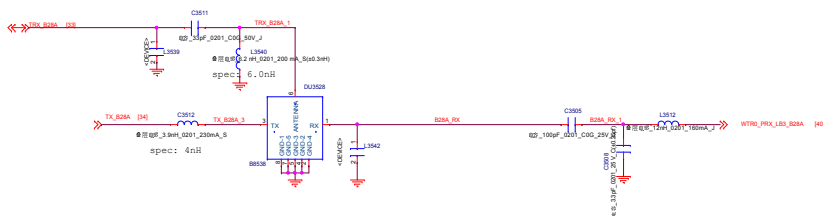


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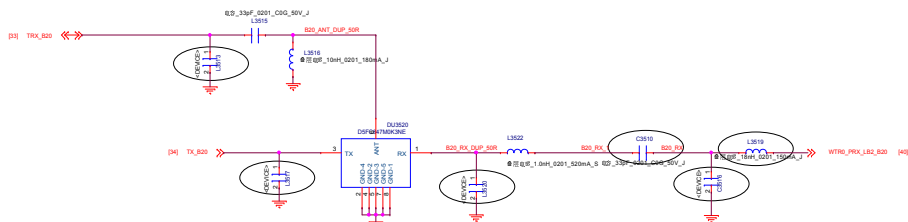
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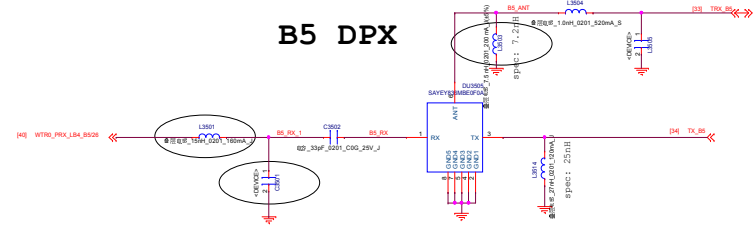
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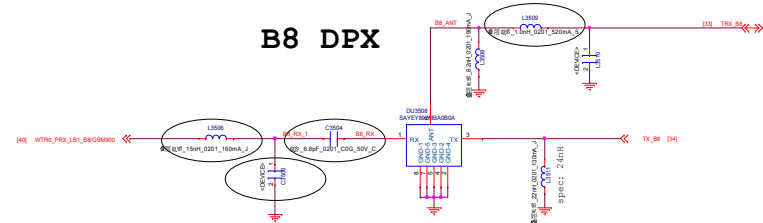
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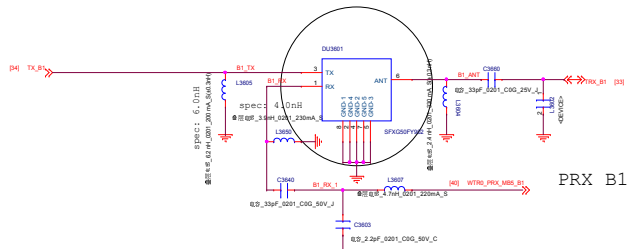


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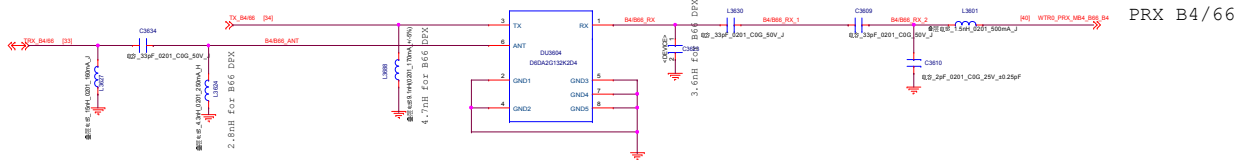
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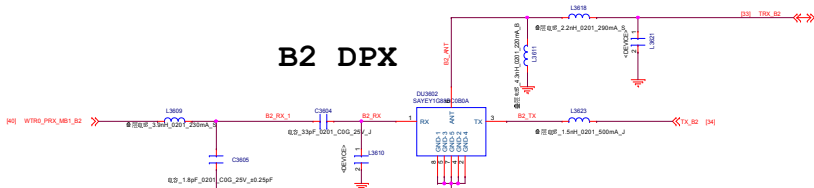


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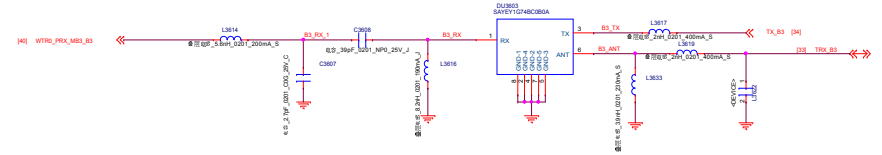
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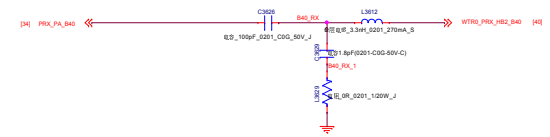
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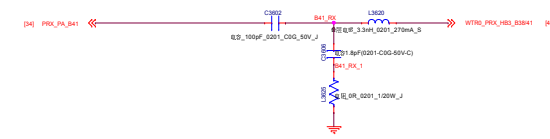
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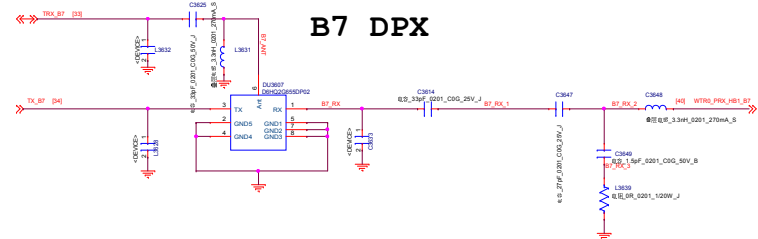
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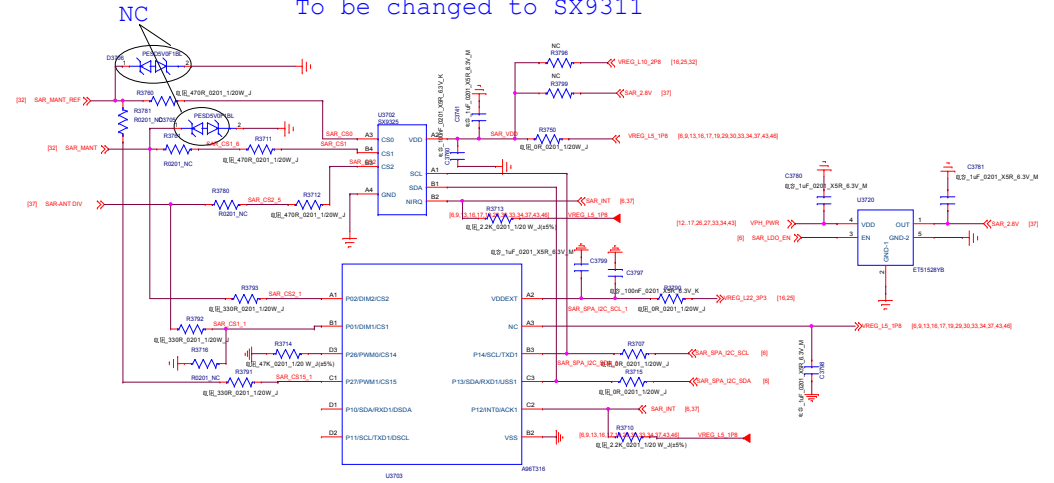
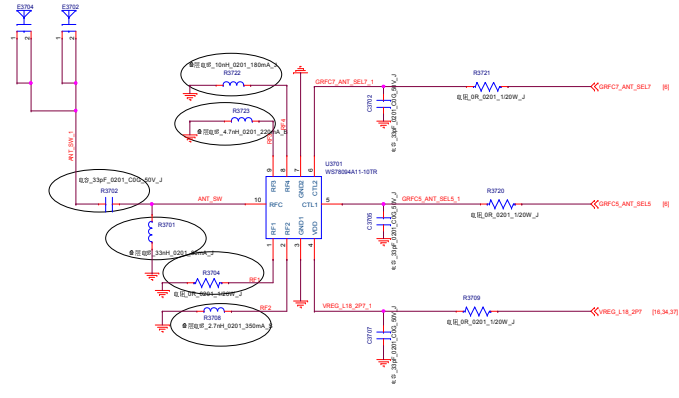


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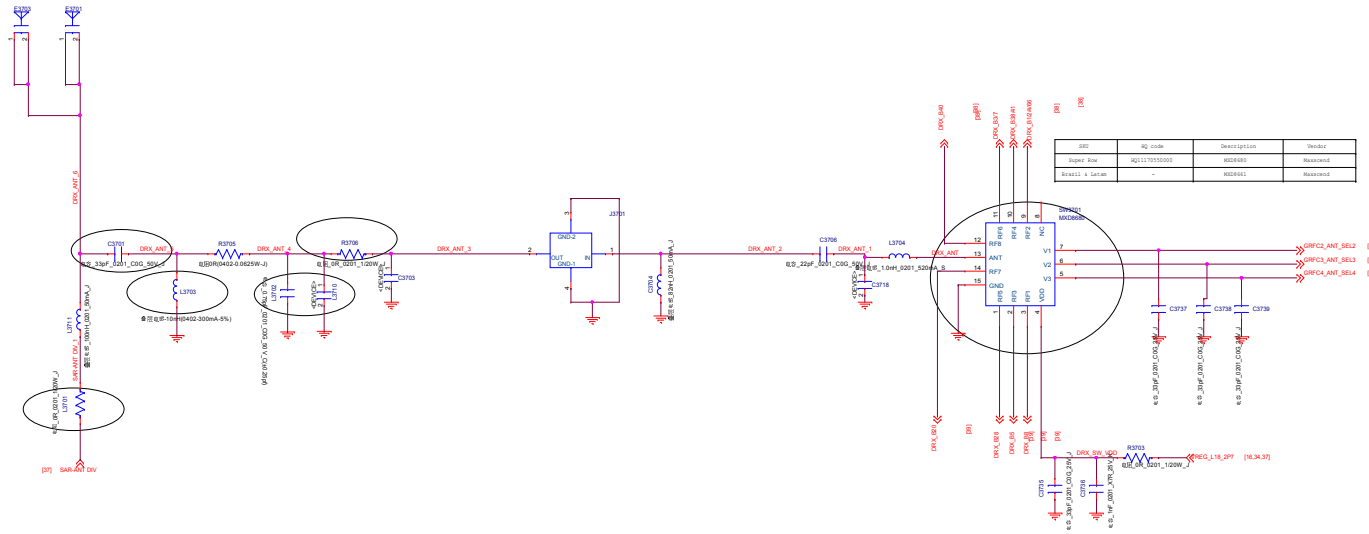


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Sar Sensor
To be changed to SX9311



兼容 SX9311 A96T356BW设计



Part	QTY	Description	Vendor
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DRX1_DRX	1	U3701 DRX	Maxim
DRX11_DRX	1	U3701 DRX	Maxim

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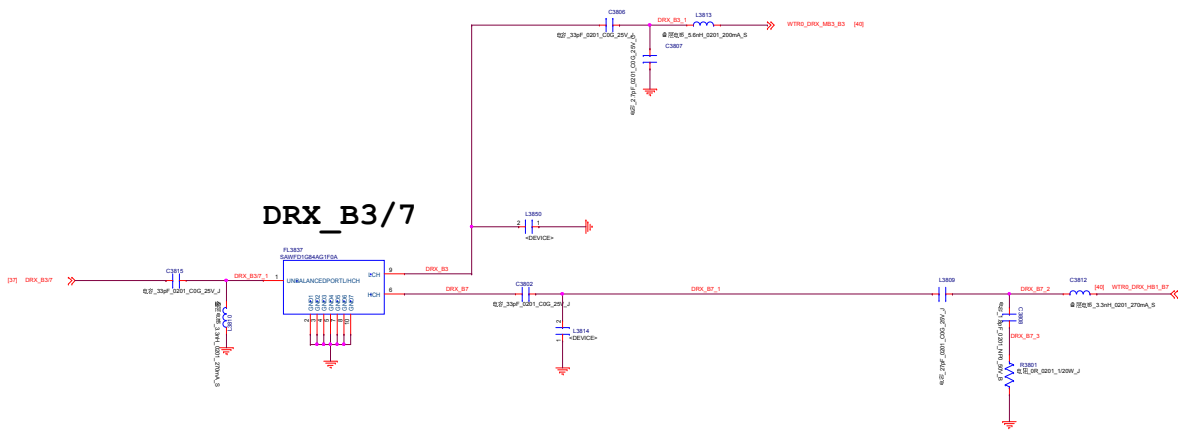
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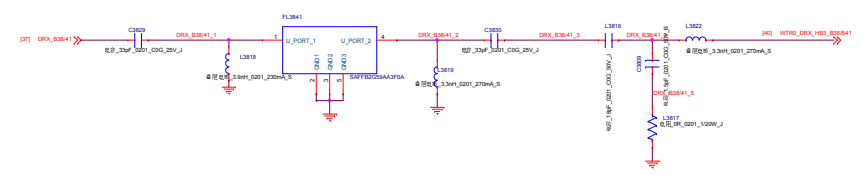
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Wednesday, September 24, 2015

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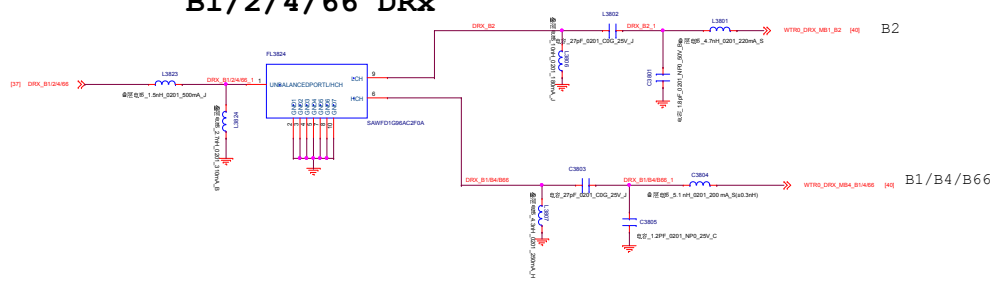


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B38/41 DRx



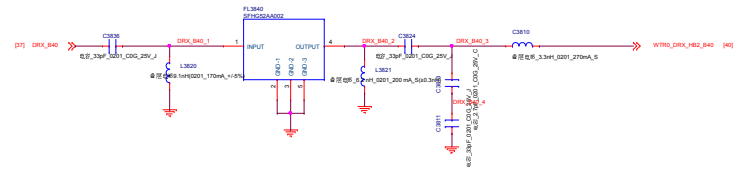
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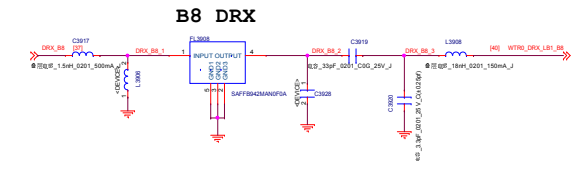
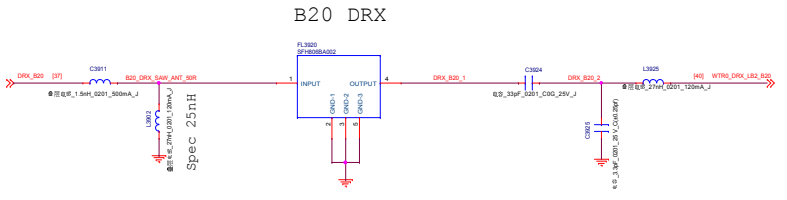
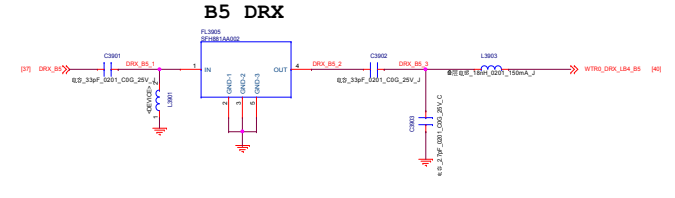
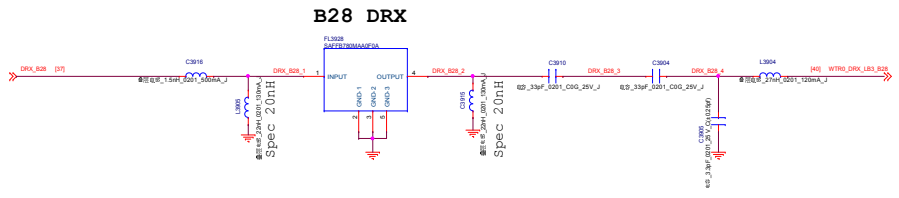


B2

B1/B4/B66

B40 DRx





DRX LB

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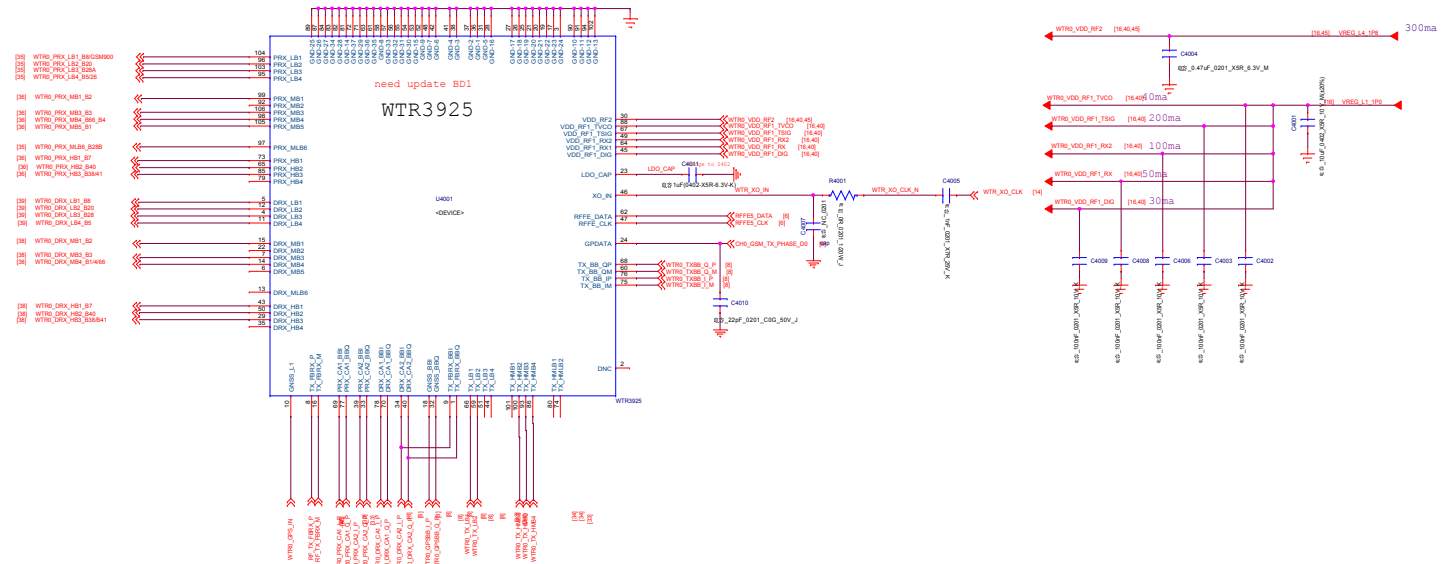
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TH0		SCHEMATIC, CCA, MAM, QDRM3_3-4-1, DVFL1	
Site	Lead	Rev	0
Doc	Lead	Date	Wednesday, September 26, 2018 10:41 AM



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Date:	Wednesday, September 26, 2018	Sheet 42 of 47

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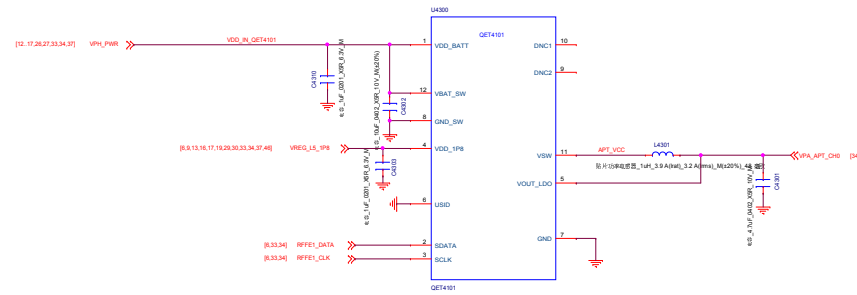
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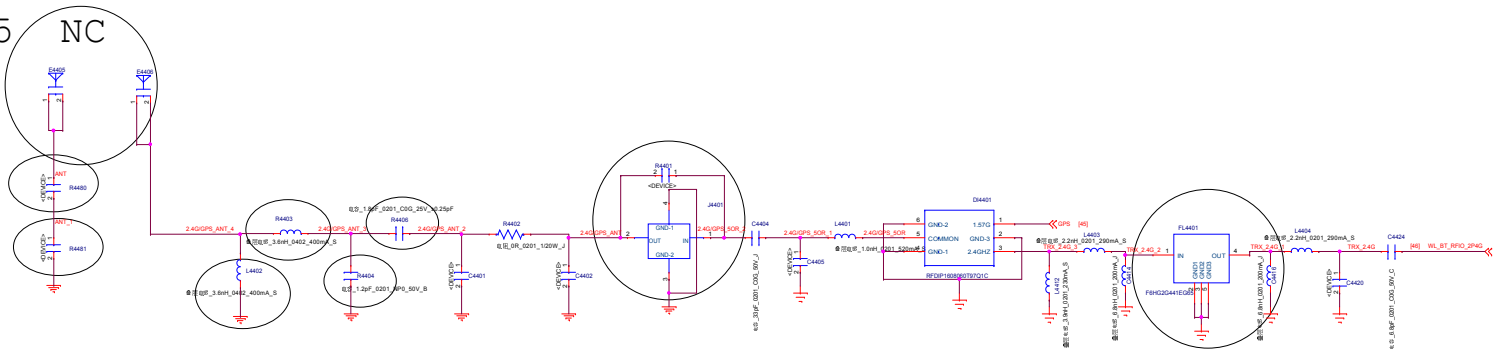
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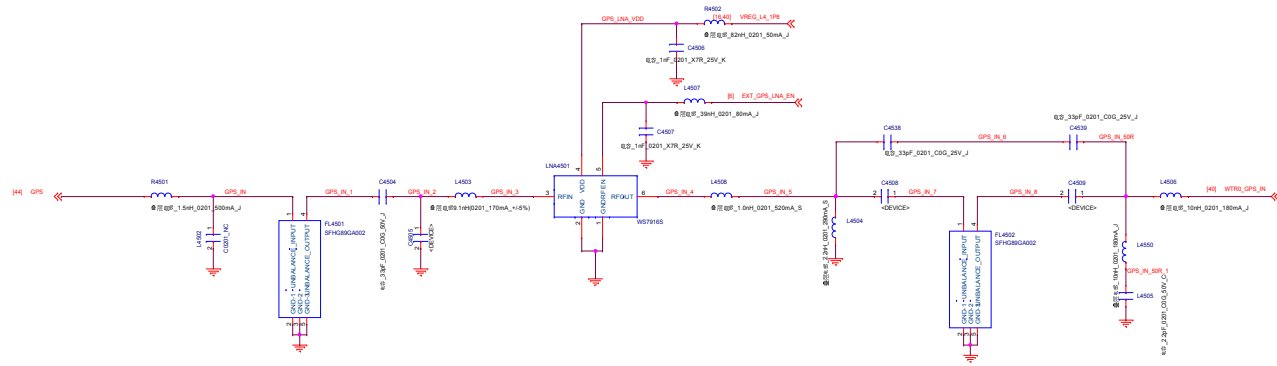
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WLAN_FEM

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<p>Part No. L004-PS912</p>	<p>Rev. 1</p>	<p>Date Wednesday, September 24, 2015</p>	<p>Page 24 of 47</p>

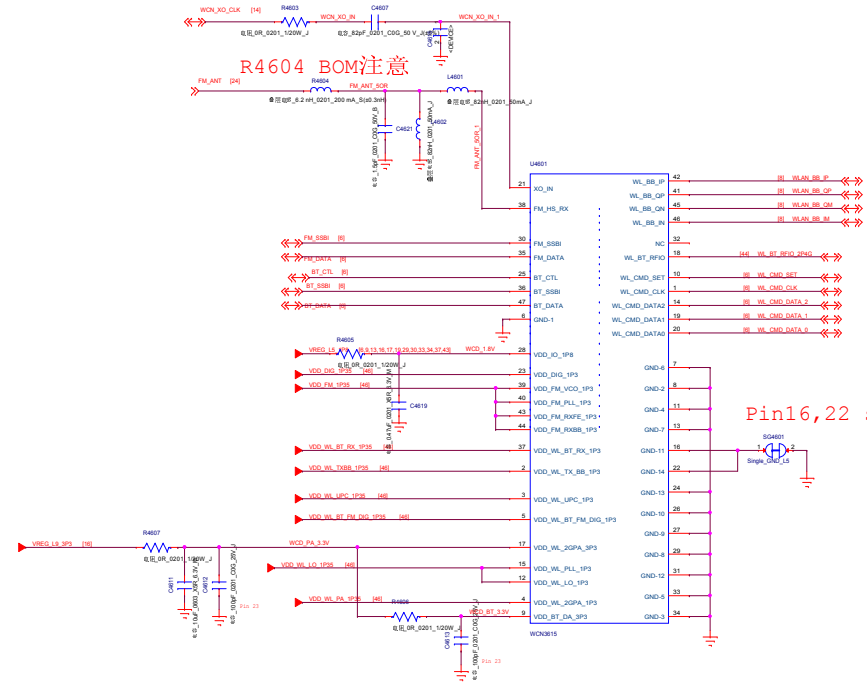
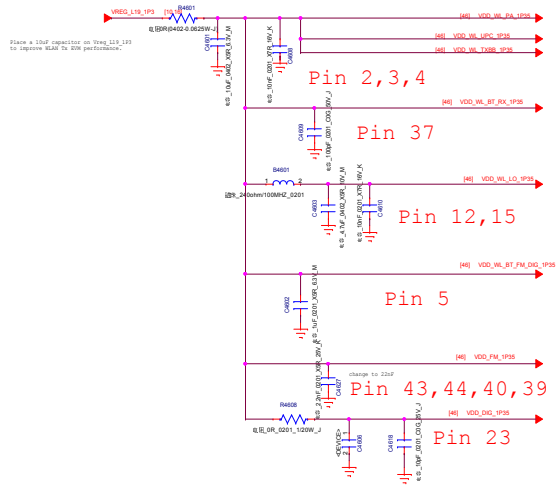
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WCN3615



Pin16, 22 single ground

WCN3680B

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