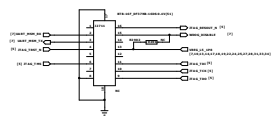
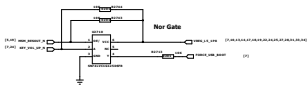
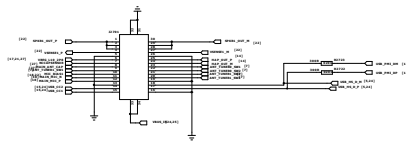
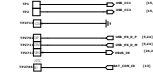




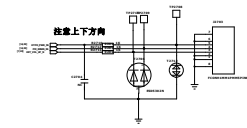
# SUB\_FPC



## Test point

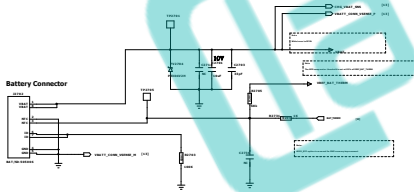


# Power Key



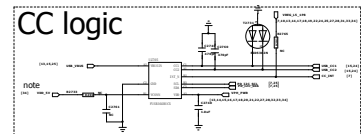
Schematic design notice of "05\_PERI\_Dual\_SIM\_LOAD\_EXTFAP" page.  
 Note ES-1: DO NOT pull pull-up resistor on PWRKEY  
 Note ES-2: Volume Up : NCME key / GND, Volume Down : (DIFFERENTIAL) or RPOC0 / GND

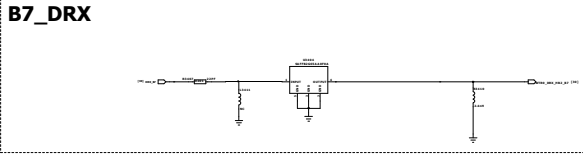
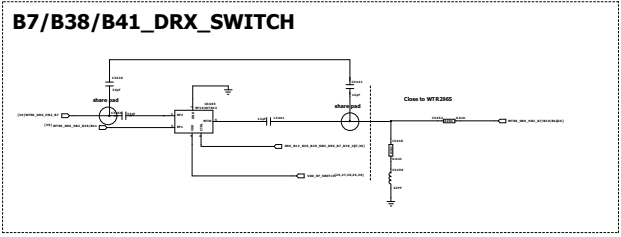
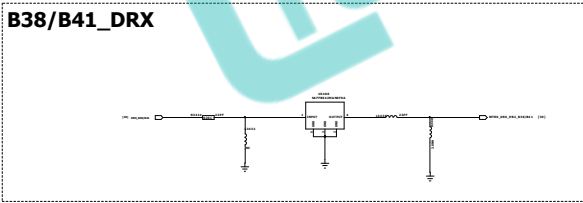
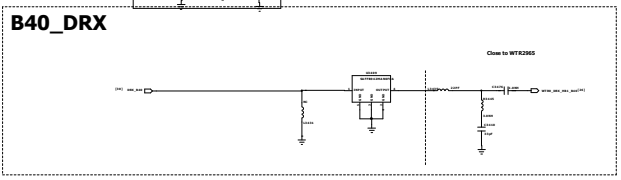
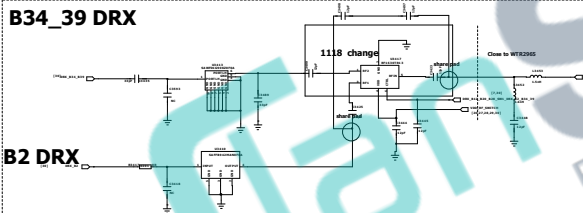
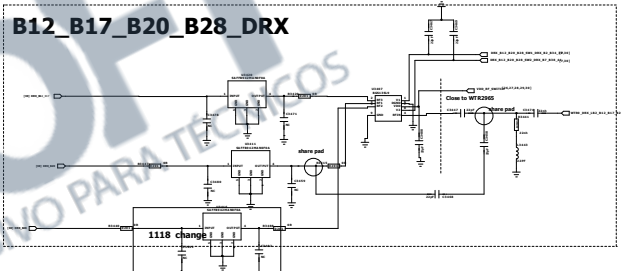
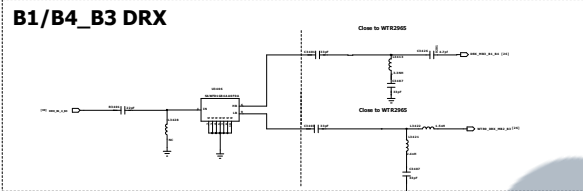
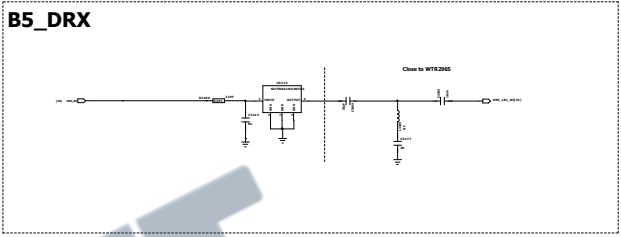
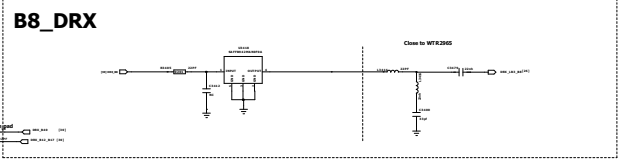
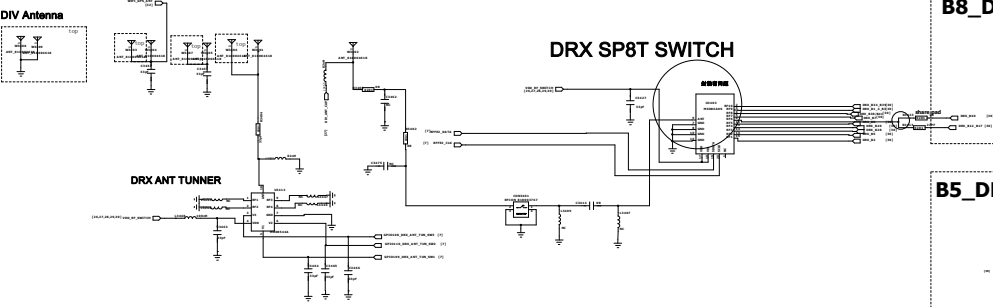
# BAT



Note:  
 Battery ID resistor value require 20K~100K.  
 Add 100K to ground of battery package ID not meet requirement

## CC logic





EXCLUSIVO PARA TÉCNICOS