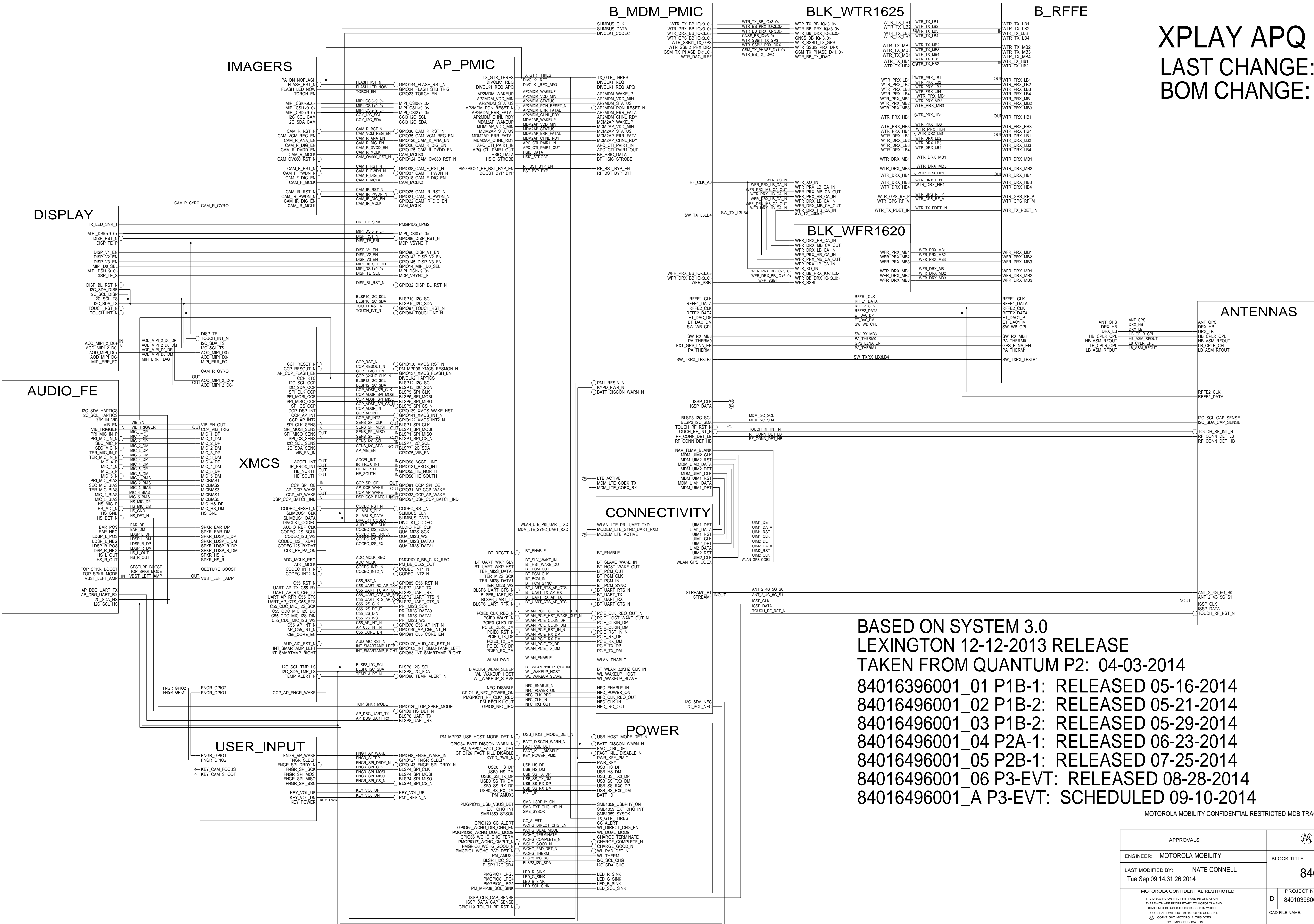


XPLAY APQ P3 TOP

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED

XPLAY APQ EVT (REV A)
LAST CHANGE: 08-25-2014
BOM CHANGE: 09-08-2014



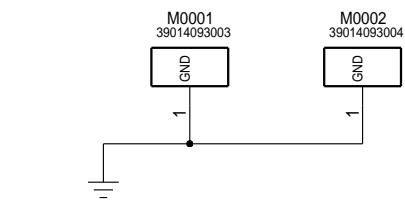
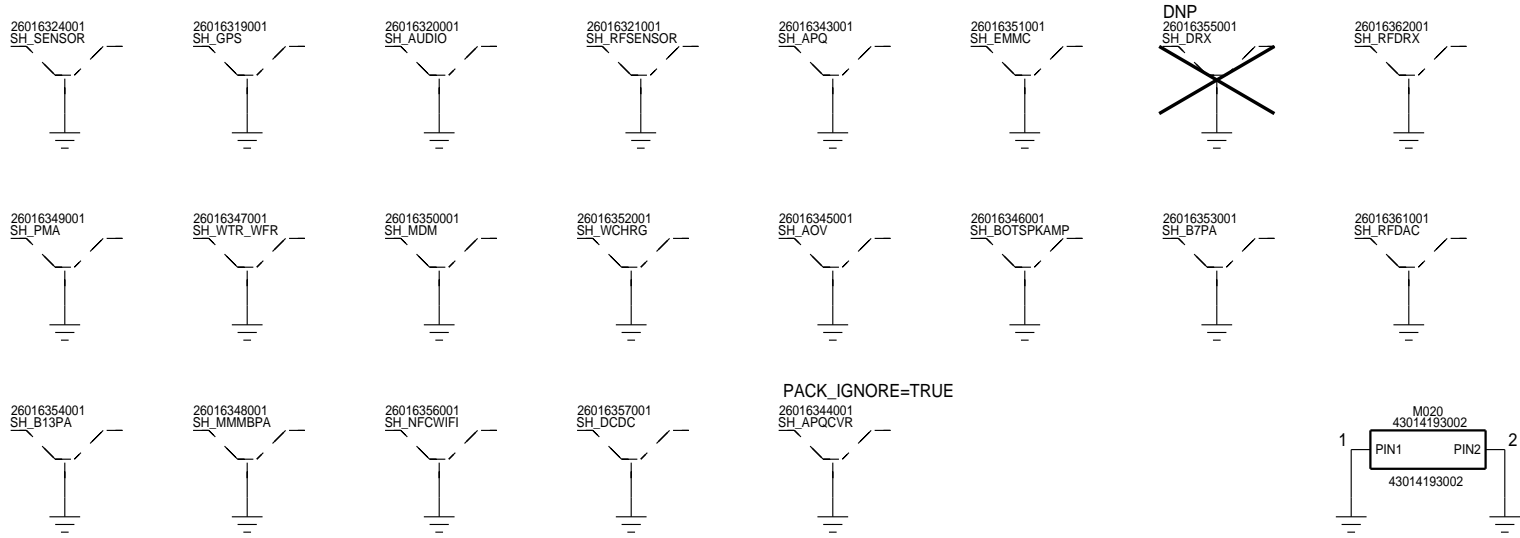
BASED ON SYSTEM 3.0
LEXINGTON 12-12-2013 RELEASE
TAKEN FROM QUANTUM P2: 04-03-2014
84016396001_01 P1B-1: RELEASED 05-16-2014
84016496001_02 P1B-2: RELEASED 05-21-2014
84016496001_03 P1B-2: RELEASED 05-29-2014
84016496001_04 P2A-1: RELEASED 06-23-2014
84016496001_05 P2B-1: RELEASED 07-25-2014
84016496001_06 P3-EVT: RELEASED 08-28-2014
84016496001_A P3-EVT: SCHEDULED 09-10-2014

MOTOROLA MOBILITY CONFIDENTIAL RESTRICTED-MDB TRACKED TRACKEDANDBLOCKED

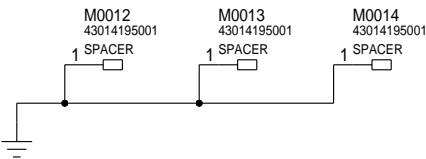
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LAST MODIFIED BY: NATE CONNELL Tue Sep 09 14:31:26 2014		PROJECT NAME: 84016396001_a.cpm	REV 01
MOTOROLA CONFIDENTIAL RESTRICTED		CAD FILE NAME:	PAGE 1 OF 80
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XPLAY APQ ME/SHIELDS

SHIELDS



BACK SIDE CHASIS GROUND CLIPS




FRONT SIDE SMT BOSSES

REFERENCE DESIGNATOR RANGES BY BLOCK

- 000 - 499: ANTENNA
- 500 - 599: USER INPUT
- 600 - 799: POWER
- 1000-2999: APQ8084 + PMA8084
- 3000-3199: WFR1620 + WTR1605
- 3200-3999: MDM9625 + PM8019
- 4000-4499: CONNECTIVITY
- 4500-4999: AUDIO FE
- 5000-5499: DISPLAY
- 5500-5999: CAMERA
- 6000-6999: XMCS
- 7000-8999: RFFE
- 9000-9999: PRODUCT-SPECIFIC (FILTERING, 100PF CAPS)

MOTOROLA MOBILITY CONFIDENTIAL RESTRICTED-MDB TRACKED TRACKEDANDBLOCKED

APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER:		BLOCK TITLE:	
LAST MODIFIED BY: Fri Sep 26 06:13:03 2014		84016396001_a	
MOTOROLA CONFIDENTIAL RESTRICTED THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCUSSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © COPYRIGHT, MOTOROLA. THIS DOES NOT IMPLY PUBLICATION		B PROJECT NAME 84016396001_a.cpm	REV 01
CAD FILE NAME:			PAGE 2 OF 80

RFFE: TRX SWITCH

REF 7100-8499

D

C

B

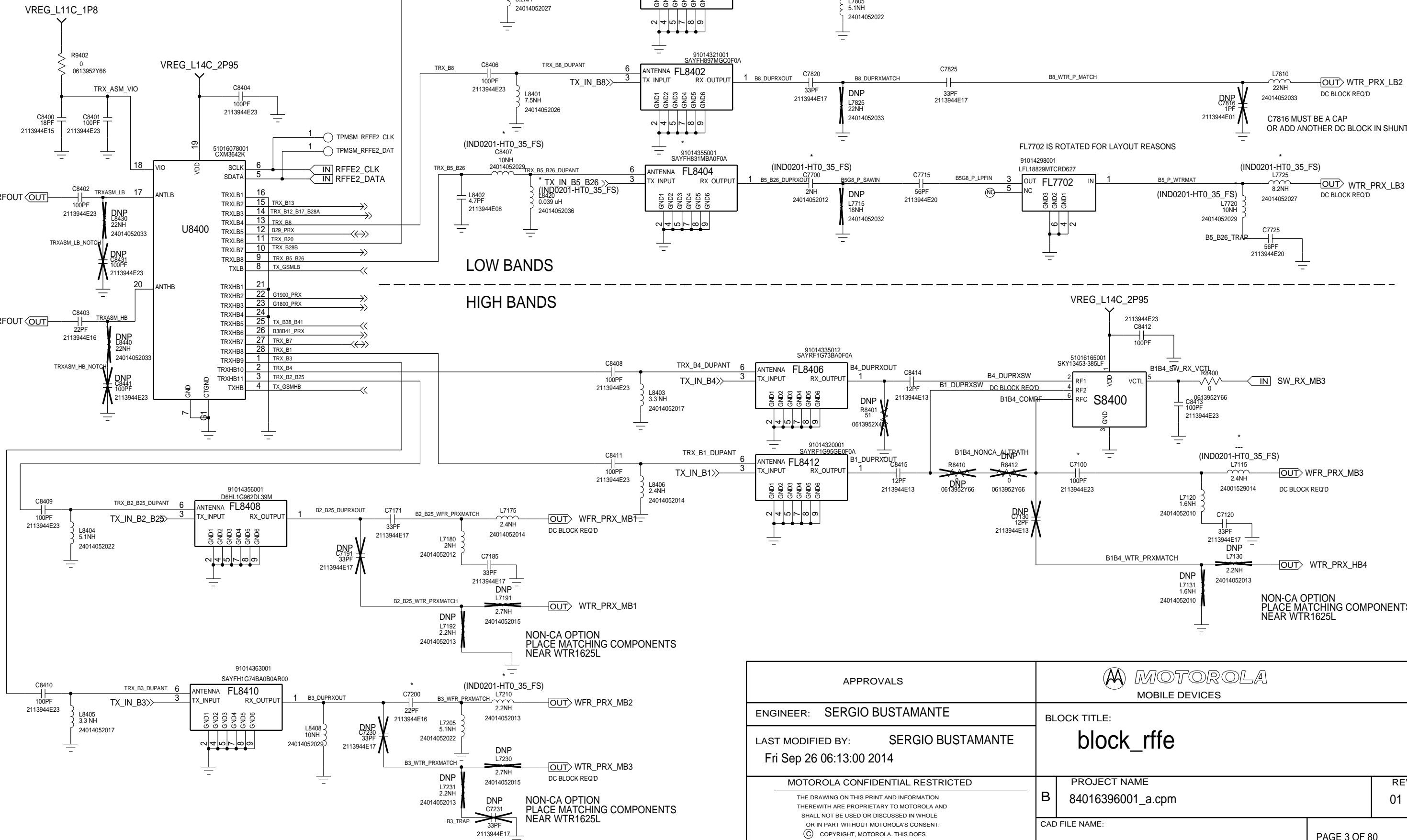
A


D

C

B

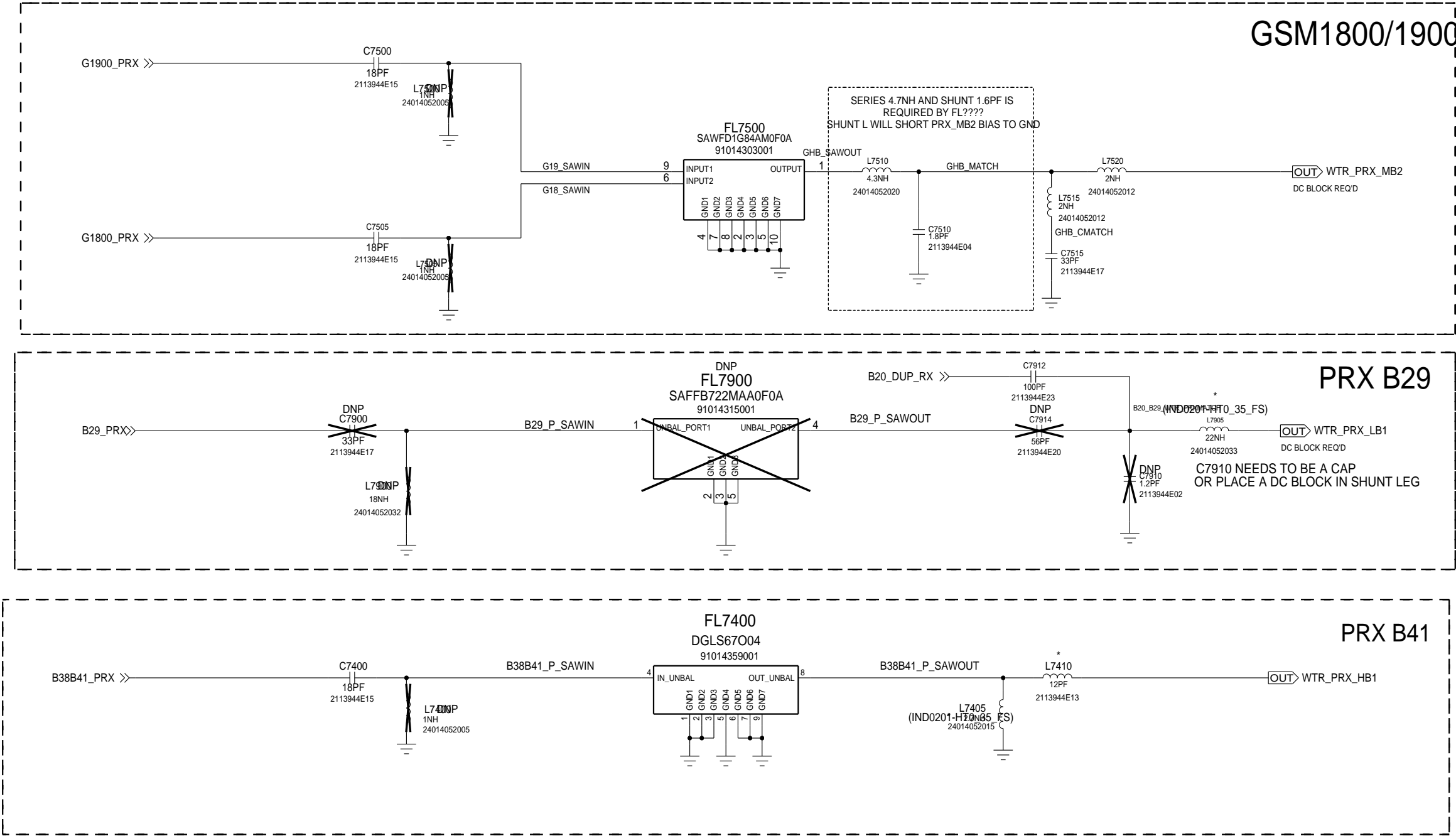
A




APPROVALS		 MOBILE DEVICES	
ENGINEER: SERGIO BUSTAMANTE		BLOCK TITLE: block_rffe	
LAST MODIFIED BY: SERGIO BUSTAMANTE Fri Sep 26 06:13:00 2014			
MOTOROLA CONFIDENTIAL RESTRICTED			
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PROJECT NAME 84016396001_a.cpm		REV 01	
CAD FILE NAME:		PAGE 3 OF 80	

RFFE: PRX B29/B41/GSM1800/GSM1900

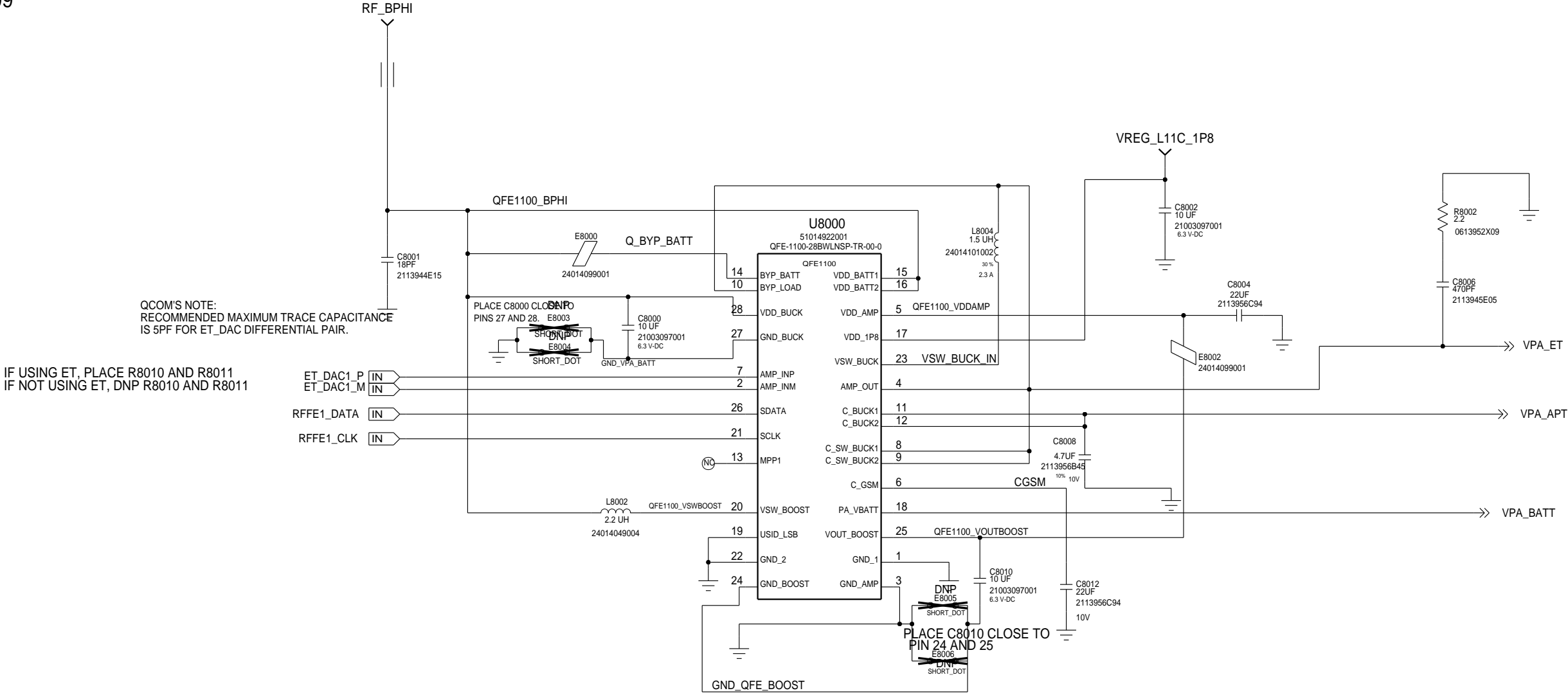
REF 7100-8499



APPROVALS		 MOTOROLA MOBILE DEVICES	
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LAST MODIFIED BY: SERGIO BUSTAMANTE Fri Sep 26 06:13:00 2014		block_rfe	
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		CAD FILE NAME:	
		PAGE 4 OF 80	

RFFE: APT/ET

REF 8000-8099




QCOM'S NOTE:
RECOMMENDED MAXIMUM TRACE CAPACITANCE
IS 5PF FOR ET_DAC DIFFERENTIAL PAIR.

IF USING ET, PLACE R8010 AND R8011
IF NOT USING ET, DNP R8010 AND R8011

QCOM'S NOTES:

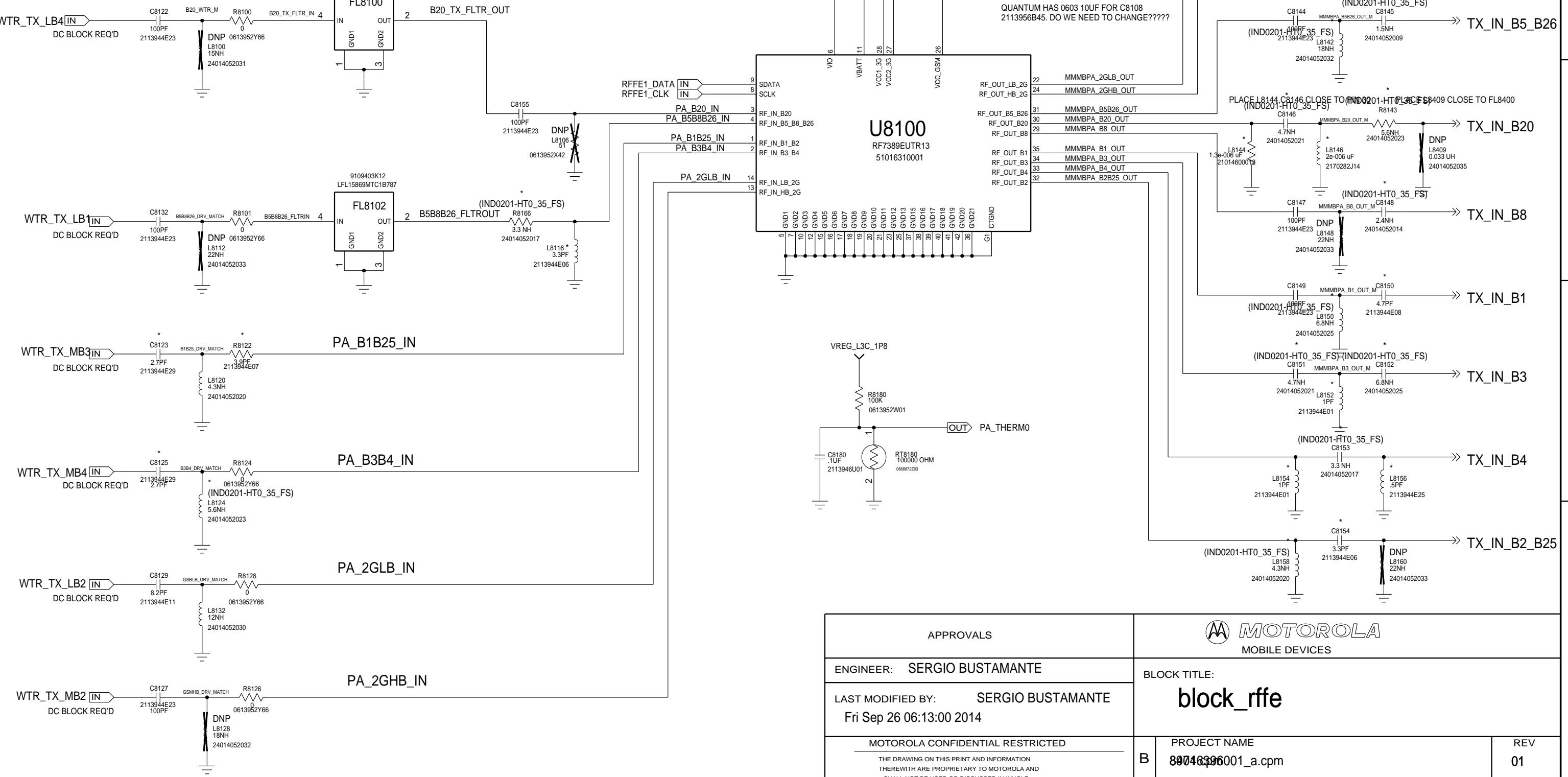
- QFE1100 ES2 PIN 13 CONNECTION TO PMIC HKADC IS FOR ET ENVELOP GAIN CALIBRATION AND QFE1100 INTERNAL TEMPERATURE SENSOR READING.
- WHEN QFE1100 ES2 PIN13 IS CONNECTED TO PMIC HKADC, PULL-UP RESISTOR ON BB SIDED SHOULD BE REMOVED FOR PM8019 (MDM9X25), OR SOFTWARE SHOULD DISABLE PULL-UP PULL-UP ON PM8941, PIN195 (MSM8974).
- FULL SUPPORT OF LOW VOLTAGE BATTERY WITH LTE 1RB IS AVAILABLE ON ES2 SAMPLE (FEB. 2013). ES2 COMBINED WITH L8001 AND C8005 CHANGE SUPPORTS LOW VOLTAGE BATTERY OVER ALL TECHNOLOGIES WHICH SUPPORT ET.


PLEASE REFER TO 80-NA681-5 AND 80-NA681-91 FOR QFE1100
CRITICAL LAYOUT GUIDE LINES IN MORE DETAIL.

APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER:		BLOCK TITLE:	
LAST MODIFIED BY:		block_rfe	
Fri Sep 26 06:13:00 2014			
MOTOROLA CONFIDENTIAL RESTRICTED		PROJECT NAME	
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		REV 01	
		CAD FILE NAME:	
		PAGE 5 OF 80	

RFFE: TX MMBB PA
REF 8100-8199

WTR_TX_LB4 IS SHARED WITH B13
PLACE C8122 AND C8328 SUCH THAT THEY SHARE COMMON PAD



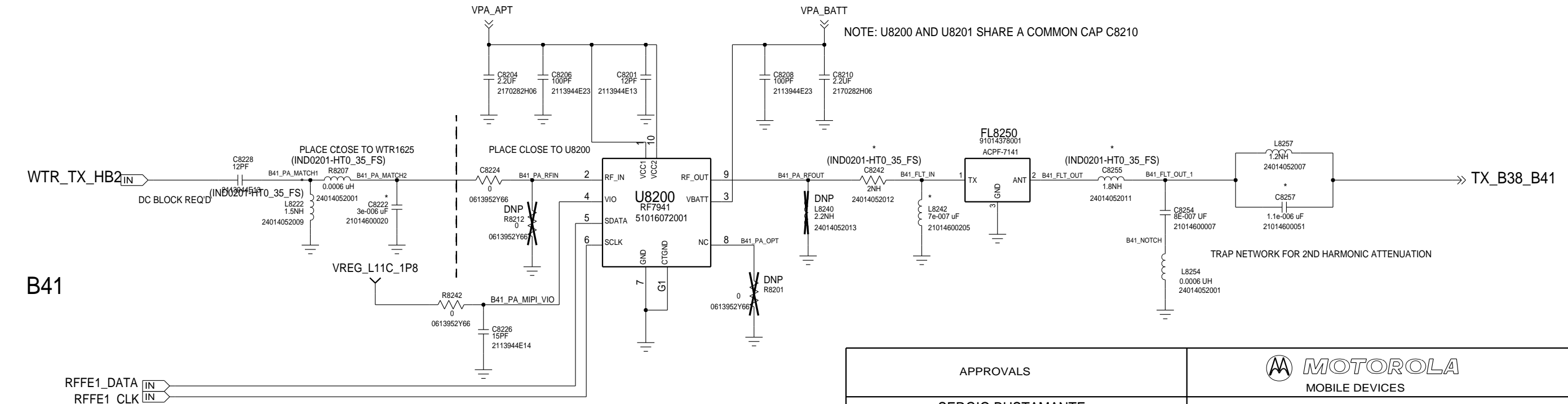
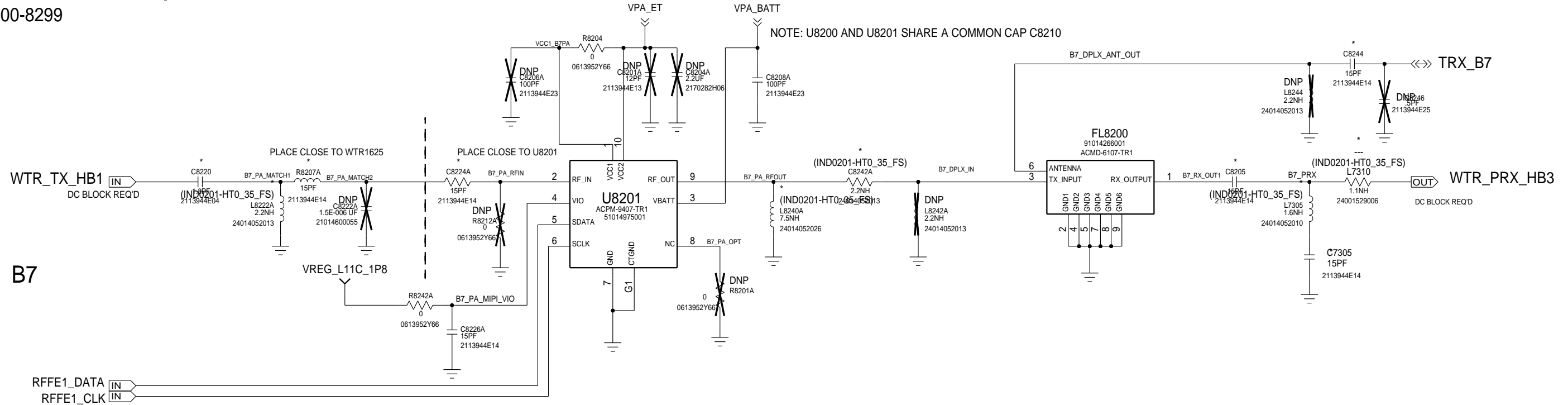
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		B 8074630001_a.cpm	01
CAD FILE NAME:		PAGE 6 OF 80	


RFFE: TX B7/B41

REF 7100-8299

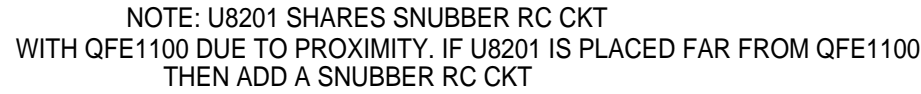
NOTE: U8201 SHARES SNUBBER RC CKT
WITH QFE1100 DUE TO PROXIMITY. IF U8201 IS PLACED FAR FROM QFE1100
THEN ADD A SNUBBER RC CKT


NOTE: U8200 AND U8201 SHARE A COMMON CAP C8210



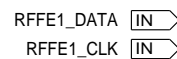
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		B 80746390001_a.cpm	
		CAD FILE NAME:	
		PAGE 7 OF 80	

REF 7600-8399

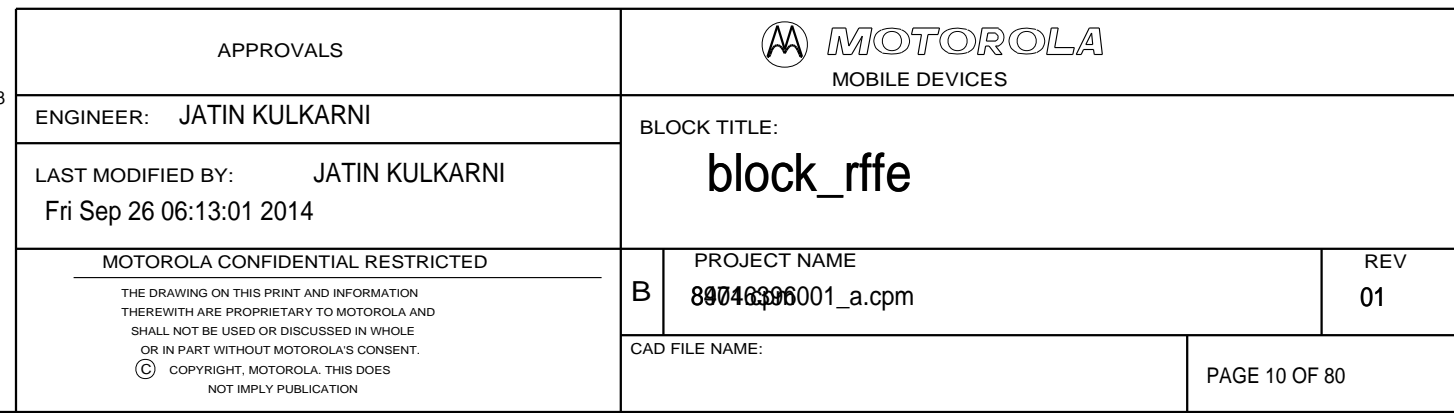


APPROVALS		 MOTOROLA MOBILE DEVICES	
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LAST MODIFIED BY: SERGIO BUSTAMANTE Fri Sep 26 06:13:01 2014			
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		CAD FILE NAME:	PAGE 8 OF 80

REF 7600-8399

PAGE 9 OF 80

REF 7100-8499

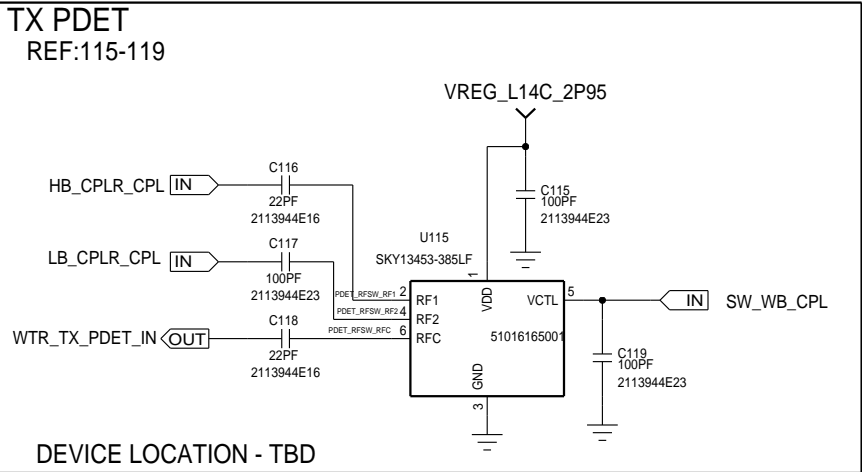


REF : 8500-8549

PAGE 11 OF 80

RFFE: PDET

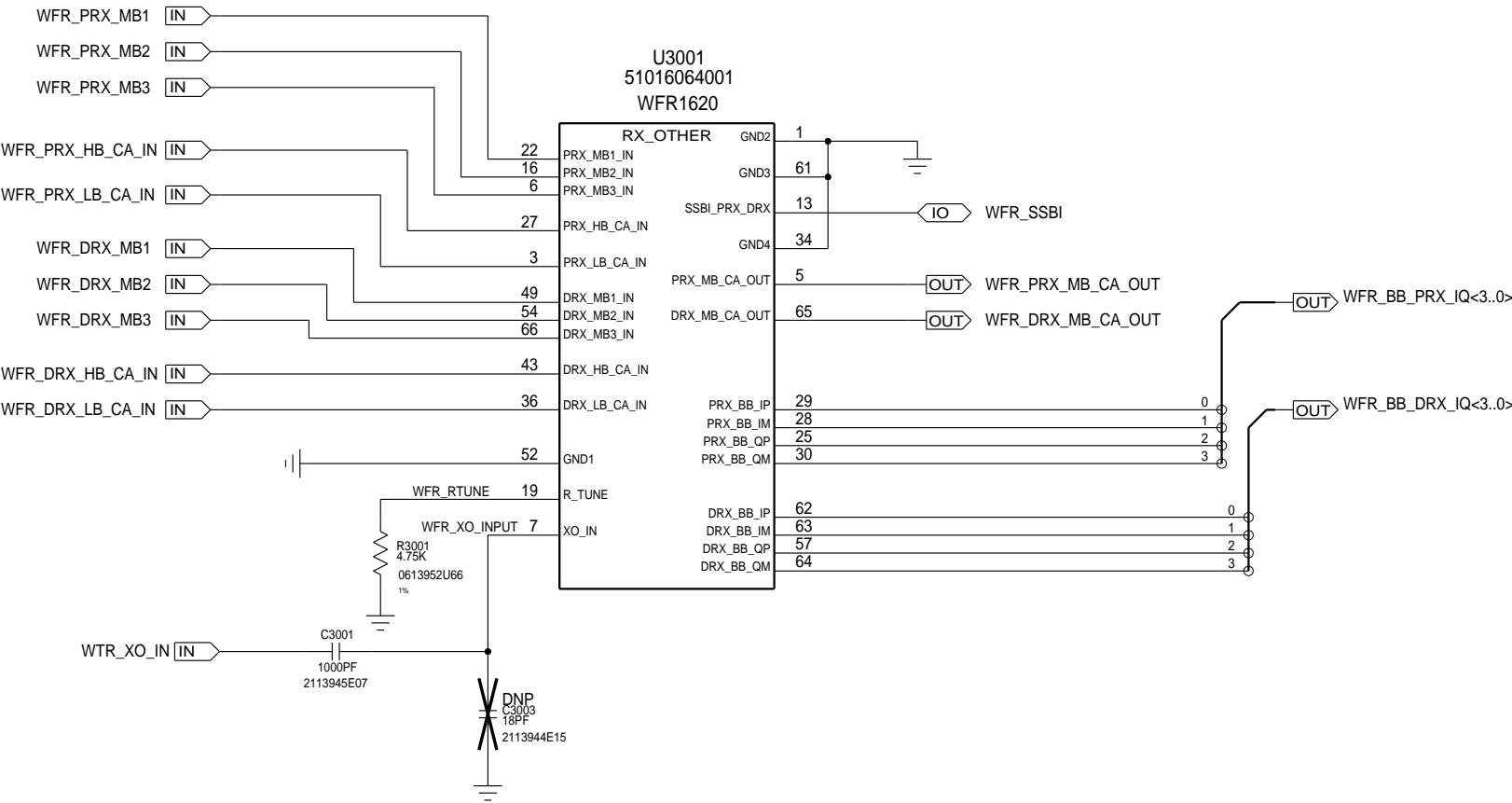
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MOTOROLA CONFIDENTIAL RESTRICTED <small>THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCUSSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © COPYRIGHT, MOTOROLA. THIS DOES NOT IMPLY PUBLICATION</small>		PROJECT NAME	REV
		B 807463960001_a.cpm	01
CAD FILE NAME:			PAGE 12 OF 80


WFR1620: BB RF

REF 3000-3019




TEMPORARY XPLAY COPY TO IMPORT RF CORE

WILL UPDATE TO FINAL NEXPLAY WHEN RF CORE IMPORT DONE

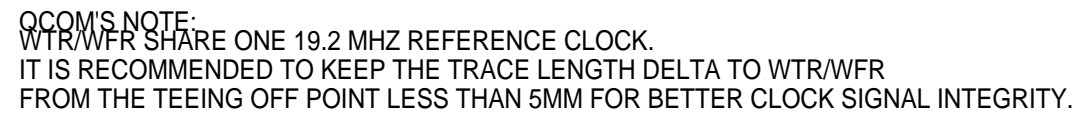
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ENGINEER:		BLOCK TITLE:	
LAST MODIFIED BY:		block_wfr1620	
Fri Sep 26 06:12:59 2014			
MOTOROLA CONFIDENTIAL RESTRICTED		PROJECT NAME	REV
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		CAD FILE NAME:	PAGE 13 OF 80


REF 3020-3099



APPROVALS		 MOTOROLA MOBILE DEVICES	
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LAST MODIFIED BY: Fri Sep 26 06:12:59 2014			
MOTOROLA CONFIDENTIAL RESTRICTED <hr/> <small>THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCUSSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT.</small> <small>© COPYRIGHT, MOTOROLA. THIS DOES NOT IMPLY PUBLICATION</small>		PROJECT NAME <div style="font-size: 18pt; font-weight: bold;">B 80746396001_a.cpm</div>	REV <div style="font-size: 24pt; font-weight: bold;">01</div>
		CAD FILE NAME:	PAGE 14 OF 80

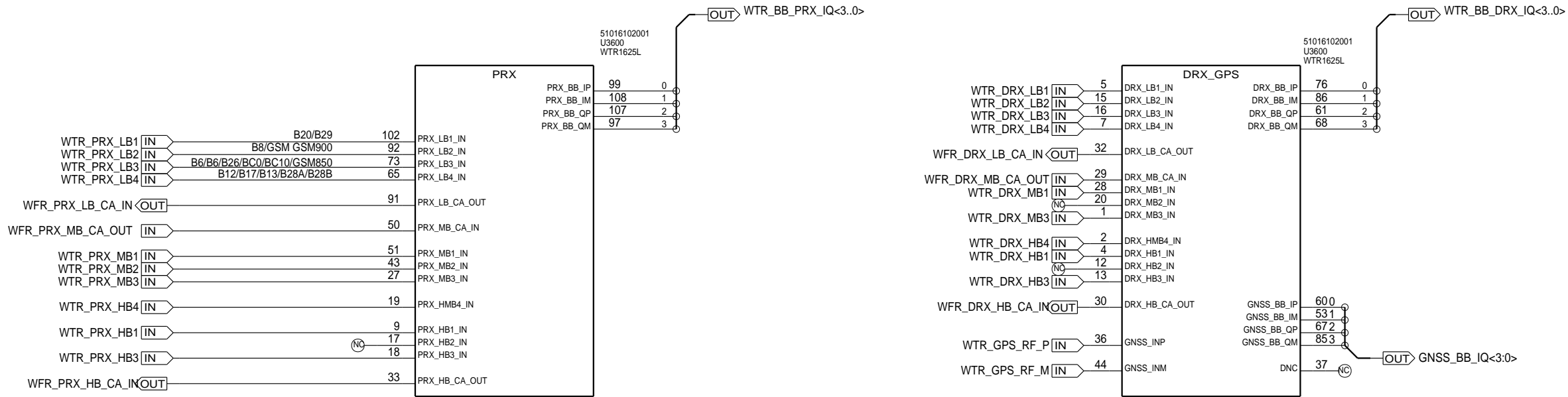
REF 3100-3109



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LAST MODIFIED BY: Fri Sep 26 06:12:58 2014			
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		CAD FILE NAME:	PAGE 15 OF 80


WTR1625: PRX/DRX/GPS

REF 3110-3119



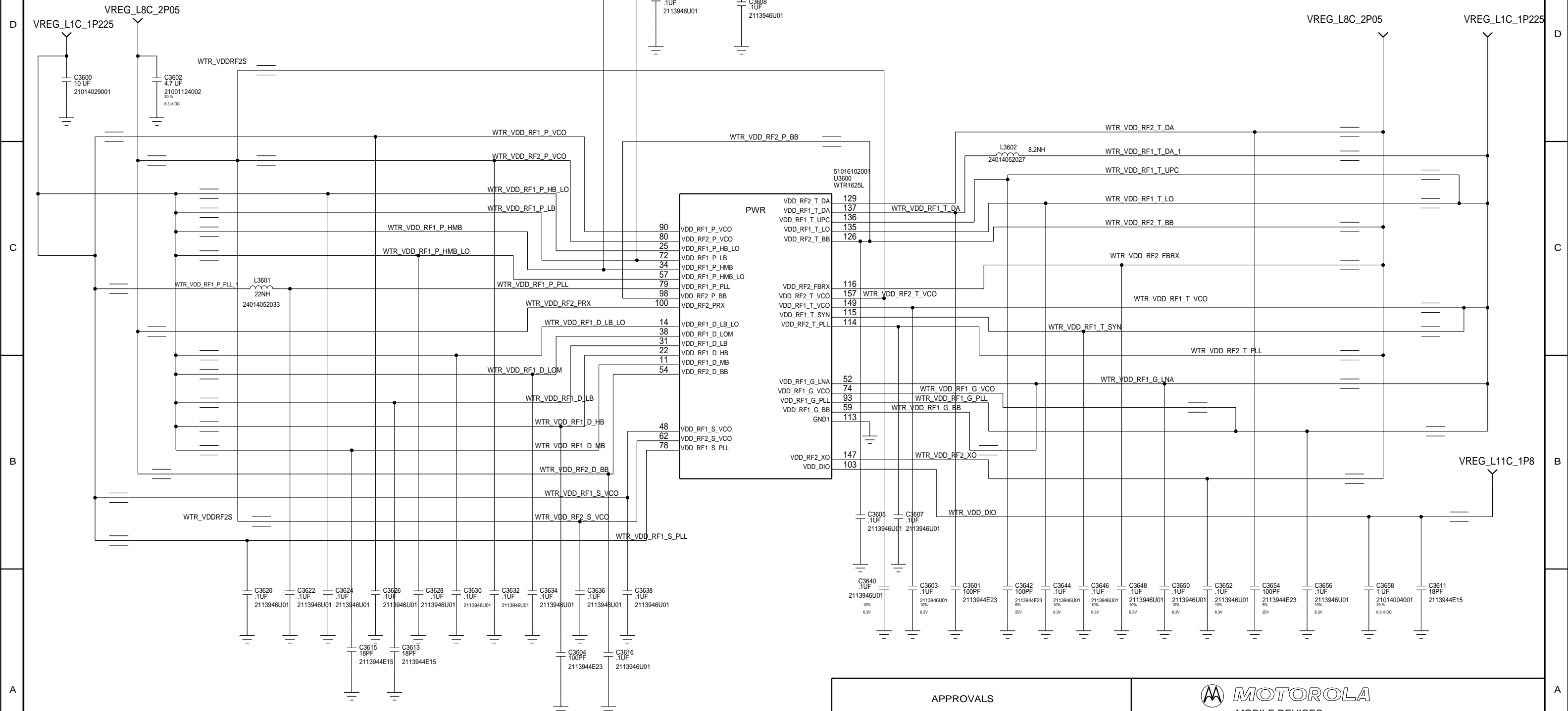
QCOM'S NOTE:
WTR/WFR CA_IN/OUT INTERCONNECTION MAXIMUM TRACE LENGTH:
LOW BAND 12 MM; MID/ HI BAND 10 MM

TEMPORARY XPLAY COPY TO IMPORT RF CORE
WILL UPDATE TO FINAL NEXPLAY WHEN RF CORE IMPORT DONE

APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER:		BLOCK TITLE:	
LAST MODIFIED BY:		block_wtr1625	
Fri Sep 26 06:12:58 2014			
MOTOROLA CONFIDENTIAL RESTRICTED		PROJECT NAME	
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		REV 01	
		CAD FILE NAME:	
		PAGE 16 OF 80	


WTR1625: POWER

REF 3120-3199



TEMPORARY XPLAY COPY TO IMPORT RF CORE

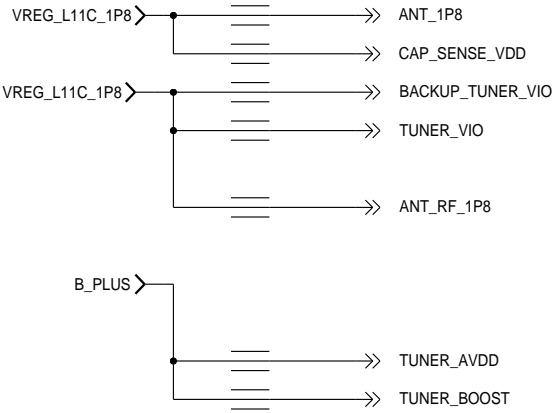
WILL UPDATE TO FINAL NEXPLAY WHEN RF CORE IMPORT DONE

APPROVALS		 MOTOROLA MOBILE DEVICES	
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LAST MODIFIED BY:		block_wtr1625	
Fri Sep 26 06:12:58 2014			
MOTOROLA CONFIDENTIAL RESTRICTED		PROJECT NAME	
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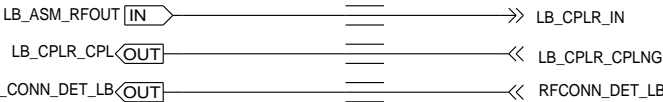
ANT: PORTS

REFDES: 000 - 499

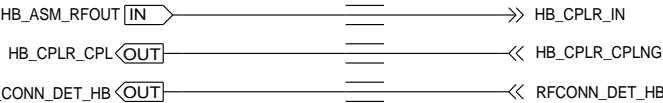
SUPPLIES



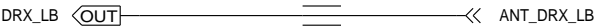
TX LOW BAND ANTENNA



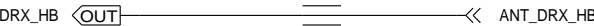
TX HIGH BAND ANTENNA



DRX LOW BAND ANTENNA



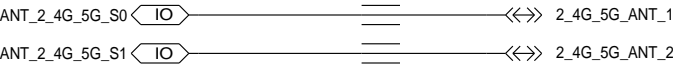
DRX HIGH BAND ANTENNA



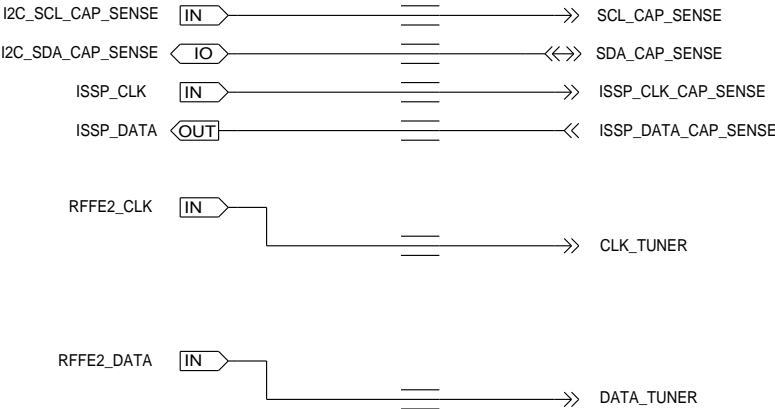
GPS ANTENNA



BT/WLAN ANTENNA



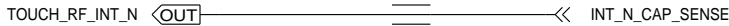
INTERFACES




GPIO - INPUT



GPIO - OUTPUT



APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: NATE CONNELL		BLOCK TITLE:	
LAST MODIFIED BY: NATE CONNELL Fri Sep 26 06:12:57 2014		antennas	
<div>MOTOROLA CONFIDENTIAL RESTRICTED</div> <div>THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCUSSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © COPYRIGHT, MOTOROLA. THIS DOES NOT IMPLY PUBLICATION</div>		PROJECT NAME	
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		PAGE 18 OF 80	

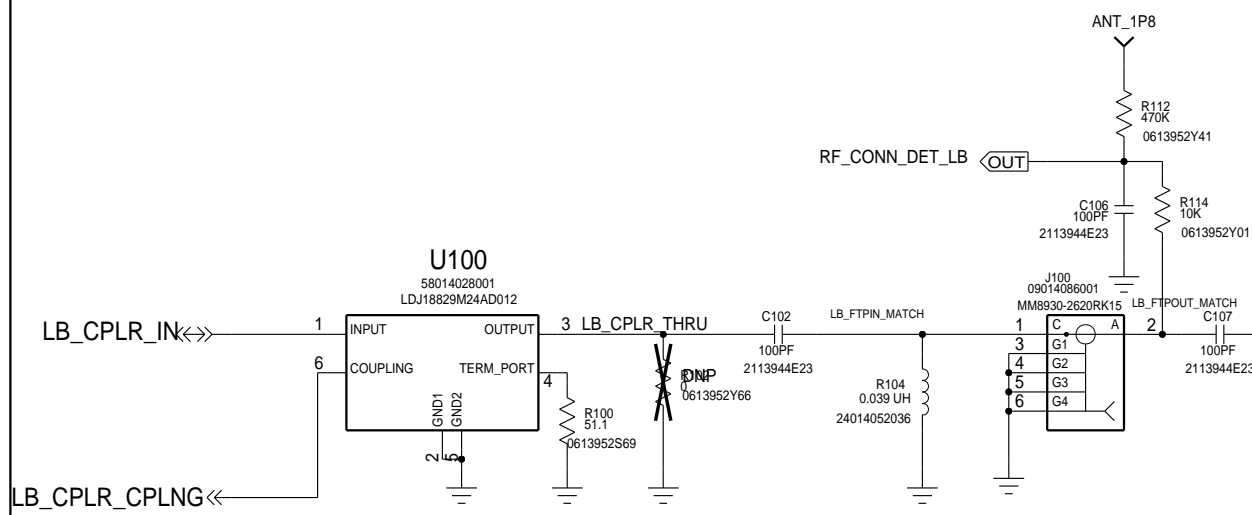
ANTENNAS

MAIN LOW BAND PORT

REF:100-114

DEVICE LOCATION - BOTTOM

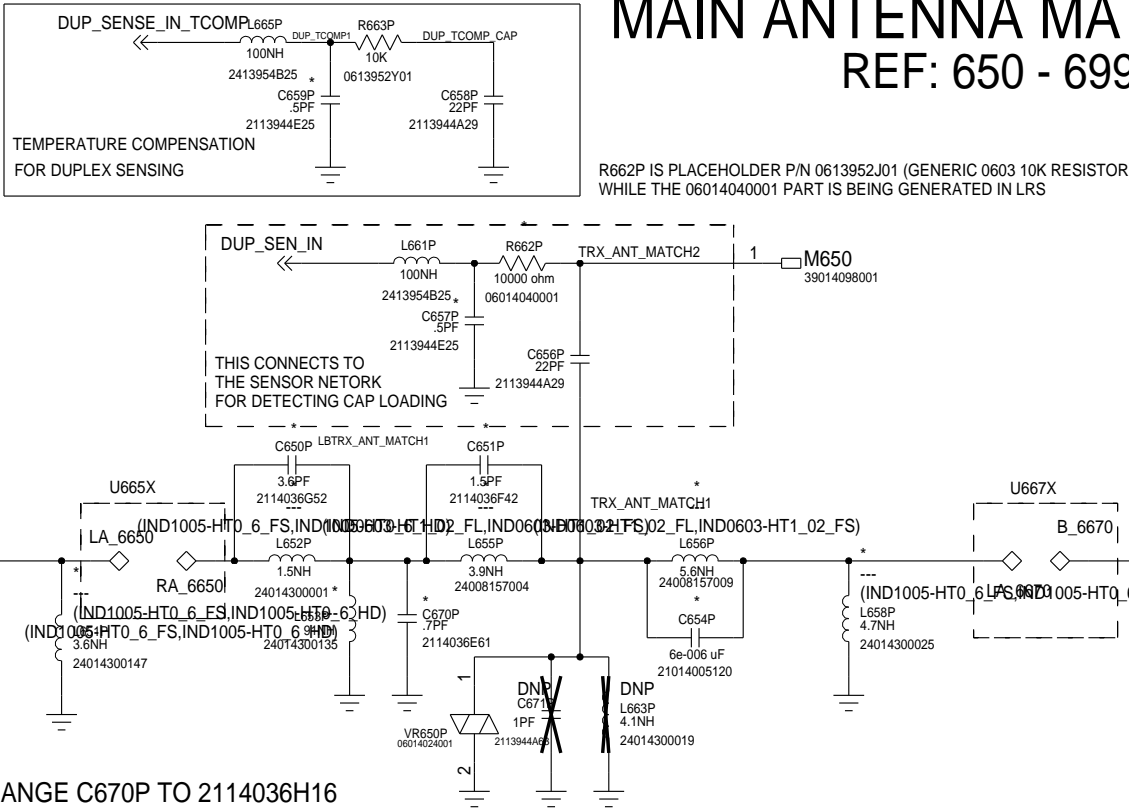
TX LOW BAND ANTENNA
(GSM/UTMS/LTE)



COMPONENTS ADDED FOR TEMPERATURE COMPENSATION. IT IS CRITICAL THAT THE CAP. C656 IN THE MATCHING NETWORK TO HAVE COG TEMPERATURE CHARACTERISTICS AND THE SAME TYPE AND SAME VALUE CAP MUST BE USED IN THE TEMPERATURE COMPENSATION CIRCUIT.

MAIN ANTENNA MATCH

REF: 650 - 699

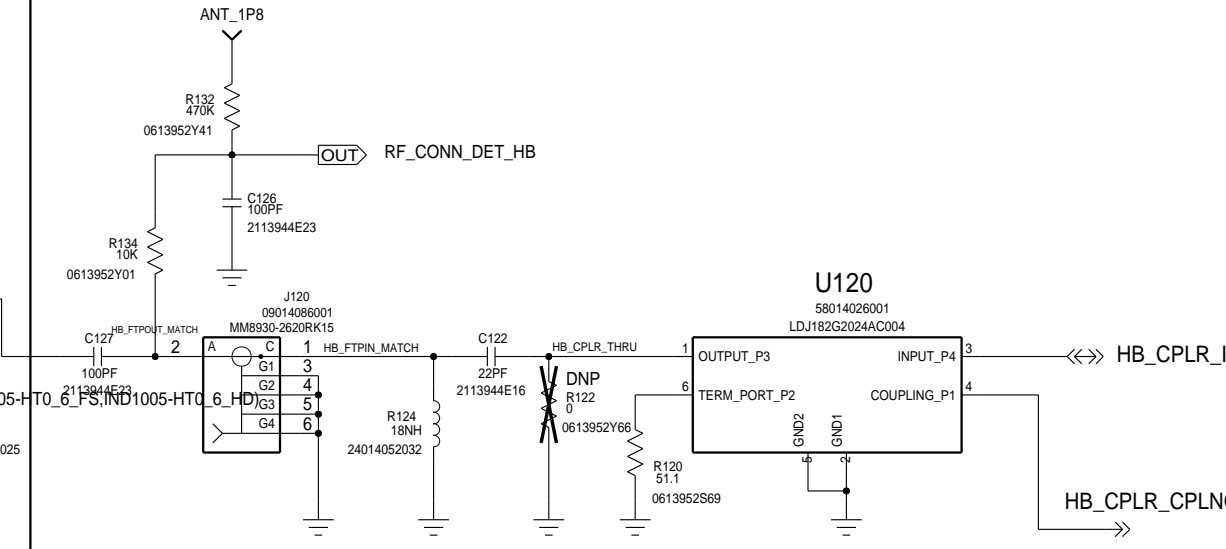


MAIN HIGH BAND PORT

REF:120-139

DEVICE LOCATION - BOTTOM

TX HIGH BAND ANTENNA
(GSM/UTMS/LTE)

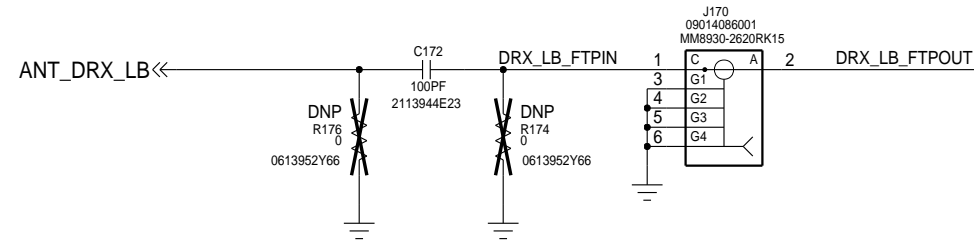


DRX LOW BAND PORT

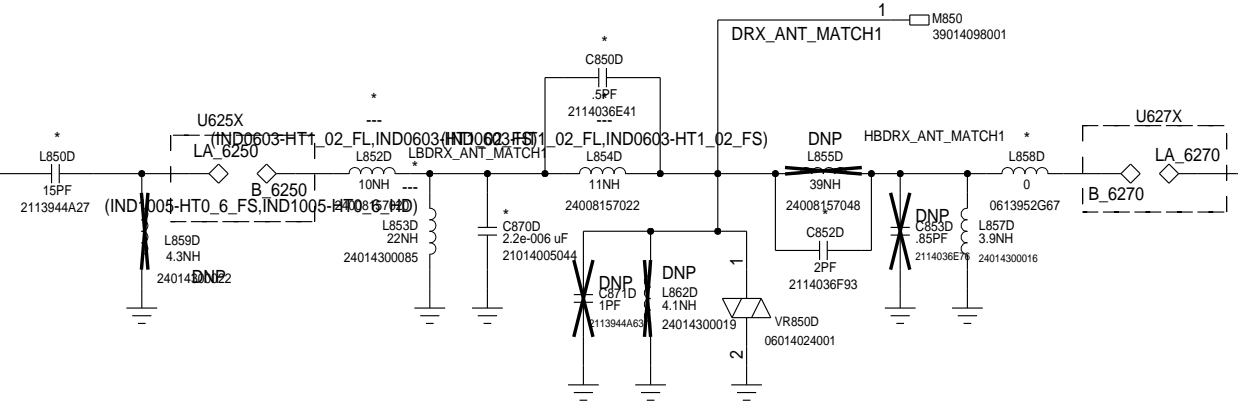
REF:170-179

DEVICE LOCATION - TOP LEFT

(GSM/UTMS/LTE)



DRX ANTENNA MATCH

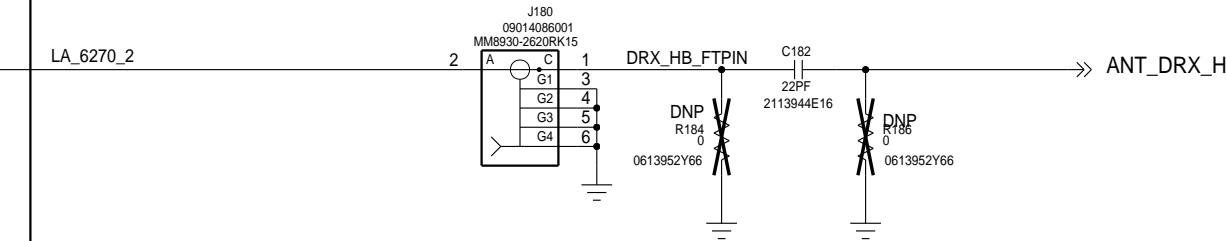


DRX HIGH BAND PORT

REF:180-189

DEVICE LOCATION - TOP LEFT

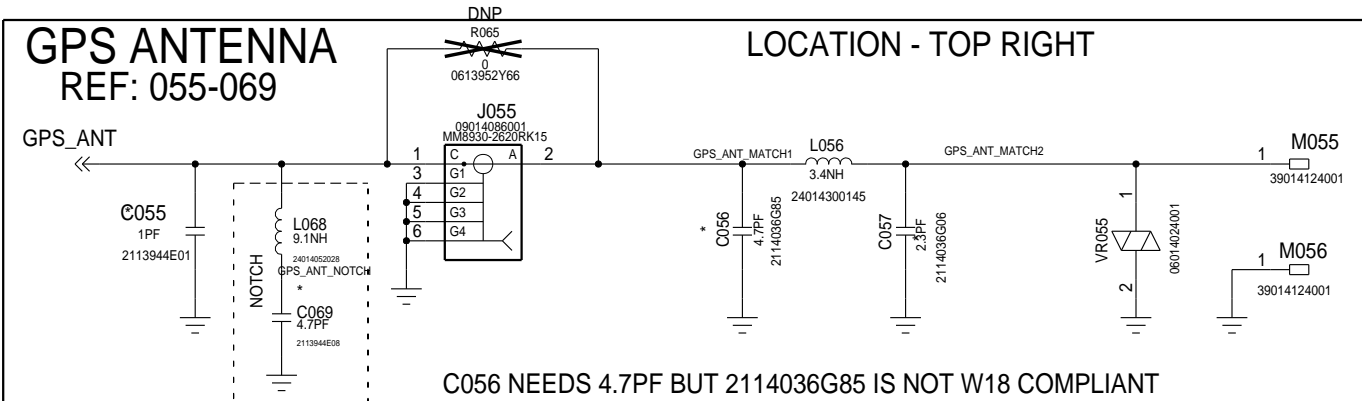
(GSM/UTMS/LTE)



GPS ANTENNA

REF: 055-069

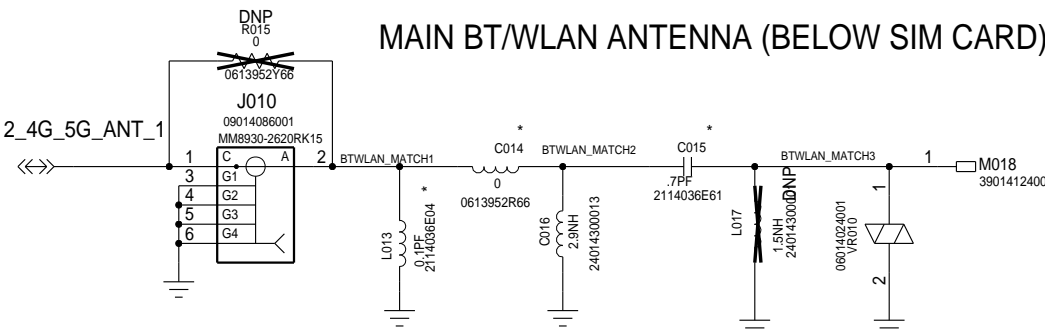
LOCATION - TOP RIGHT



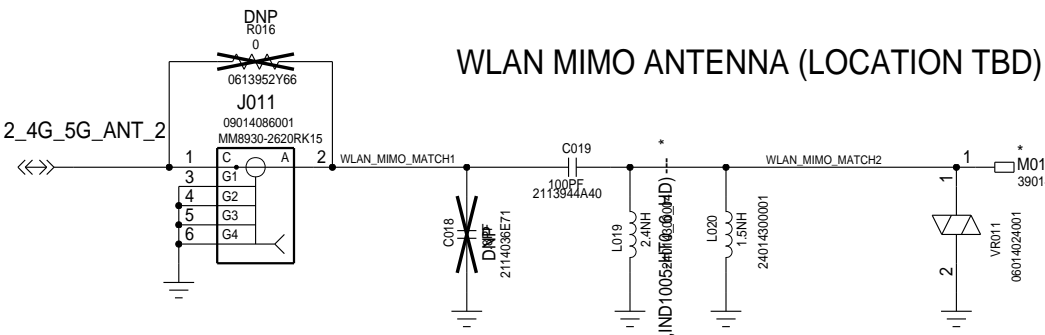
BT/WLAN ANTENNA

REF:010-019

MAIN BT/WLAN ANTENNA (BELOW SIM CARD)



WLAN MIMO ANTENNA (LOCATION TBD)

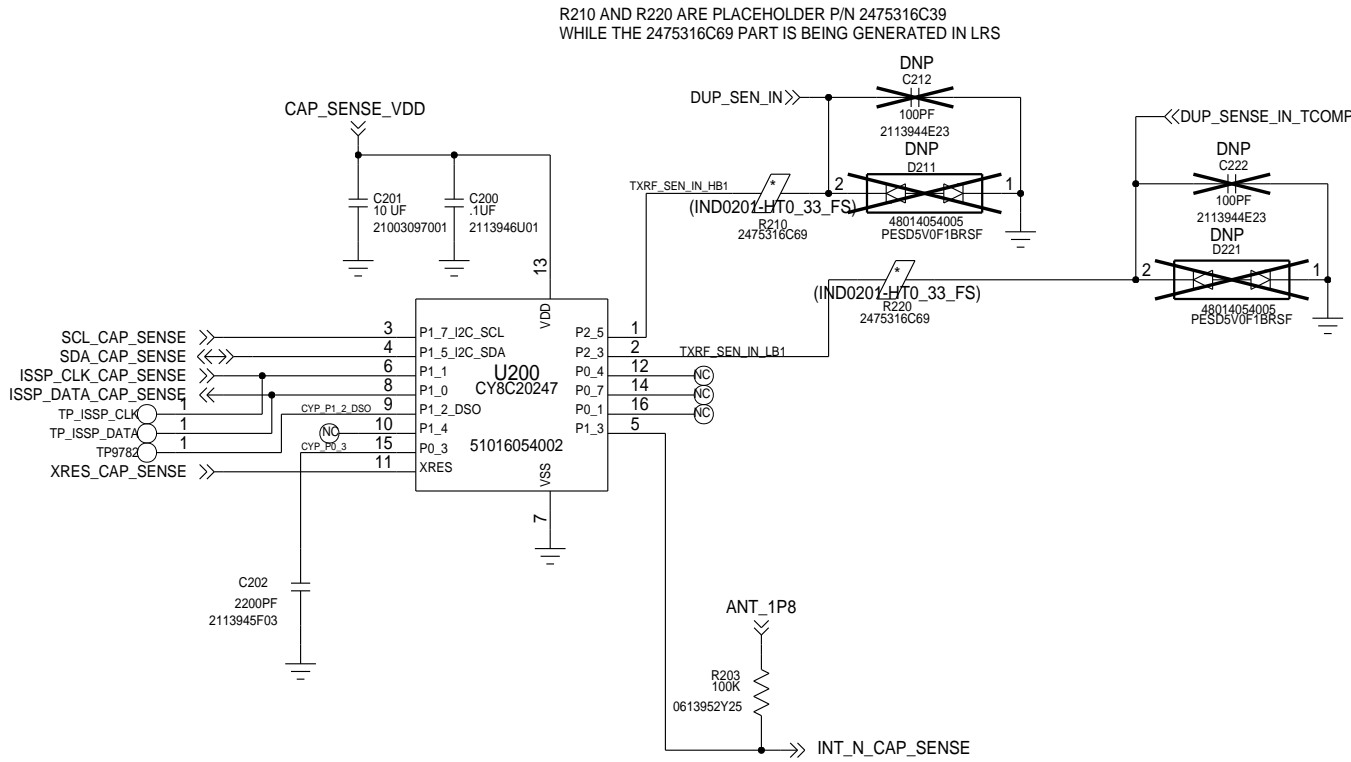



MOTOROLA MOBILITY CONFIDENTIAL RESTRICTED-MDB TRACKED TRACKEDANDBLOCKED

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ENGINEER:	BUSTAMANTE	BLOCK TITLE:	
LAST MODIFIED BY:	BUSTAMANTE	antennas	
Fri Sep 26 06:12:57 2014		PROJECT NAME	
MOTOROLA CONFIDENTIAL RESTRICTED		84016396001_a.cpm	
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CAD FILE NAME:		PAGE 19 of 80	

ANT: CAP SENSE

REFDES: 200-299



APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE:	
LAST MODIFIED BY: MOTOROLA Fri Sep 26 06:12:57 2014		antennas	
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		PAGE 20 OF 80	

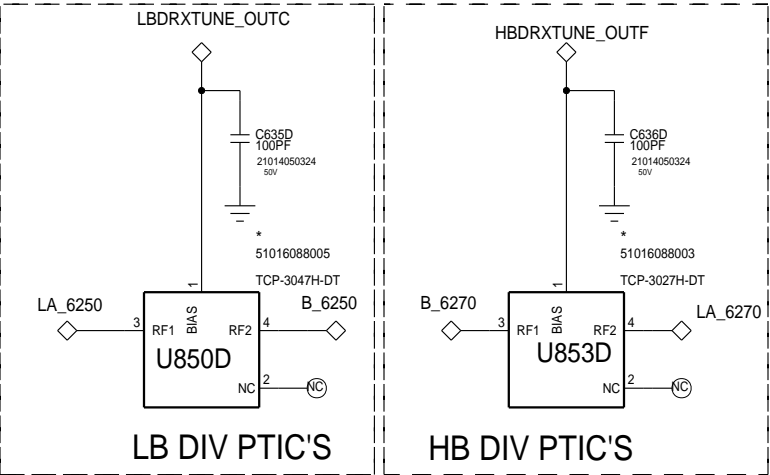
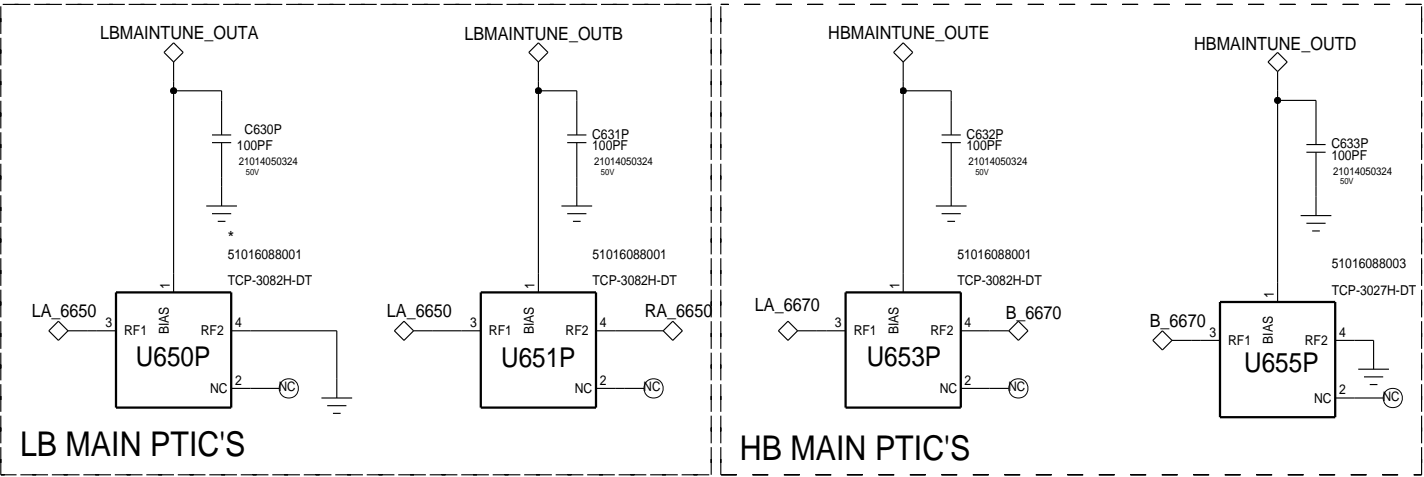
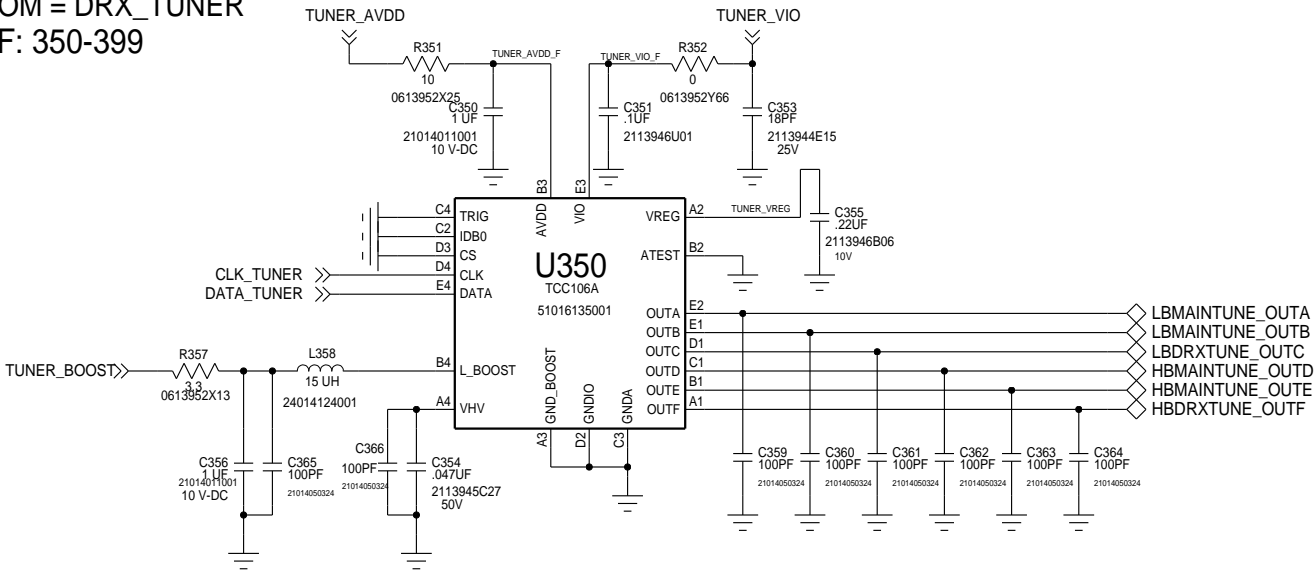
ANT: TUNERS


REFDES: 300-499

MAIN / DIVERSITY RX (RFFE2)

ROOM = DRX_TUNER

REF: 350-399

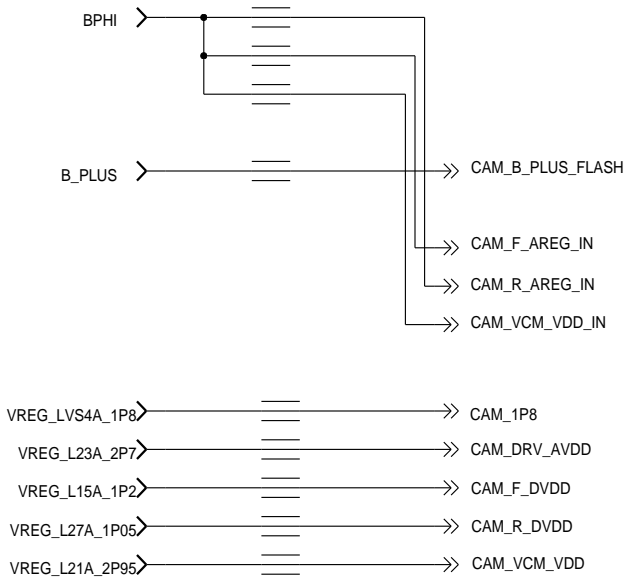


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ENGINEER: MOTOROLA		BLOCK TITLE:	
LAST MODIFIED BY: MOTOROLA Fri Sep 26 06:12:58 2014		antennas	
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		B 84016396001_a.cpm	
		REV 01	
		CAD FILE NAME:	
		PAGE 21 OF 80	

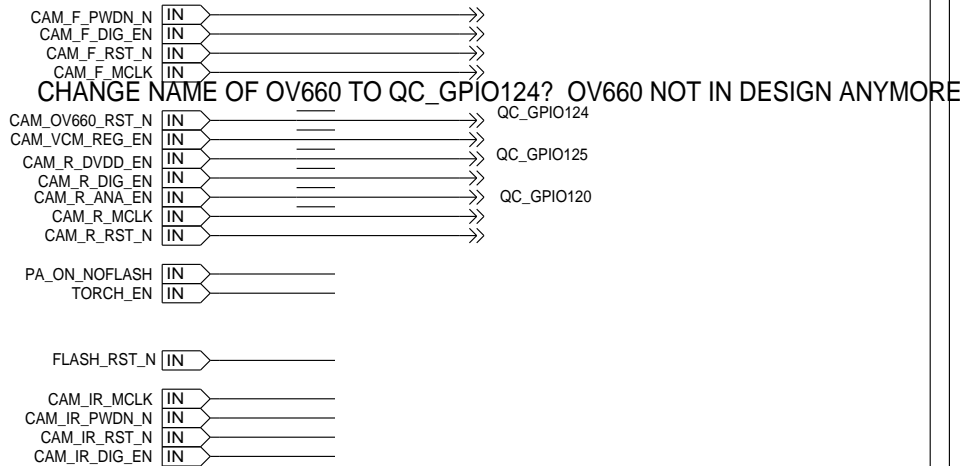
CAM: PORTS

REF 5500-5999

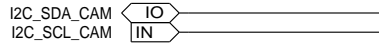
SUPPLIES



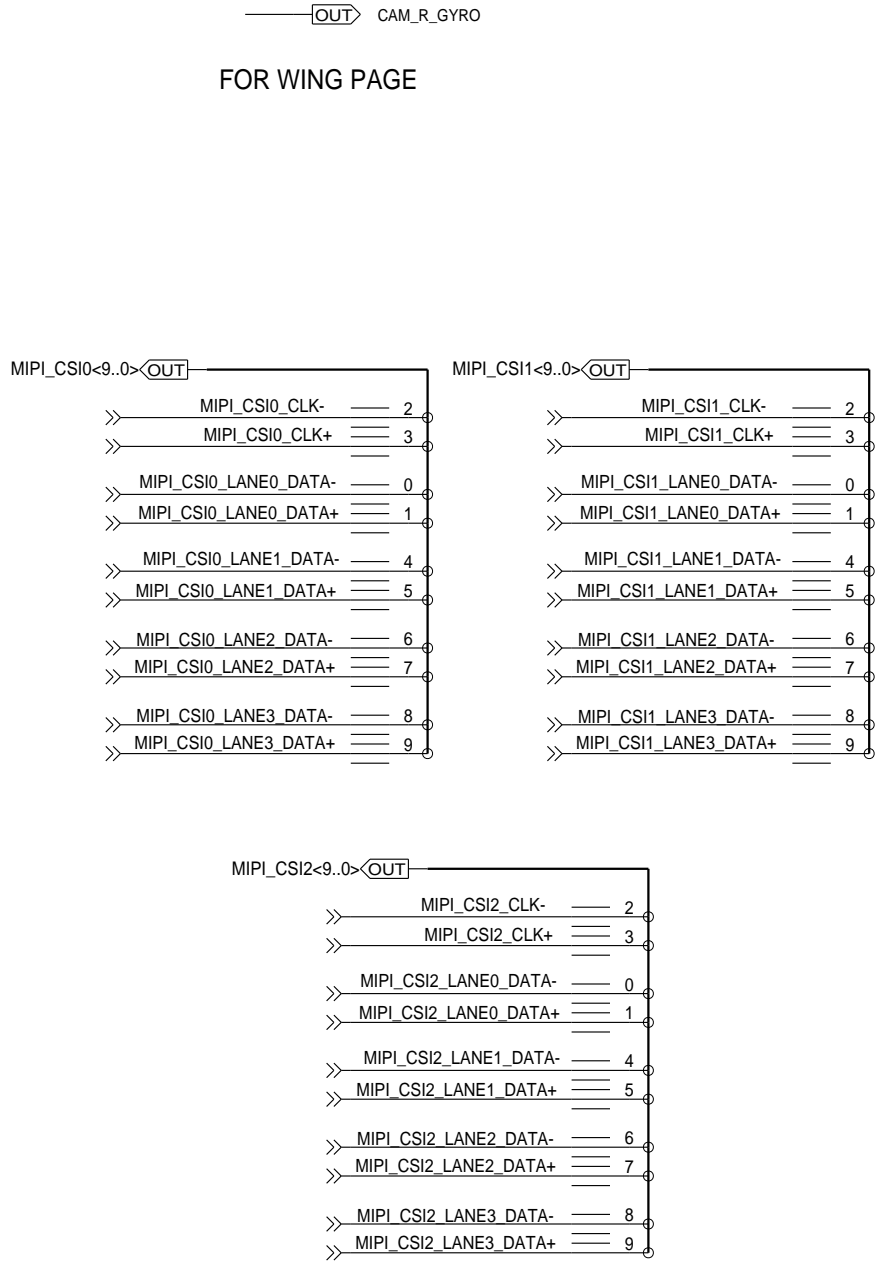
INPUTS



INTERFACES



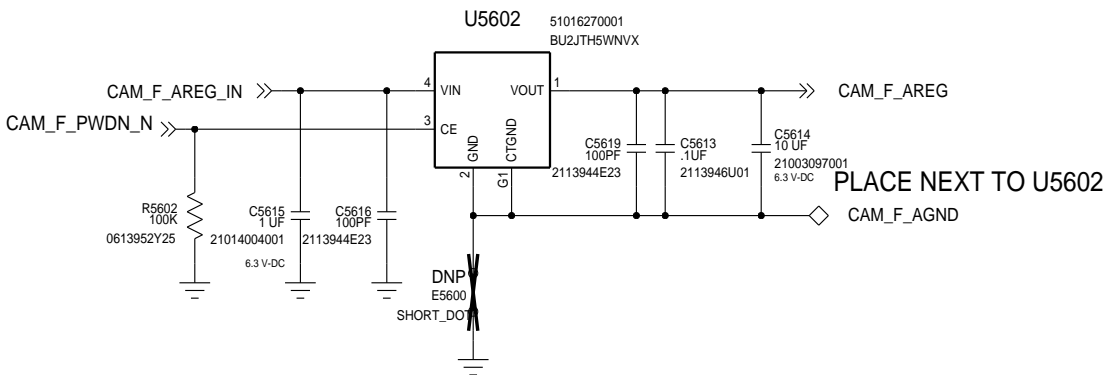
OUTPUTS



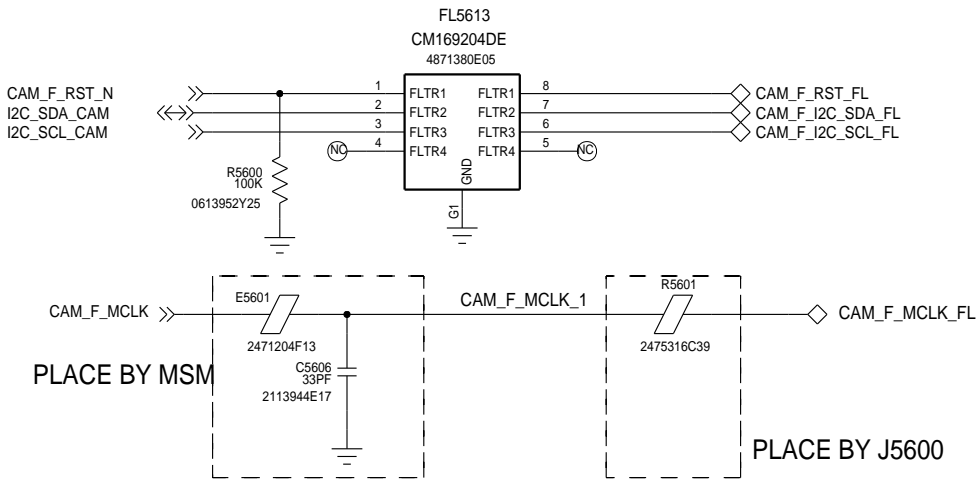
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		PAGE 22 OF 80	

CAM: FRONT
REF 5600-5699


POWER



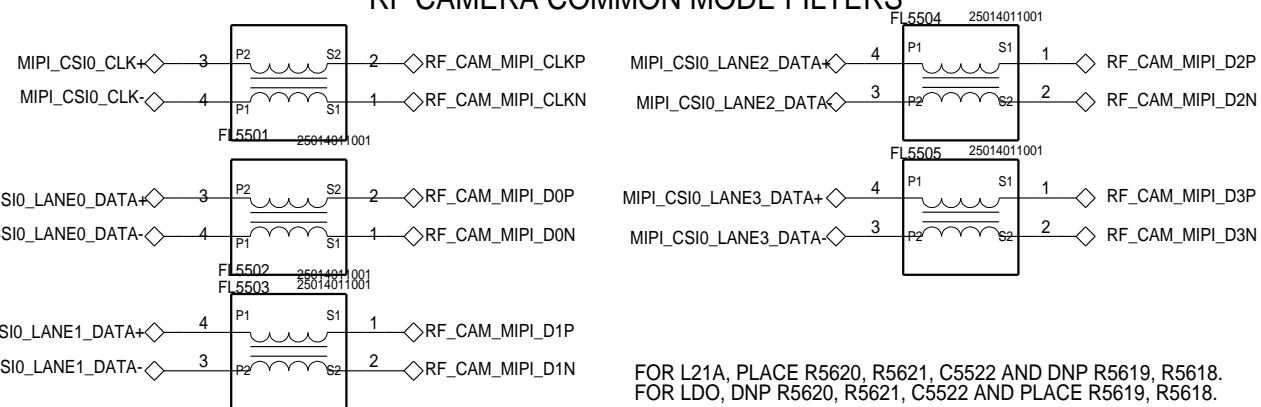
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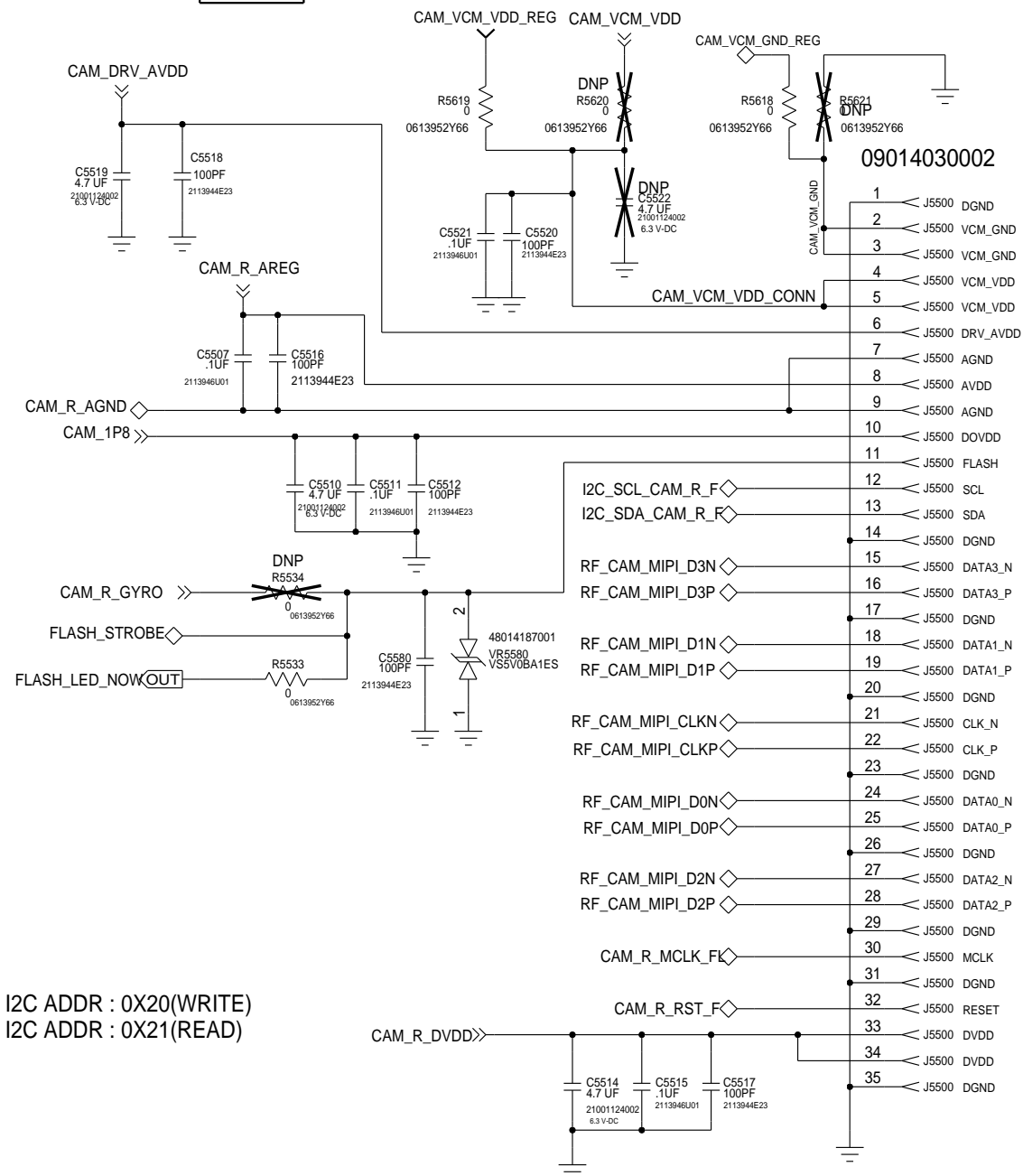
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APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: JOHN KRAMER		BLOCK TITLE:	
LAST MODIFIED BY: UID ELRAN Fri Sep 26 06:12:56 2014		imagers	
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		REV 01	
		CAD FILE NAME:	
		PAGE 23 OF 80	

REF:5500-5599

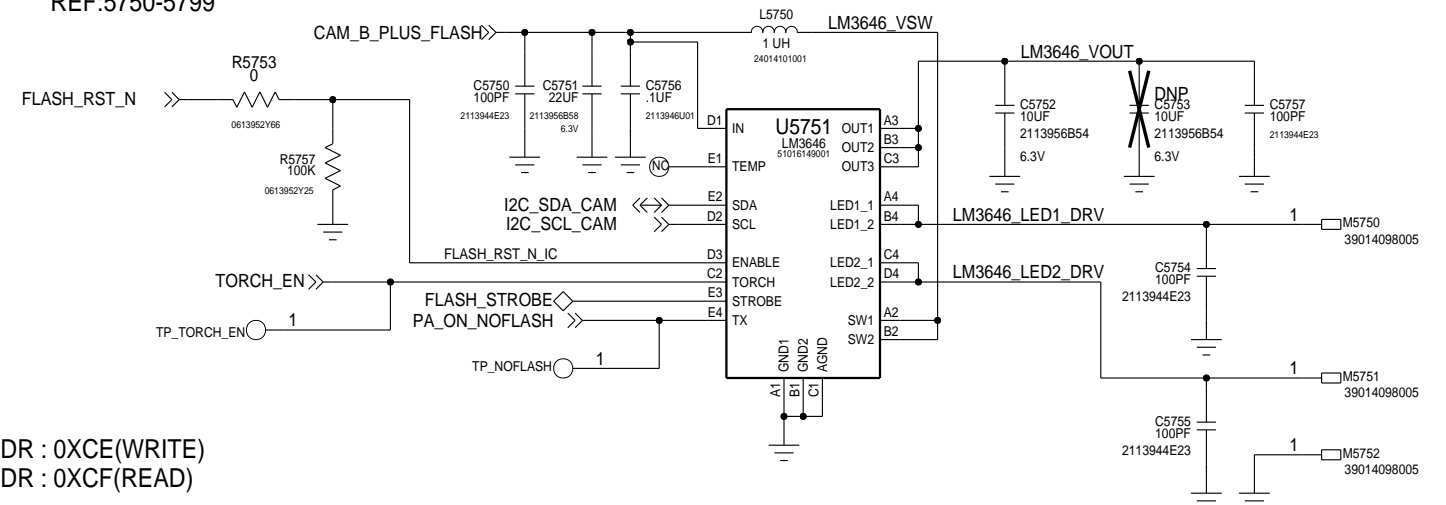


FOR L21A, PLACE R5620, R5621, C5522 AND DNP R5619, R5618.
FOR LDO, DNP R5620, R5621, C5522 AND PLACE R5619, R5618.

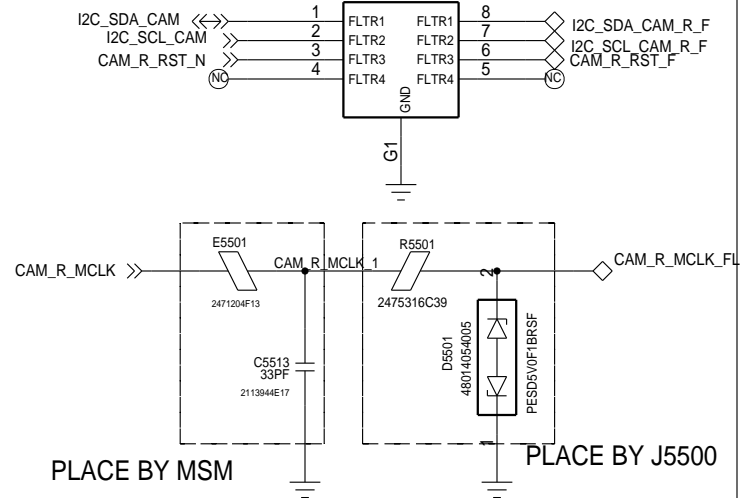


I2C ADDR : 0X20(WRITE)
I2C ADDR : 0X21(READ)

REF:5750-5799



I2C ADDR : 0XCE(WRITE)
I2C ADDR : 0XCF(READ)



PLACE BY MSM

PLACE BY J5500

NOTE : R5501 IS FOR RF TEST


The schematic diagram illustrates the CAM_VCM power supply circuit. It features a 5V regulator (U1) with the following connections:

- VIN (Pin 4):** Connected to CAM_VCM_VDD_IN.
- VOUT (Pin 1):** Connected to CAM_VCM_GND_REG.
- CE (Pin 3):** Connected to CAM_VCM_REG_EN.
- GND (Pin 2):** Connected to ground.
- CTGND (Pin 1):** Connected to ground.

Decoupling and output filtering components include:

- C5610 (1 uF):** Input decoupling capacitor.
- C5609 (100 pF):** Input decoupling capacitor.
- C5608 (10 uF):** Output decoupling capacitor.
- C5607 (10 uF):** Output decoupling capacitor.

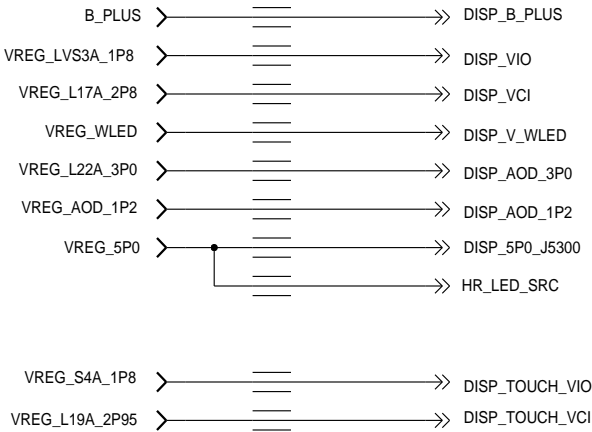
The output of the regulator is connected to CAM_VCM_GND_REG. The circuit is protected by DNP and SHORT_DOT components.

APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA	BLOCK TITLE: <h1>imagers</h1>		
LAST MODIFIED BY: MOTOROLA Fri Sep 26 06:12:56 2014			
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		CAD FILE NAME:	PAGE 24 OF 80

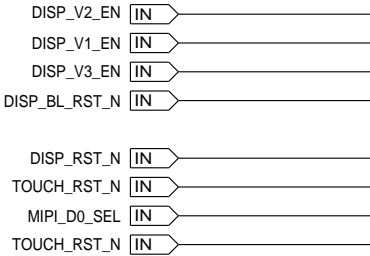
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REF 5000-5499

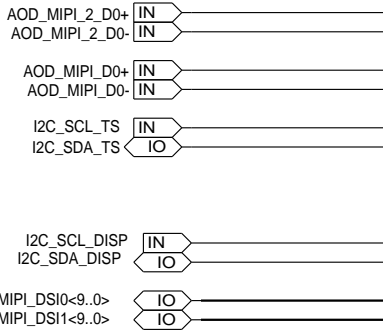
SUPPLIES



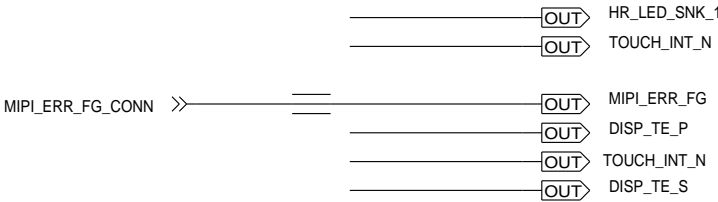
INPUTS



INTERFACES



OUTPUTS



APPROVALS

ENGINEER: NATE CONNELL

LAST MODIFIED BY: NATE CONNELL
Fri Sep 26 06:12:54 2014

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BLOCK TITLE:

display

PROJECT NAME
84016396001_a.cpm

REV
01

CAD FILE NAME:

REF 5000-5499

1. CHANGE GLOBAL SUPPLY NAME OF OUTPUT OF U5304 FROM VBAT_DISP_CONN TO VBAT_DISP; DELETE GLOBAL VBAT_DISP_CONN ON J5301
2. CONNECT SUPPLY ELVDD, ELVSS, AVDD_DISP AND SIGNAL ELVDD_SNS_FL, ELON1_AVDD_FL, ELON2_FL TO DISPLAY CONN J5301 PER PRODUCT PLAN
3. E5361 AND E5362 ARE 0402 PLACE HOLDERS FOR 0402 SIZE FERRITE BEAD PER PRODUCT DISP RF DESENSE NEEDS; CAPS ON VBAT_DISP CAN BE TWEAKED ON PRODUCT.
4. CHANGE REF 5300-5349 TO REF 5300-5399 FOR THIS PAGE

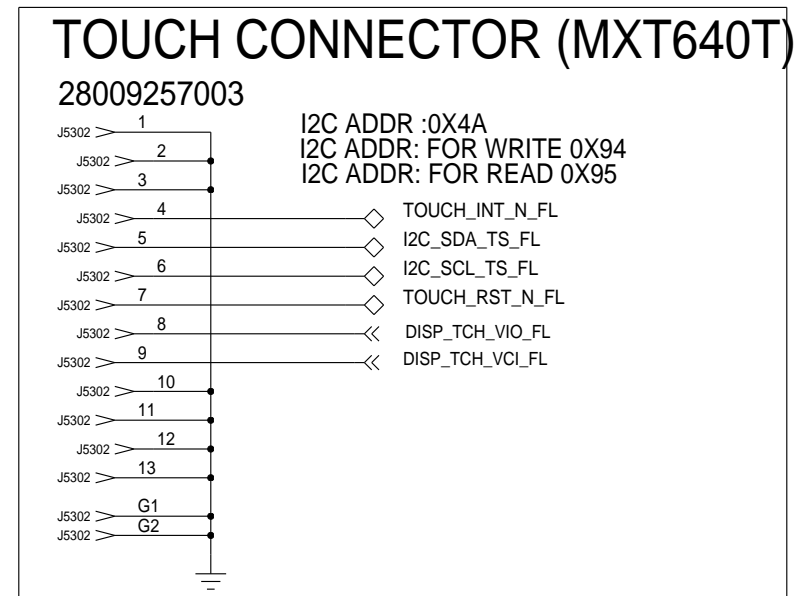
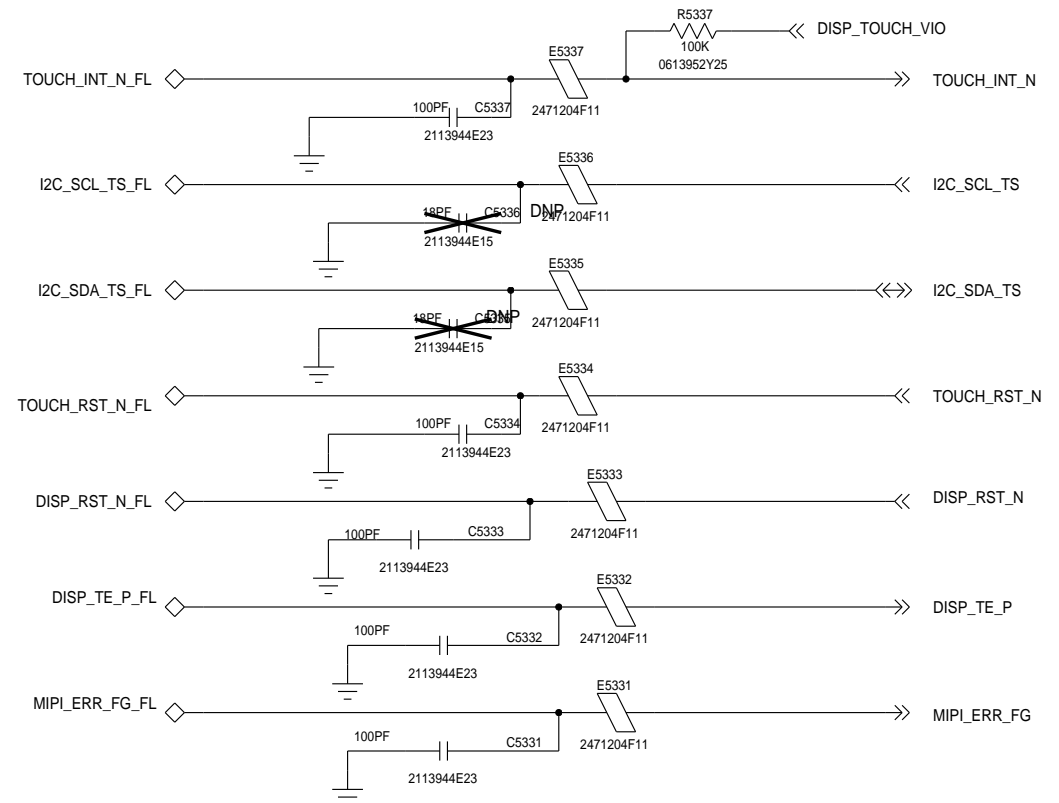
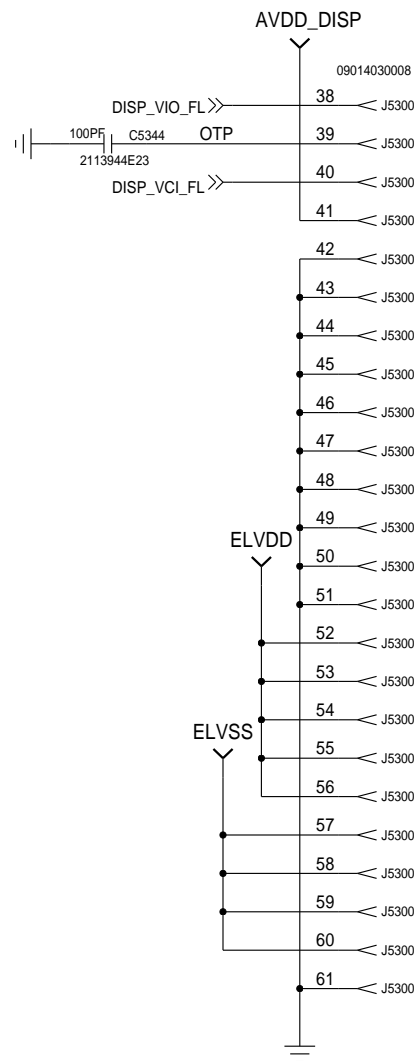
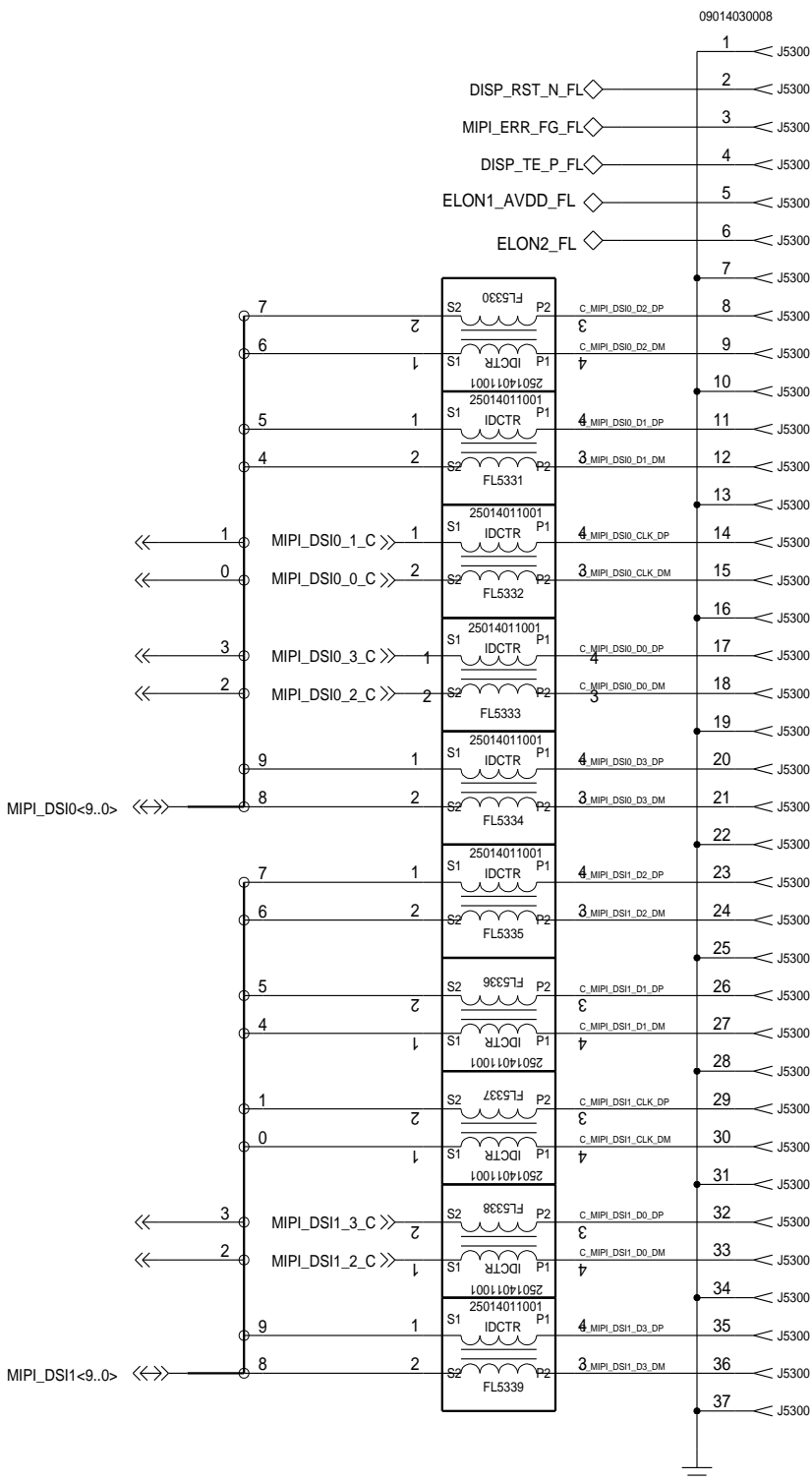

$$\text{display}$$
REV
01


PAGE 26 OF 80

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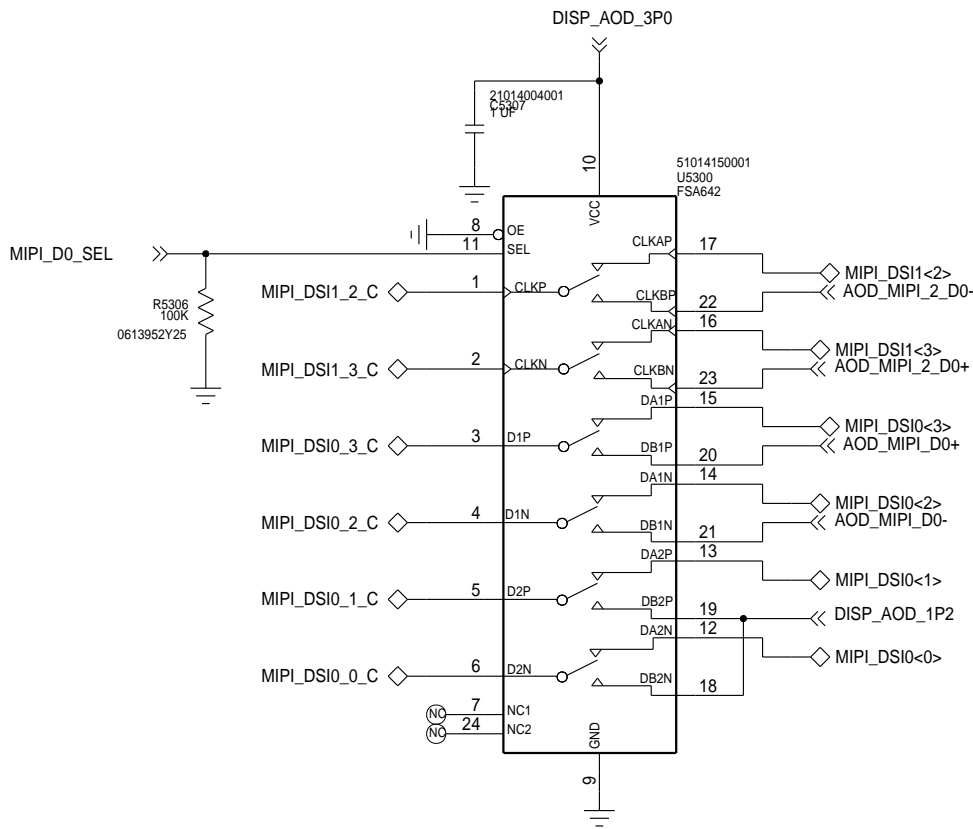
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REF 5300-5349



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LAST MODIFIED BY: NATE CONNELL Fri Sep 26 06:12:55 2014		display	
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		CAD FILE NAME:	PAGE 27 OF 80

DISP: AOD

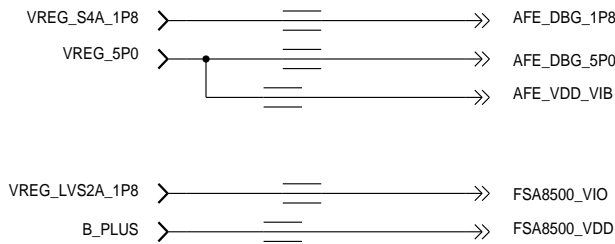


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ENGINEER: MOTOROLA		BLOCK TITLE:	
LAST MODIFIED BY: MOTOROLA Fri Sep 26 06:12:55 2014		display	
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		B 84016396001_a.cpm	REV 01
		CAD FILE NAME:	
		PAGE 28 OF 80	

AFE: PORTS

REF 4500-4999

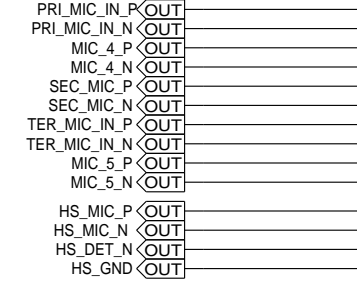
SUPPLIES



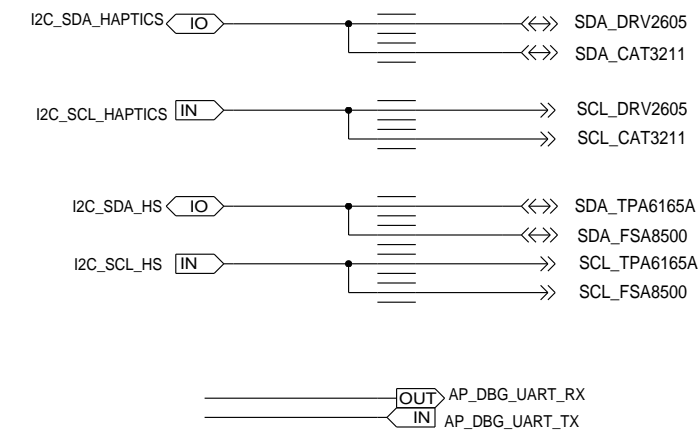
INPUTS



OUTPUTS

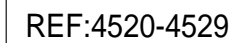
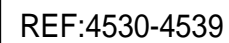
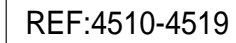
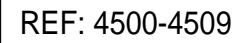


INTERFACES




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LAST MODIFIED BY: NATE CONNELL Fri Sep 26 06:12:53 2014		PROJECT NAME B 84016396001_a.cpm	
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		CAD FILE NAME: PAGE 29 OF 80	

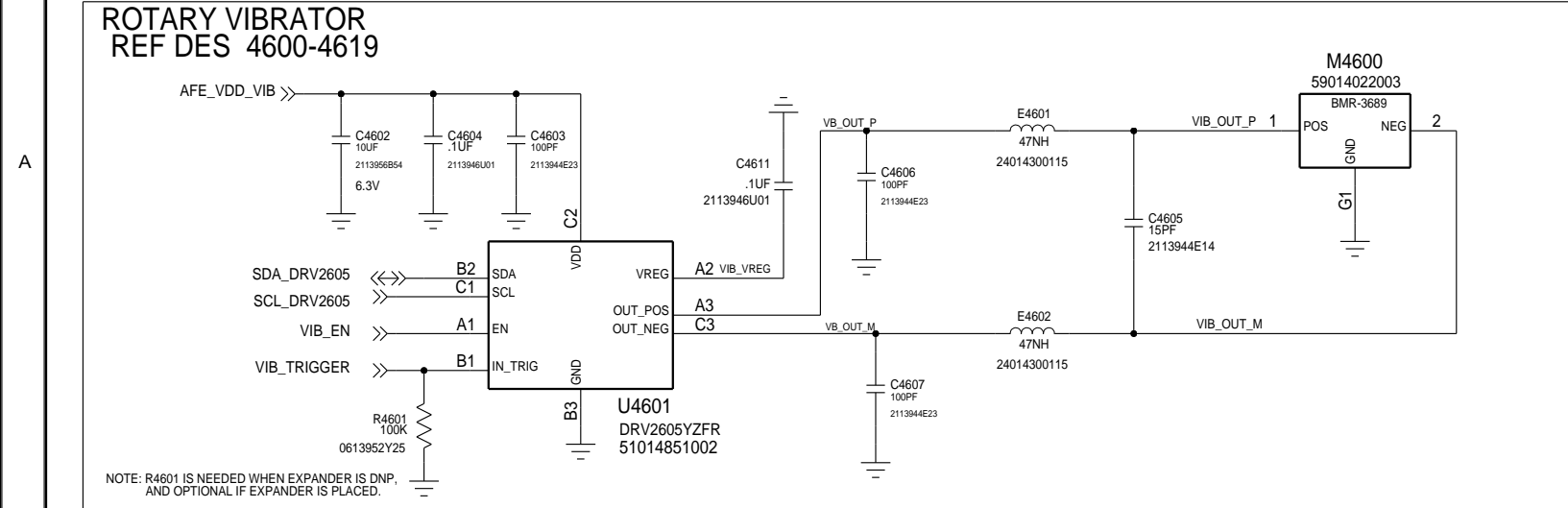
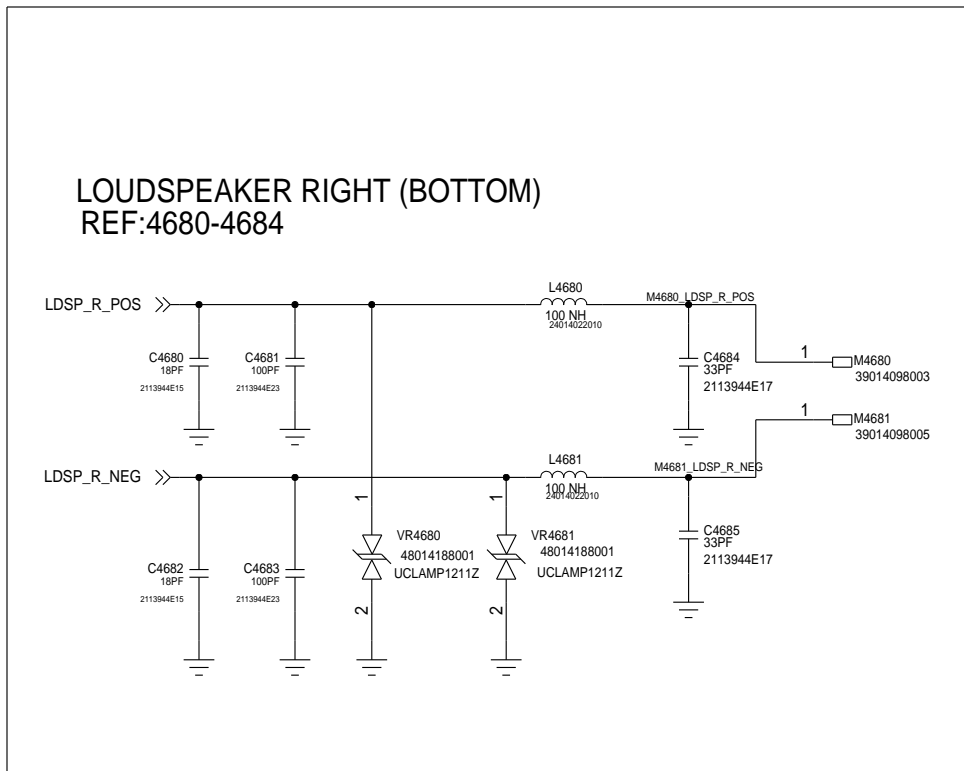
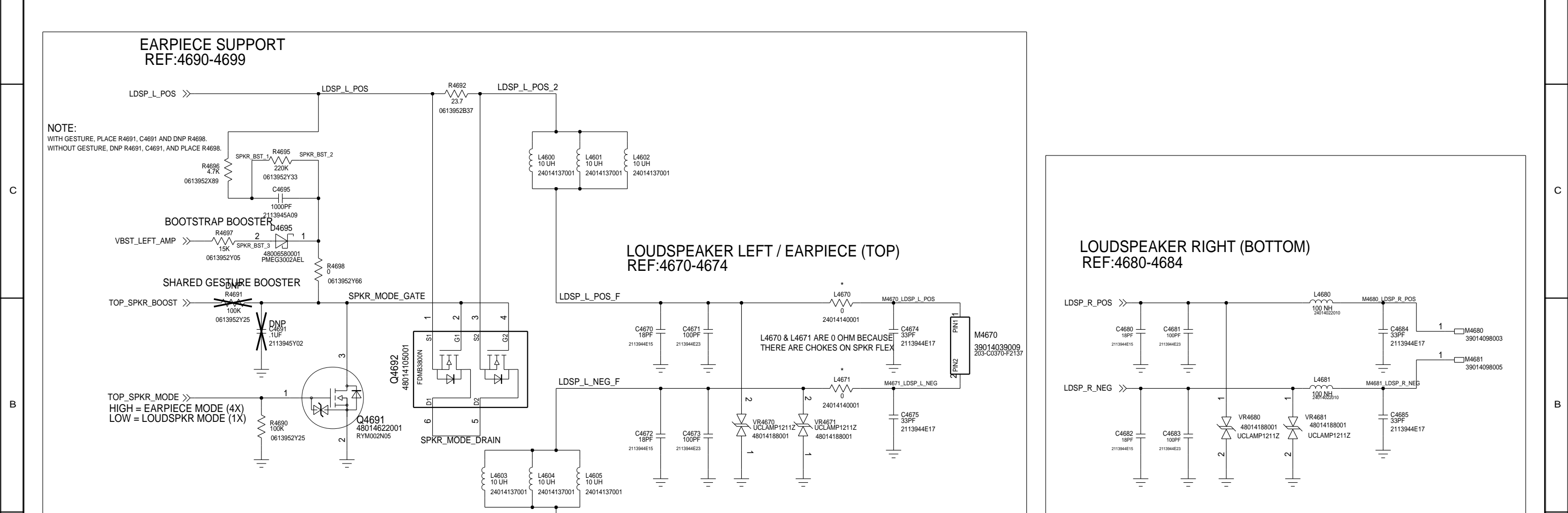
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	mic1	mic2	mic3	mic4	HS mic
Voice Handset	X	X			
Voice Speakerphone		X			
Camcorder		X	X		
AoV	X		X	X	
AoV Loudspeaker		X			
AoV Headset					X

APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA	BLOCK TITLE: <h1>audio_fe</h1>		
LAST MODIFIED BY: MOTOROLA Fri Sep 26 06:12:53 2014			
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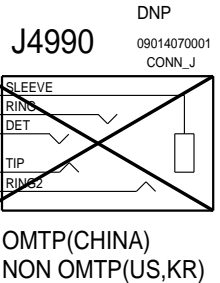
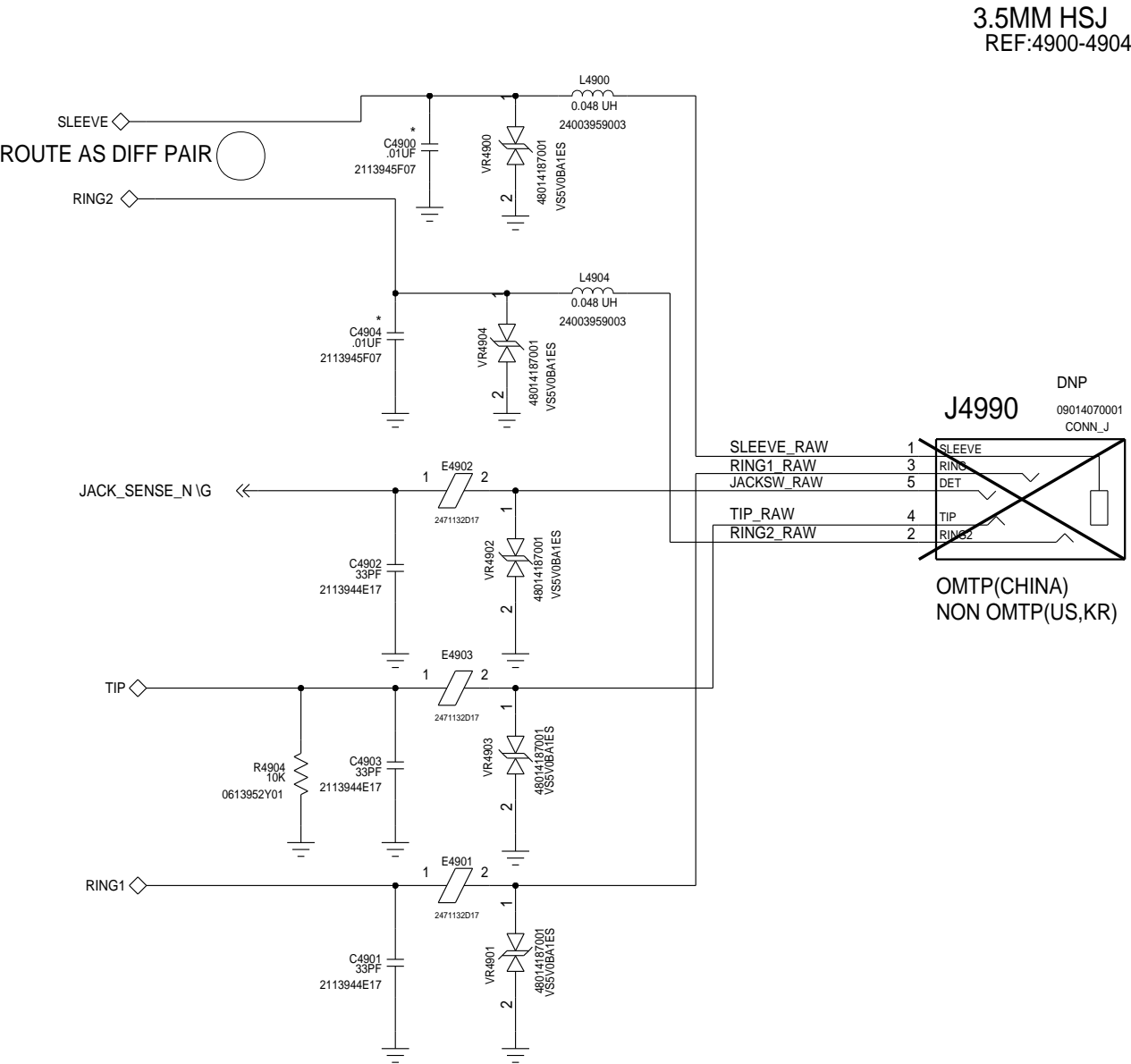
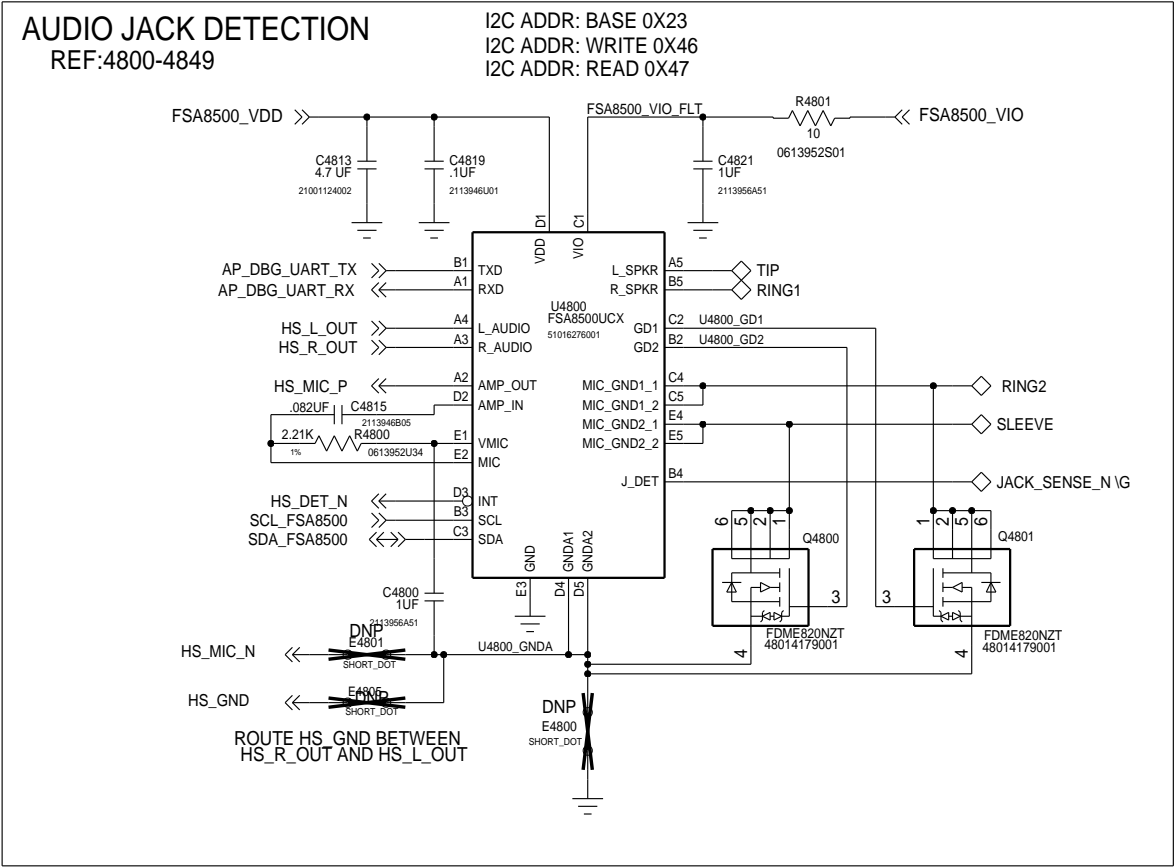
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ENGINEER: DENNIS BUDNICK		BLOCK TITLE: audio_fe	
LAST MODIFIED BY: TIM MILLER Fri Sep 26 06:12:54 2014			
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		84016396001_a.cpm01	
		CAD FILE NAME:	
		PAGE 31 OF 80	

AFE: 3.5MM HEADSET



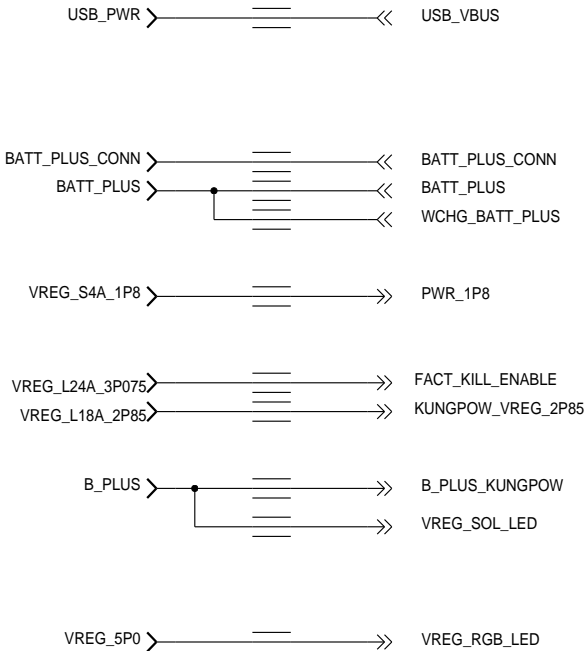
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LAST MODIFIED BY: NATE CONNELL Fri Sep 26 06:12:54 2014		PROJECT NAME B 84016396001_a.cpm	
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REV 01			

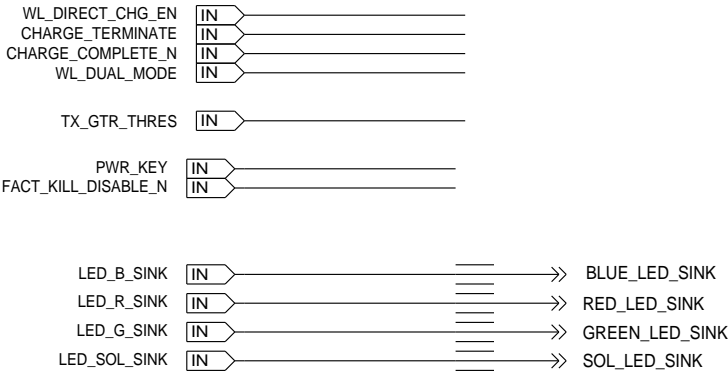
PWR: PORTS

REF 500-799

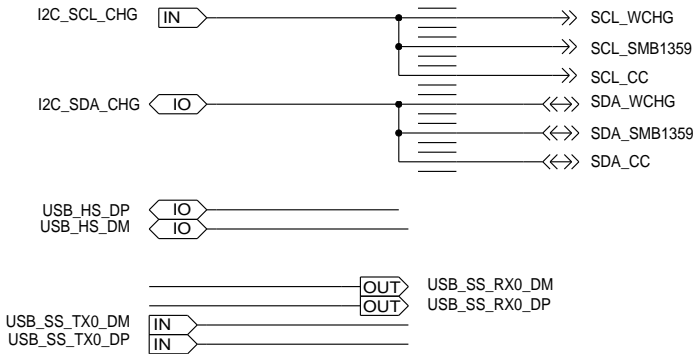
SUPPLIES



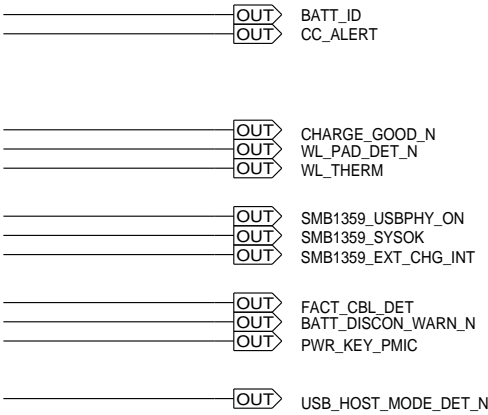
INPUTS



INTERFACES



OUTPUTS



APPROVALS

ENGINEER: NATE CONNELL

LAST MODIFIED BY: NATE CONNELL
Fri Sep 26 06:12:52 2014

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BLOCK TITLE:

power

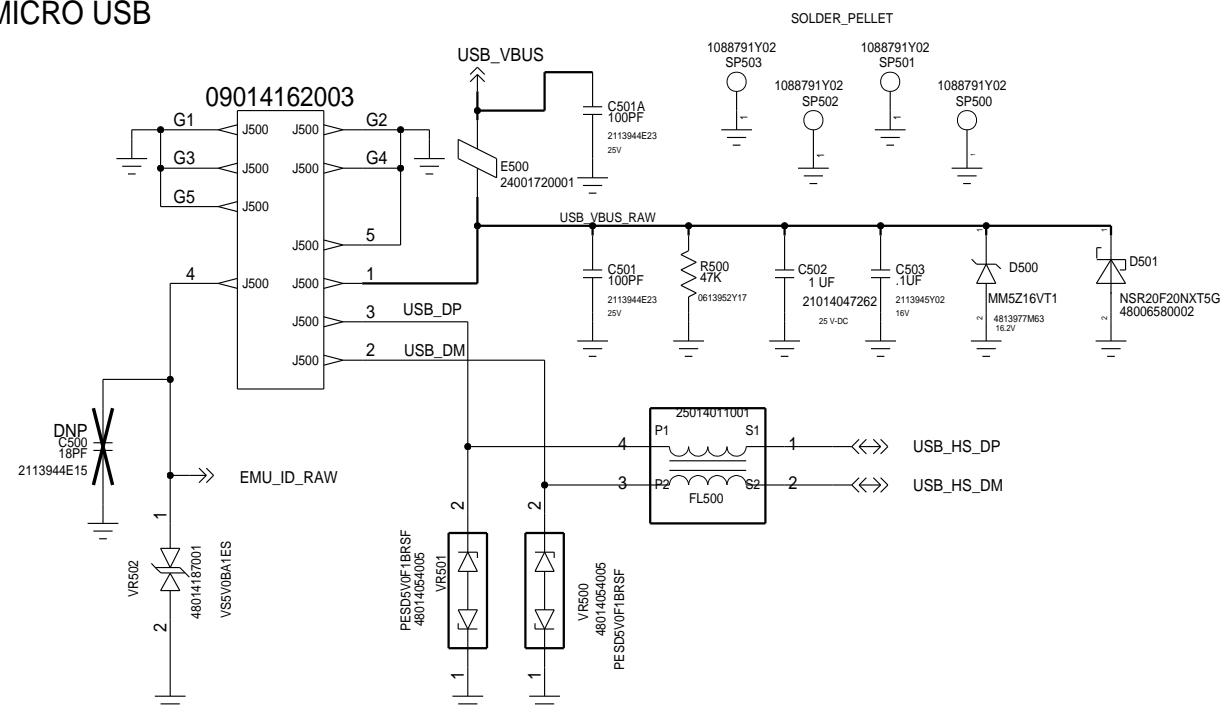
PROJECT NAME
84016396001_a.cpm

REV
01

CAD FILE NAME:

PAGE 33 OF 80

REF: 500-530



NOTE:
FOR BATTERIES THAT SUPPORT POSITIVE CELL SENSE, USE TP_BAT_PIN2 AND DNI R513.
EZ30 BATTERY DOES NOT SUPPORT POSITIVE CELL SENSE. BATTERY FLEX PIN 2 MUST BE FLOATING.

U650
51016348001
SLG4K4386VTR
SLG4K4287

Pin Connections:


- Pin 1: CLR
- Pin 2: FACT_EN
- Pin 3: BP
- Pin 4: ID_DIV5
- Pin 5: PWR_KEY_OUT
- Pin 6: PWR_KEY
- Pin 7: GND
- Pin 8: CBL_PWR
- Pin 9: FACT_DET
- Pin 10: BATT_DIS
- Pin 11: CTGND
- Pin 12: WARN

Other Components and Signals:

- Power Supply:** KUNGPOW_VREG_2P85, EMU_ID_RAW, EMU_ID_DIV5, USB_HOST_MODE_DET_N, PWR_1P8.
- Battery Connection:** BATT_DISCON, BATT_PLUS_CONN, BATT_DISCON_WARN_N.
- Resistors:** R650, R651, R652, R653, R654, R655, R656, R657.
- Capacitors:** C650, C655.
- Diodes:** D650, D651.
- Notes:** R651 IS REQUIRED TO PREVENT LEAKAGE FROM EMU_ID TO VREG WITH FACTORY CABLE. DNP (Do Not Populate) near GND pin.

[illegible]

BATT THERM PLACEMENT WILL BE PRODUCT SPECIFIC
BUT SHOULD FOLLOW THE TOPOLOGY SHOWN HERE

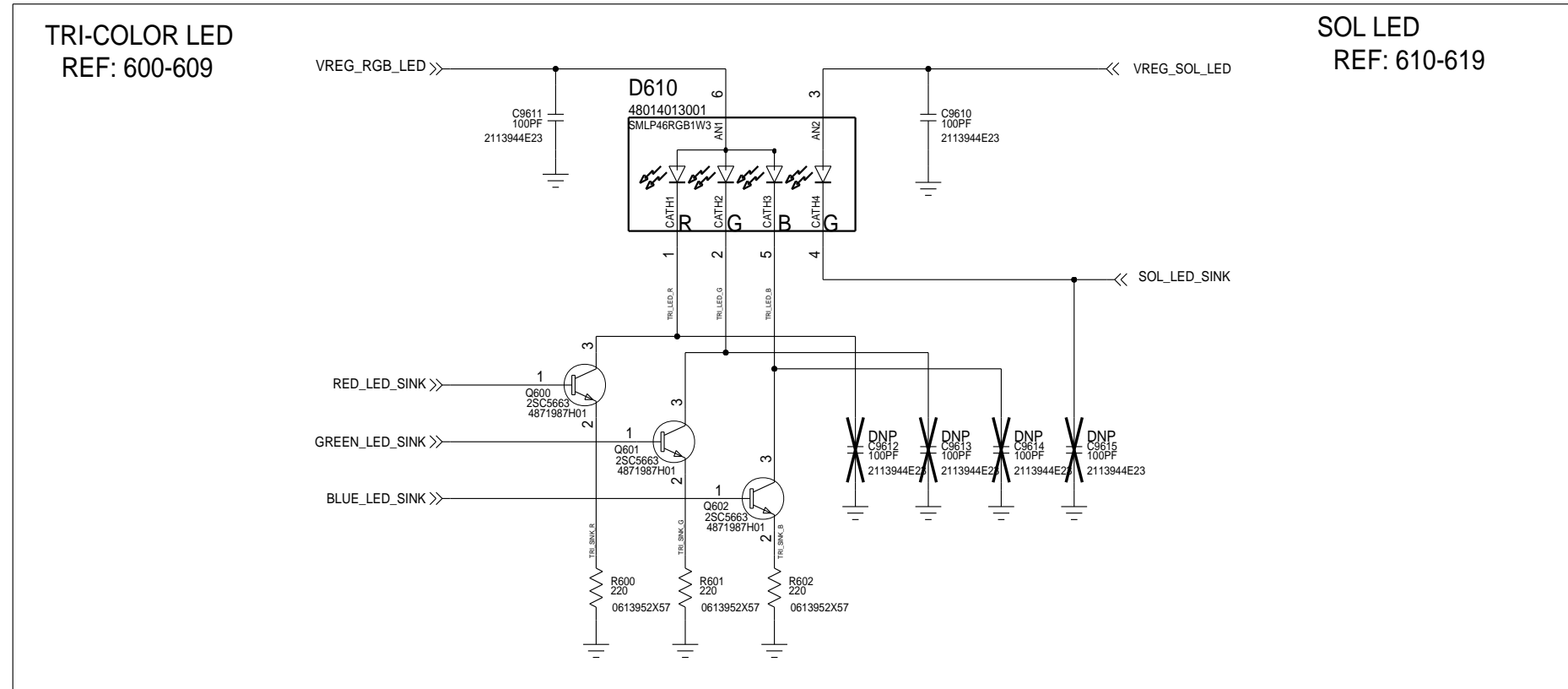
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APPROVALS		 MOTOROLA MOBILE DEVICES			
ENGINEER: RICH NG		BLOCK TITLE: <div>power</div>			
LAST MODIFIED BY: UDI ELRAN Fri Sep 26 06:12:52 2014					
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			84016396001_a.cpm		01
		CAD FILE NAME:			PAGE 34 OF 80

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A	APPROVALS	MOTOROLA	A
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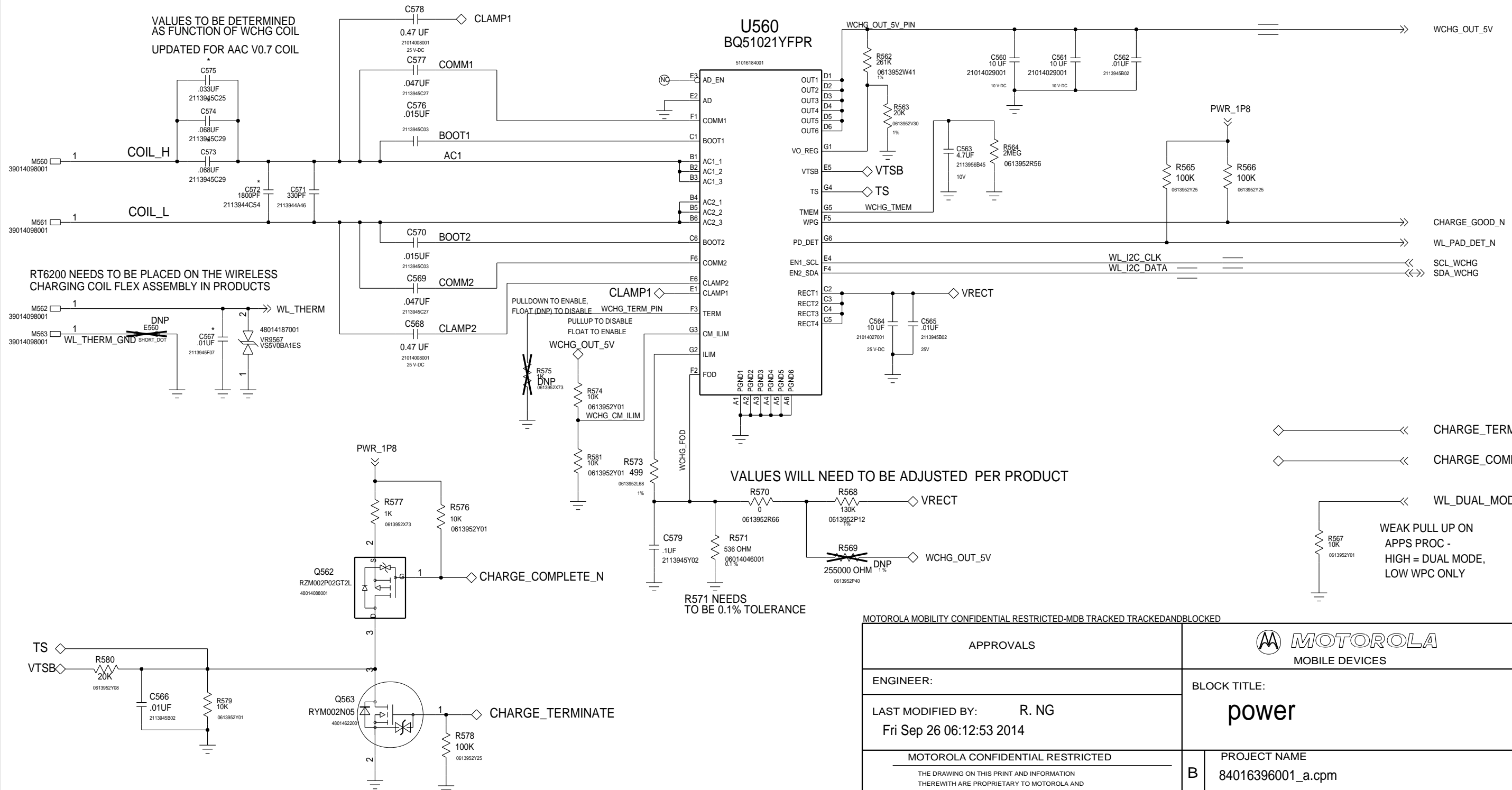
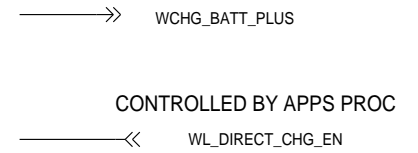
PWR: LIGHTING




MOTOROLA MOBILITY CONFIDENTIAL RESTRICTED-MDB TRACKED TRACKEDANDBLOCKED

APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER:	BLOCK TITLE: <h1>power</h1>		
LAST MODIFIED BY: TIM MILLER Fri Sep 26 06:12:52 2014			
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		CAD FILE NAME:	PAGE 36 OF 80

REF: 560-599

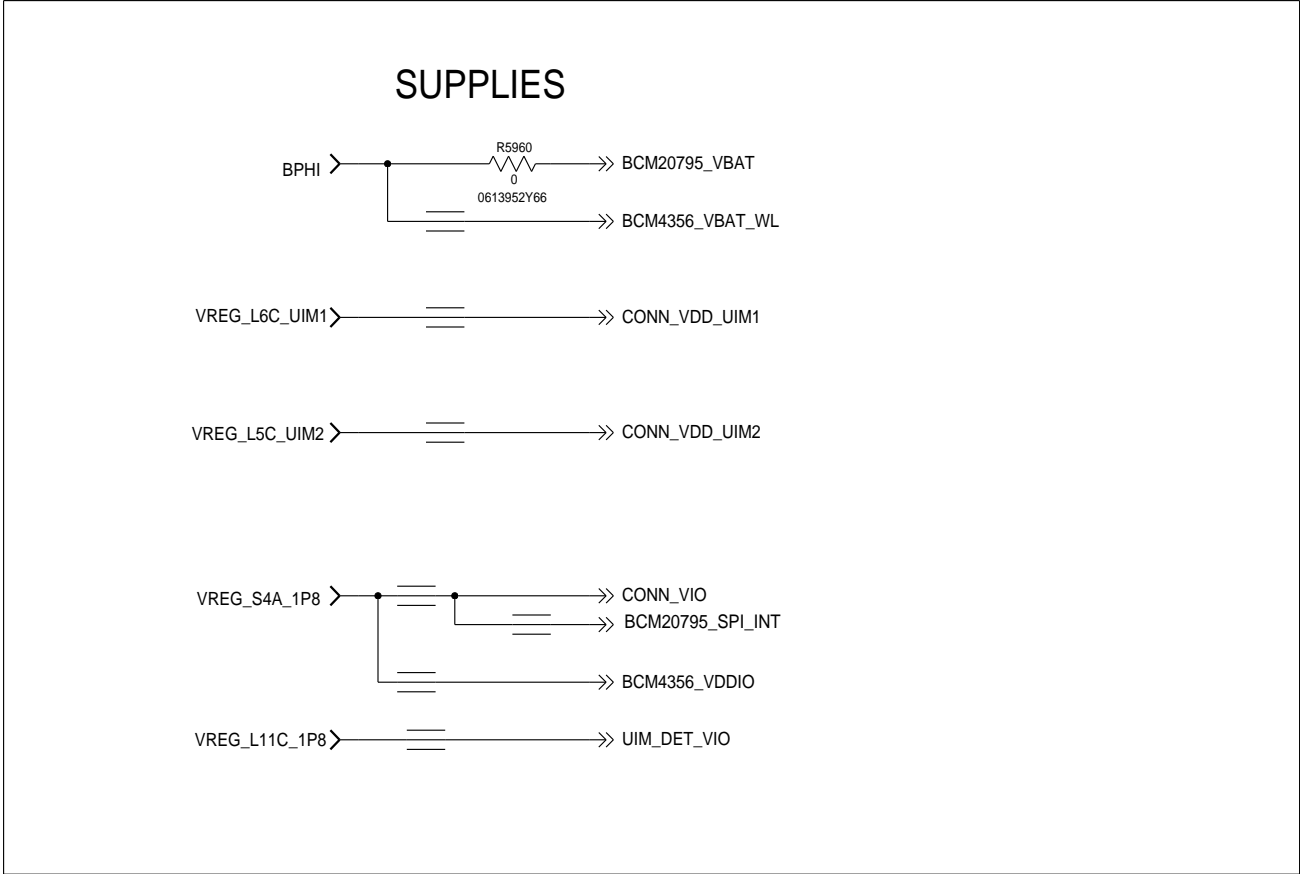


MOTOROLA MOBILITY CONFIDENTIAL RESTRICTED-MDB TRACKED TRACKEDANDBLOCKED

APPROVALS		 MOTOROLA MOBILE DEVICES	
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LAST MODIFIED BY: R. NG Fri Sep 26 06:12:53 2014			
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		CAD FILE NAME:	
		PAGE 37 OF 80	

CONN: POWER

REF 4000-4499



APPROVALS		MOTOROLA MOBILE DEVICES	
ENGINEER: FROEHLING		BLOCK TITLE:	
LAST MODIFIED BY: FROEHLING Fri Sep 26 06:12:50 2014		connectivity	
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		B 84016396001_a.cpm	REV 01
		CAD FILE NAME:	
		PAGE 38 OF 80	

CONN: PORTS

REF 4000-4499

BT/WLAN SYSTEM

BT_WLAN_32KHZ_CLK_IN IN BCM4356_32KHZ_CLK_IN

WLAN

WLAN_ENABLE IN BCM4356_WL_EN
WL_WAKEUP_HOST OUT BCM4356_WL_WAKEUP_HOST
WL_WAKEUP_SLAVE IN BCM4356_WL_WAKEUP_SLAVE

PCIE

PCIE_CLK_REQ_OUT_N OUT BCM4356_PCIE_CLKREQ_N
PCIE_CLKIN_DP IN BCM4356_PCIE_REFCLKP
PCIE_CLKIN_DM IN BCM4356_PCIE_REFCLKN
PCIE_RX_DP IN BCM4356_PCIE_RXP
PCIE_RX_DM IN BCM4356_PCIE_RXN
PCIE_TX_DP OUT BCM4356_PCIE_TXP
PCIE_TX_DM OUT BCM4356_PCIE_TXN
PCIE_RST_IN_N IN BCM4356_PCIE_RST_N
PCIE_HOST_WAKE_OUT_N OUT BCM4356_PCIE_WAKE_N

BT CONTROLS AND INTERFACES

BT_ENABLE IN BCM4356_BT_EN
BT_SLAVE_WAKE_IN IN BCM4356_BT_WAKEUP_SLAVE
BT_HOST_WAKE_OUT OUT BCM4356_BT_WAKEUP_HOST

BT_PCM_IN IN BCM4356_PCM_IN
BT_PCM_OUT OUT BCM4356_PCM_OUT
BT_PCM_CLK IN BCM4356_PCM_CLK
BT_PCM_SYNC IO BCM4356_PCM_SYNC

BT_UART_TX OUT BCM4356_UART_TXD
BT_UART_RX IN BCM4356_UART_RXD
BT_UART_CTS_N IN BCM4356_UART_CTS_N
BT_UART_RTS_N OUT BCM4356_UART_RTS_N

CELLULAR MODEM/GPS COEXISTENCE

MODEM_LTE_SYNC_UART_RXD IN BCM4356_GPIO4_SECI_UART_RX
MODEM_LTE_ACTIVE IN NO_CONNECT_LTE_ACTIVE
WLAN_LTE_PRI_UART_TXD OUT BCM4356_GPIO5_SECI_UART_TX
WLAN_GPS_COEX OUT BCM4356_TX_RF_ACTIVE

NFC

NFC_CLK_IN IN BCM20795_EXT_CLK_IN
NFC_CLK_REQ_OUT OUT BCM20795_CLK_REQ_OUT
NFC_ENABLE_IN IN BCM20795_ENABLE_IN
NFC_IRQ_OUT OUT BCM20795_IRQ_OUT
I2C_SCL_NFC IN BCM20795_SCL
I2C_SDA_NFC IO BCM20795_SDA
NFC_POWER_ON IN BCM20795_REG_PU

UIM1

UIM1_DATA IO UIM1_DATA
UIM1_CLK IN UIM1_CLK
UIM1_RST IN UIM1_RST
UIM1_DET OUT UIM1_DET

UIM2

UIM2_DATA IO UIM2_DATA
UIM2_CLK IN UIM2_CLK
UIM2_RST IN UIM2_RST
UIM2_DET OUT UIM2_DET

ANTENNA

STREAM0_BT IO BT_WLAN_ANT0
STREAM1 IO WLAN_ANT1

APPROVALS

ENGINEER: FROEHLING

LAST MODIFIED BY: FROEHLING
Mon May 05 19:14:54 2014

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connectivity

B

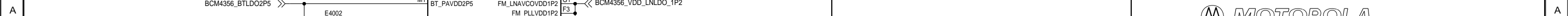
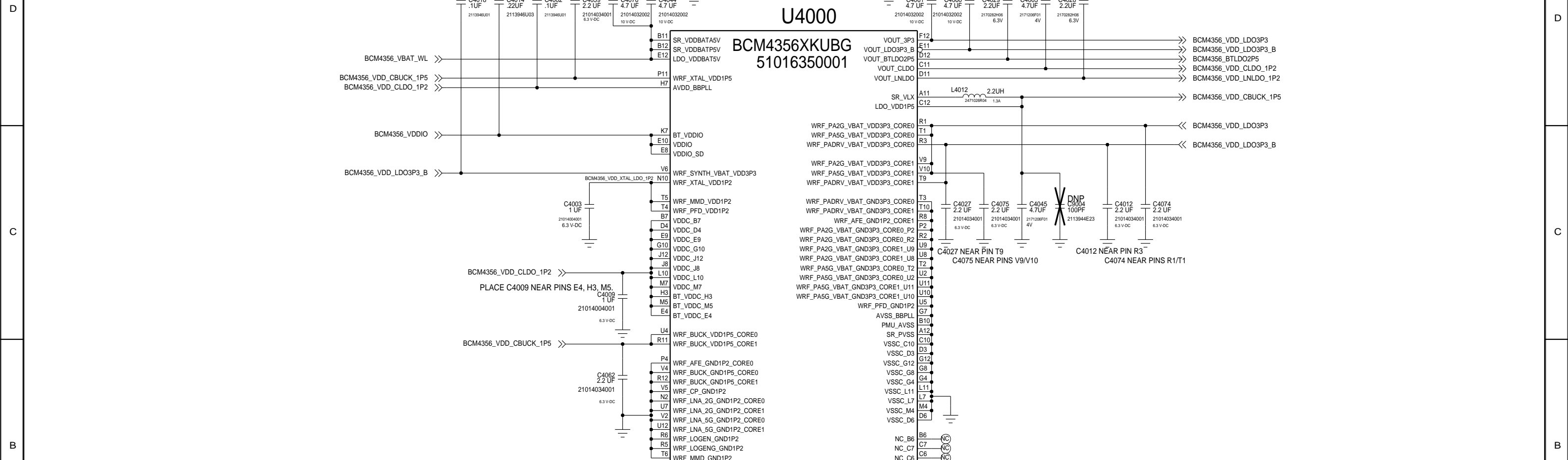
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84016396001_a.cpm

REV
01

CAD FILE NAME:

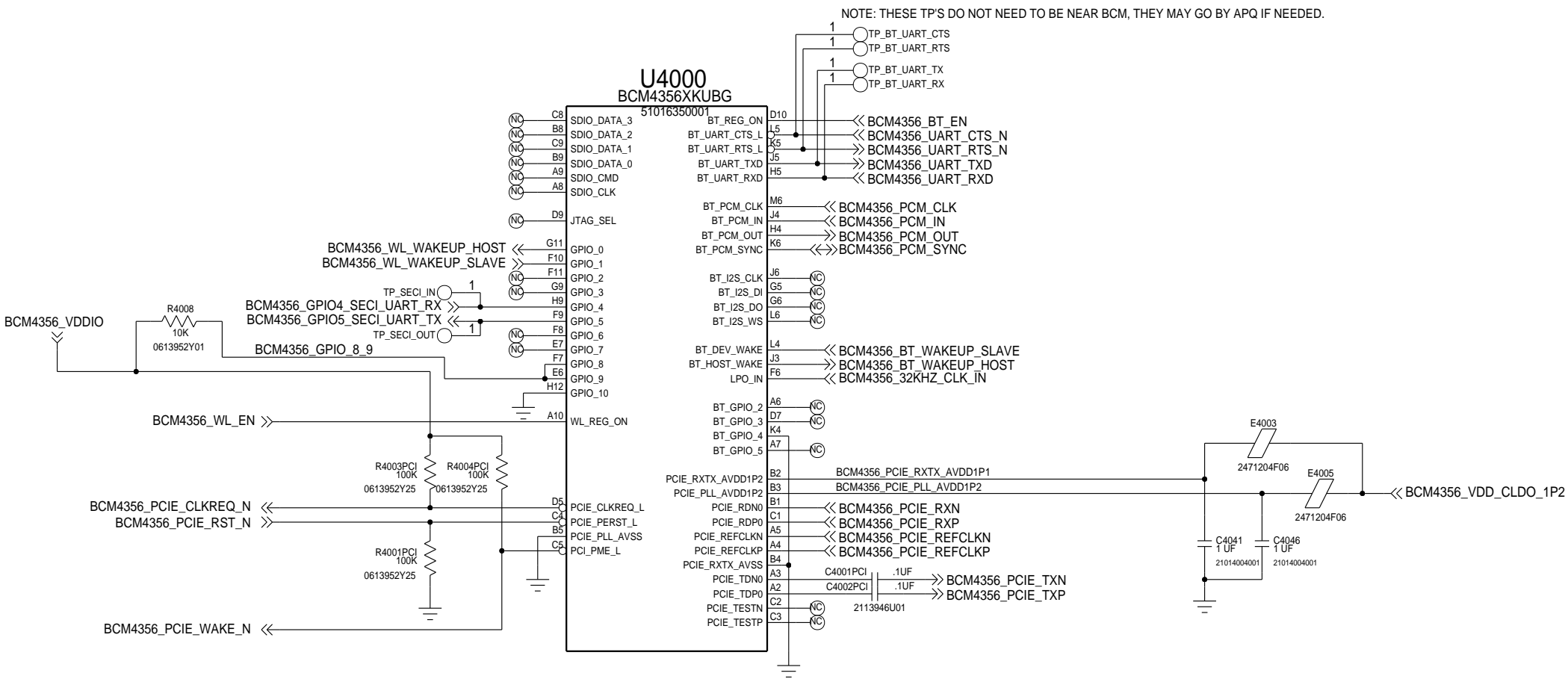
PAGE 39 OF 80

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CONN: BCM4356 CONTROL
REF 4000-4099



APPROVALS		MOTOROLA MOBILE DEVICES	
ENGINEER: FROEHLING		BLOCK TITLE:	
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		REV 01	
		CAD FILE NAME:	
		PAGE 41 OF 80	

D



B

A

A

A


2

D



B

A

APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: FROEHLING	BLOCK TITLE: <h1>connectivity</h1>		
LAST MODIFIED BY: FROEHLING Fri Sep 26 06:12:51 2014			
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		CAD FILE NAME:	PAGE 43 OF 80

D

OR

I2C ADDR: 0X76 (BOOT DEFAULT WHEN PIN 3 IS FLOATED/PULLED HIGH)



D

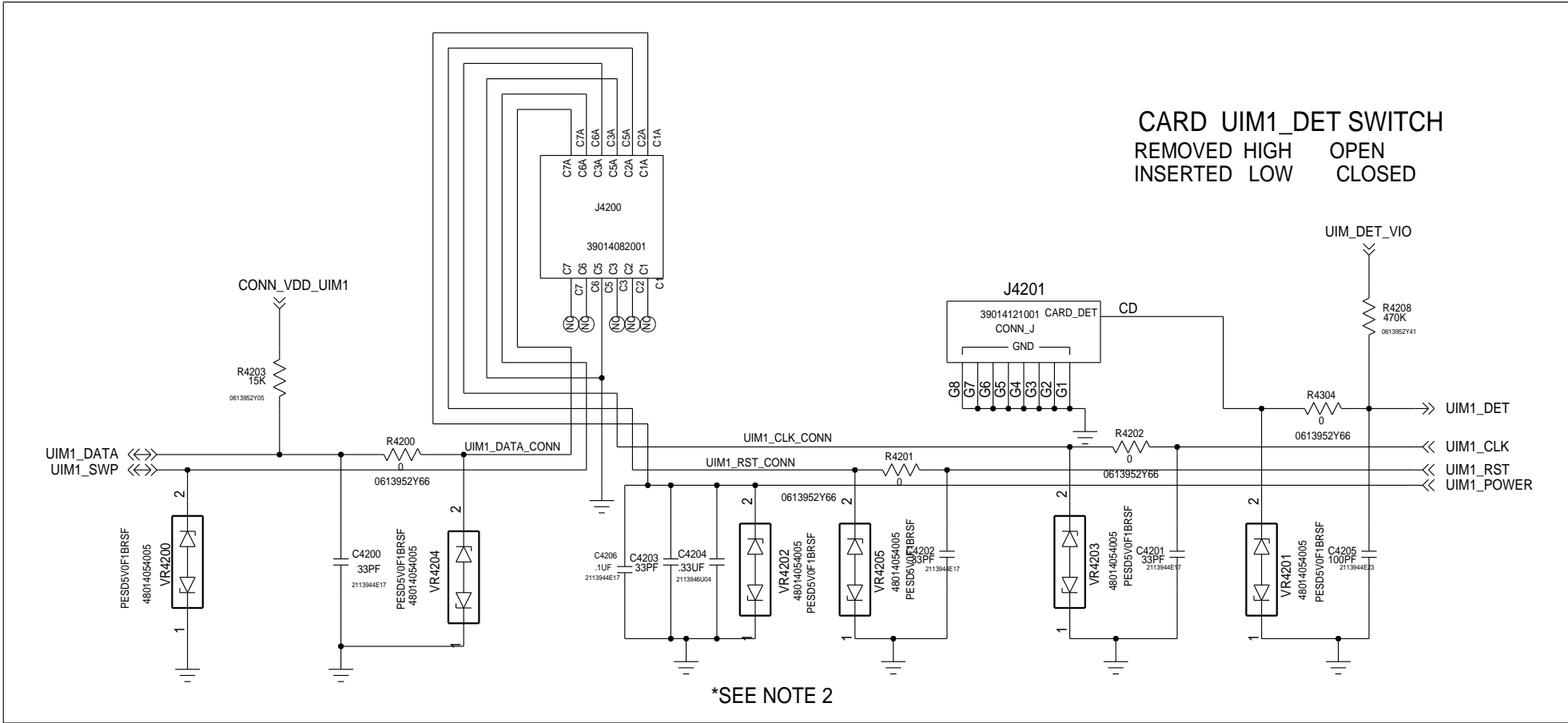
C

2. L5900W AND L5901W PLACEMENT MUST MINIMIZE MUTUAL INDUCTIVE COUPLING, OR CIRCUIT'S WIRELESS FUNCTION WILL BE SEVERELY IMPAIRED

PAGE 44 OF 80

CONN: UIM1


REF:4200-4219



NOTE 1: DELETED

NOTE 2: UIM_PWR BYPASS OF 0.33UF RECOMMENDED BY NFC IC SUPPLIER, PLACE NEAR CONNECTOR.

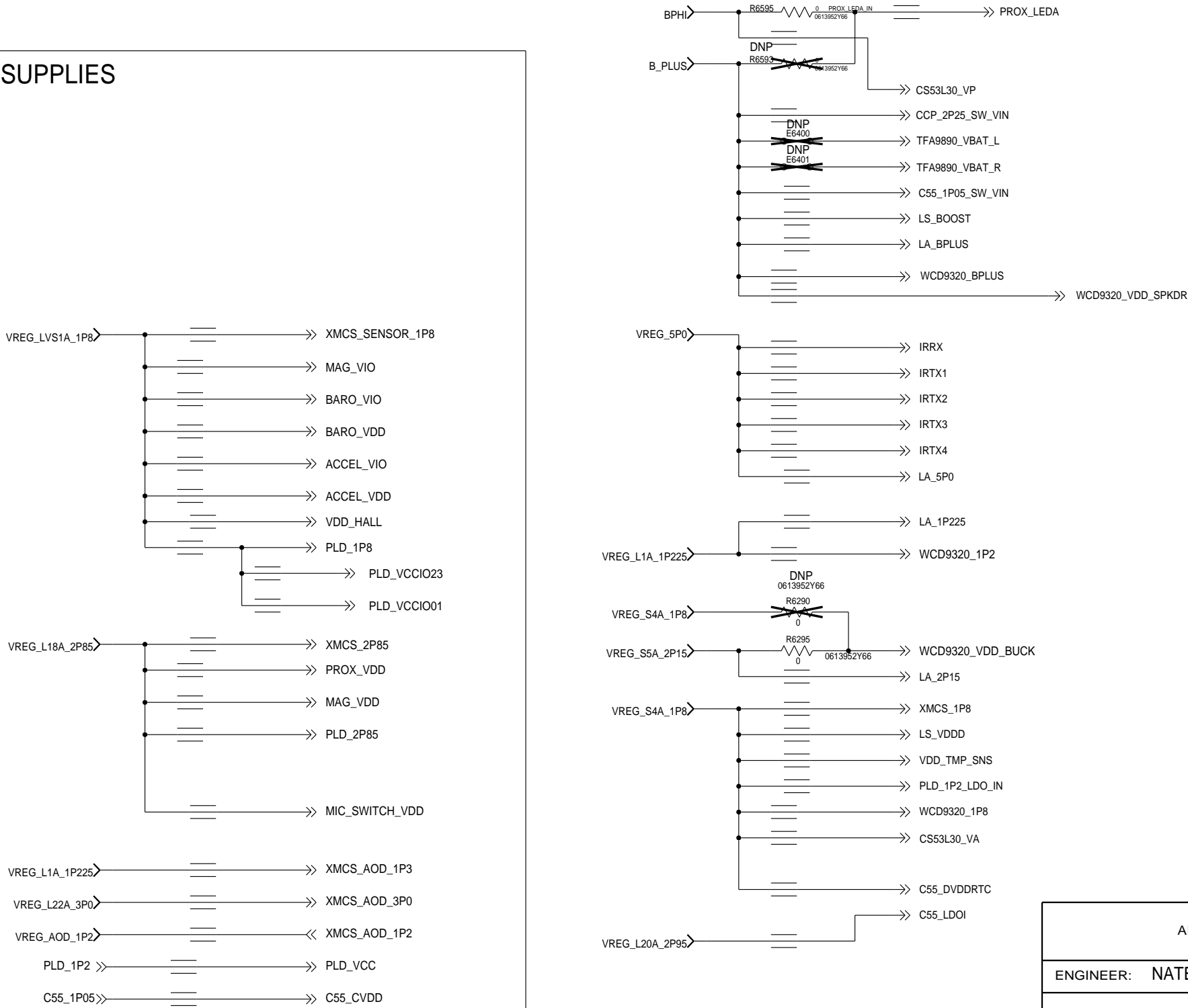
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LAST MODIFIED BY: FROEHLING Fri Sep 26 06:12:51 2014		connectivity	
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		B 84016396001_a.cpm	
		CAD FILE NAME:	
		REV 01	
		PAGE 45 OF 80	

XMCS: POWER

REF 6000-6999

SUPPLIES

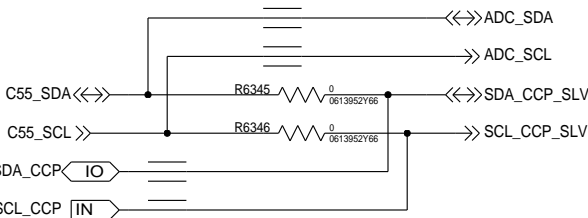
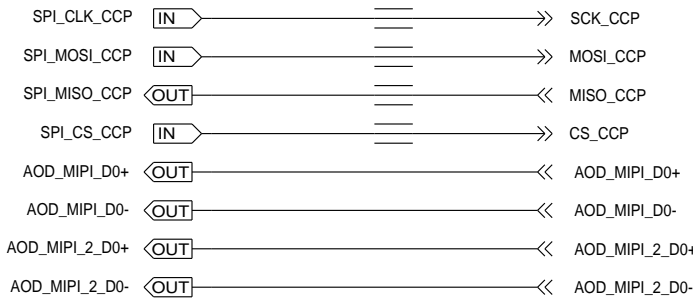
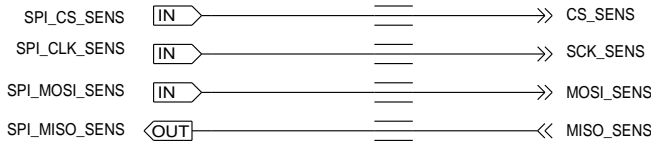
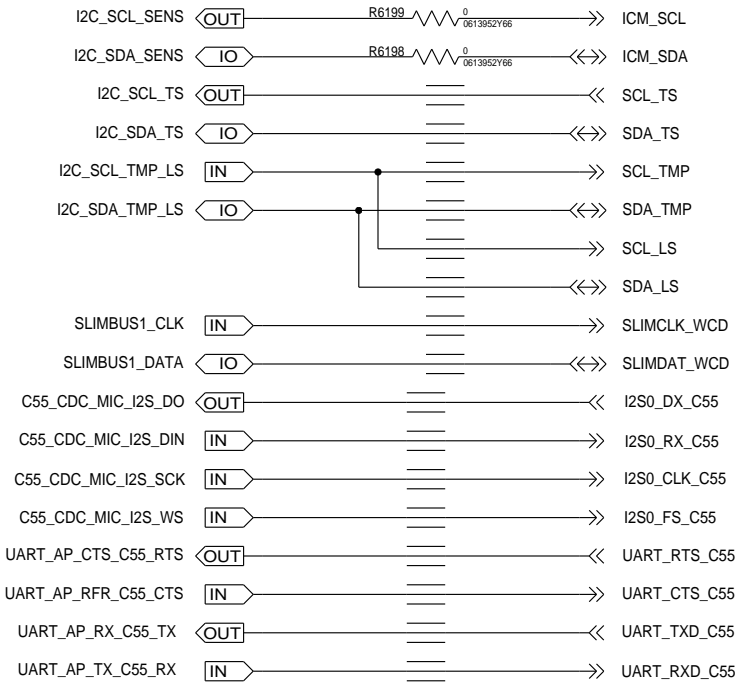


APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: NATE CONNELL		BLOCK TITLE:	
LAST MODIFIED BY: NATE CONNELL Fri Sep 26 06:12:45 2014		xmcs	
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		01	
		CAD FILE NAME:	
		PAGE 46 OF 80	

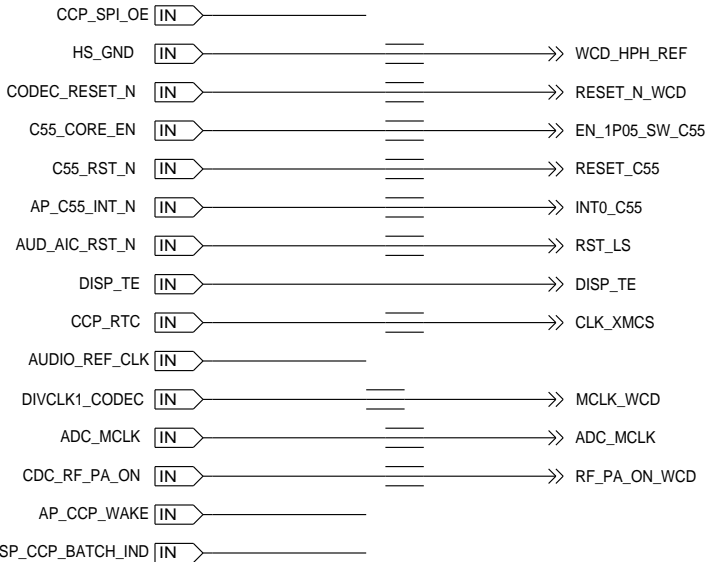
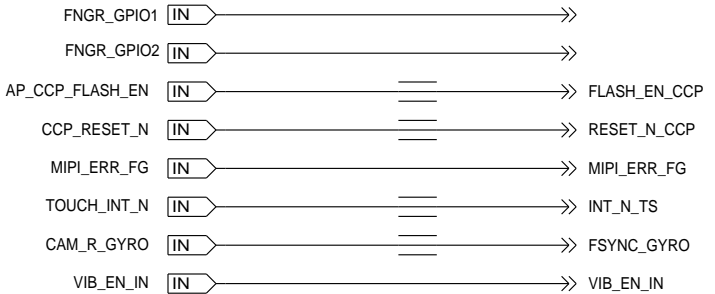
XMCS: PORTS

REF 6000-6999

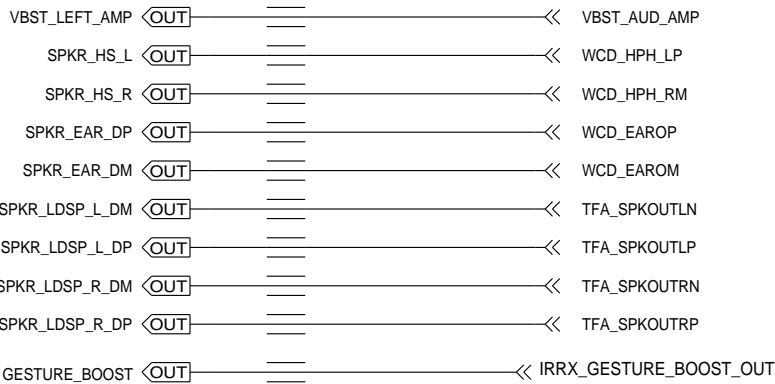
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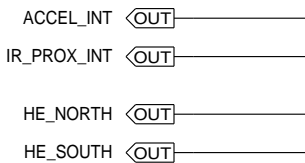
INPUTS



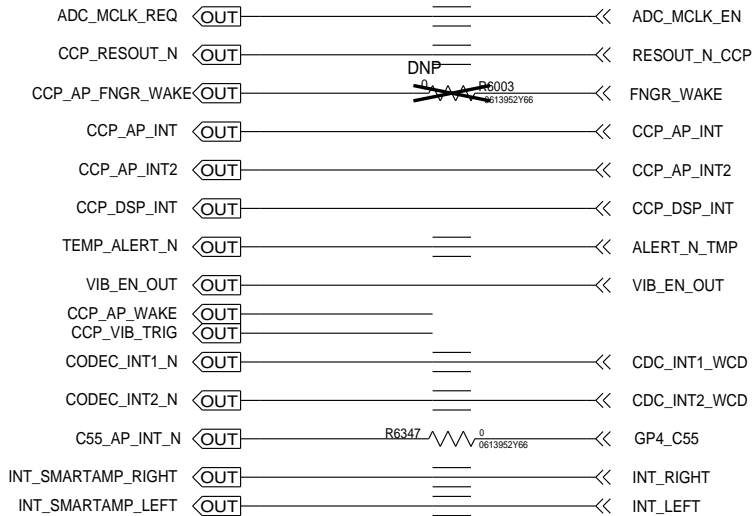
SPEAKER PATHS



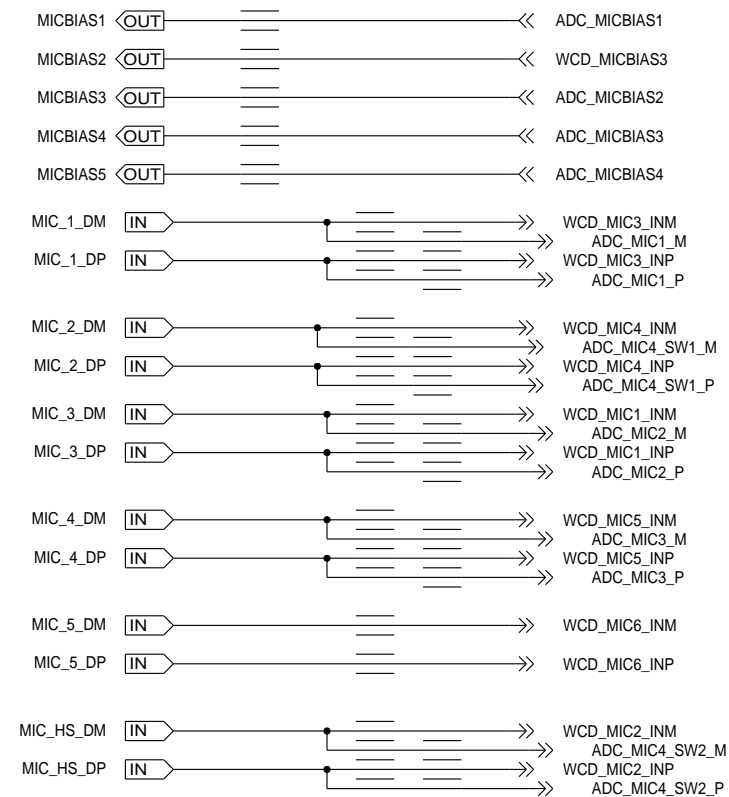
SENSOR INTERRUPT OUTPUTS



OUTPUTS



MIC PATHS



APPROVALS

ENGINEER: NATE CONNELL

LAST MODIFIED BY: NATE CONNELL
Fri Jul 11 13:58:52 2014

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MOTOROLA

MOBILE DEVICES

BLOCK TITLE:

xmcs

PROJECT NAME

B 84016396001_a.cpm

REV

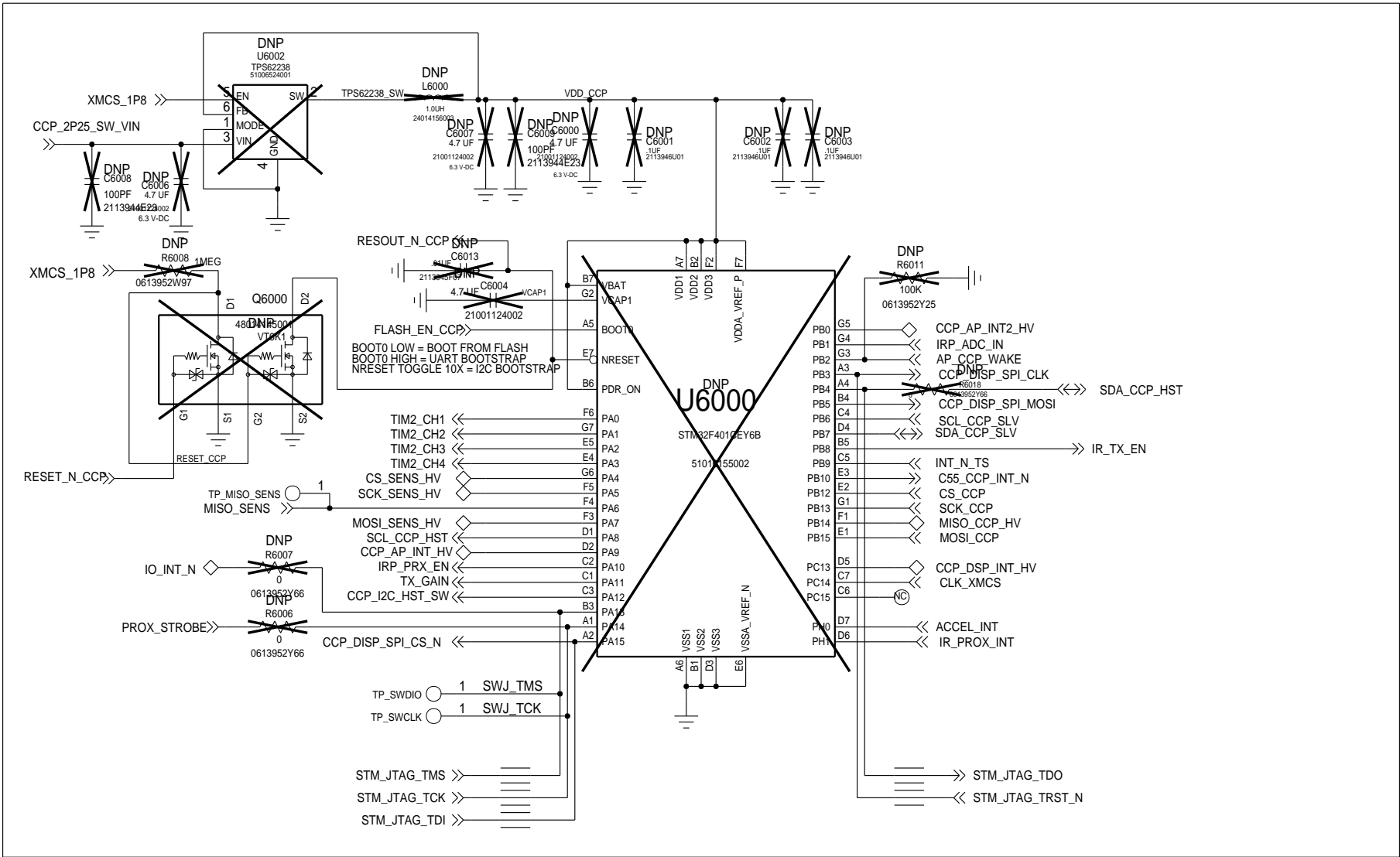
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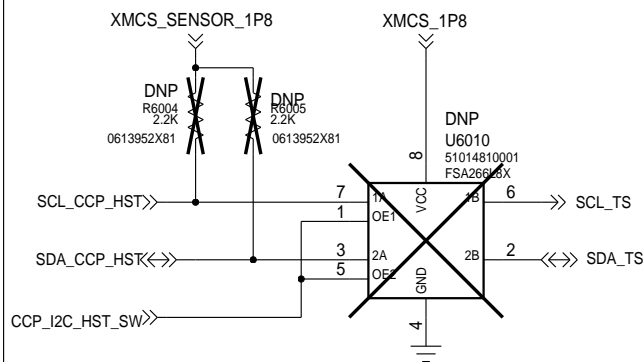
PAGE 47 OF 80

XMCS: CCP

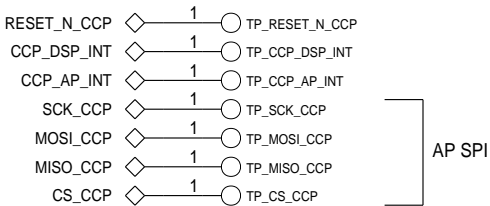
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I2C SUPPORT

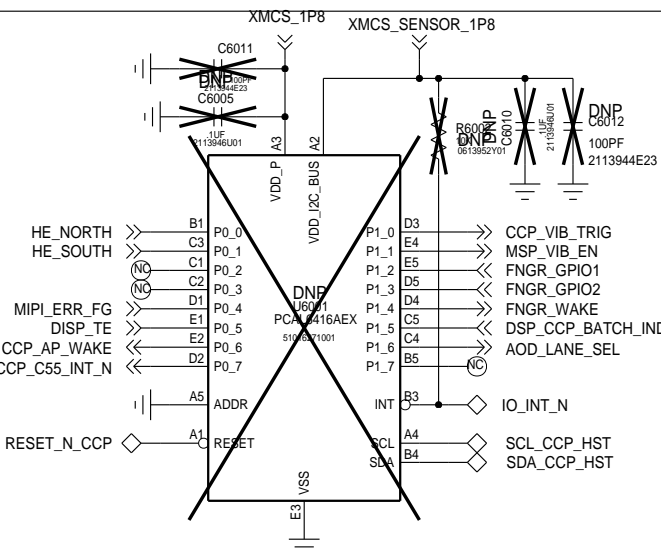


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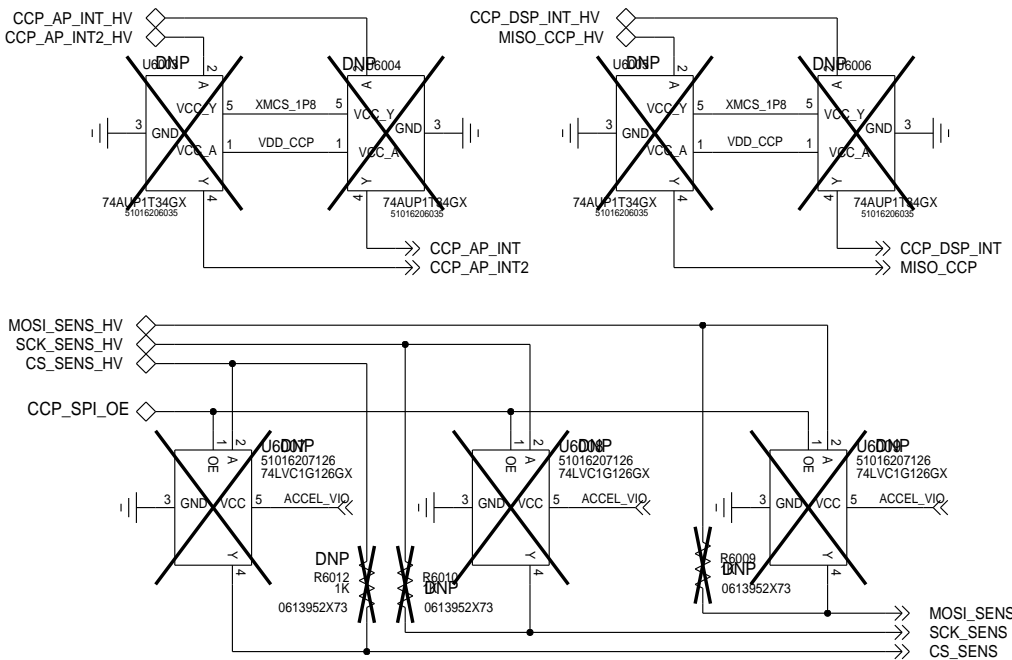


I/O EXPANSION

I2C ADDR: 0X20



I/O LEVEL SHIFTERS



APPROVALS

ENGINEER: JOHN GORSICA

LAST MODIFIED BY: NATE CONNELL
Fri Sep 26 06:12:46 2014

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MOBILE DEVICES

BLOCK TITLE:

xmcs

PROJECT NAME
84016396001_a.cpm

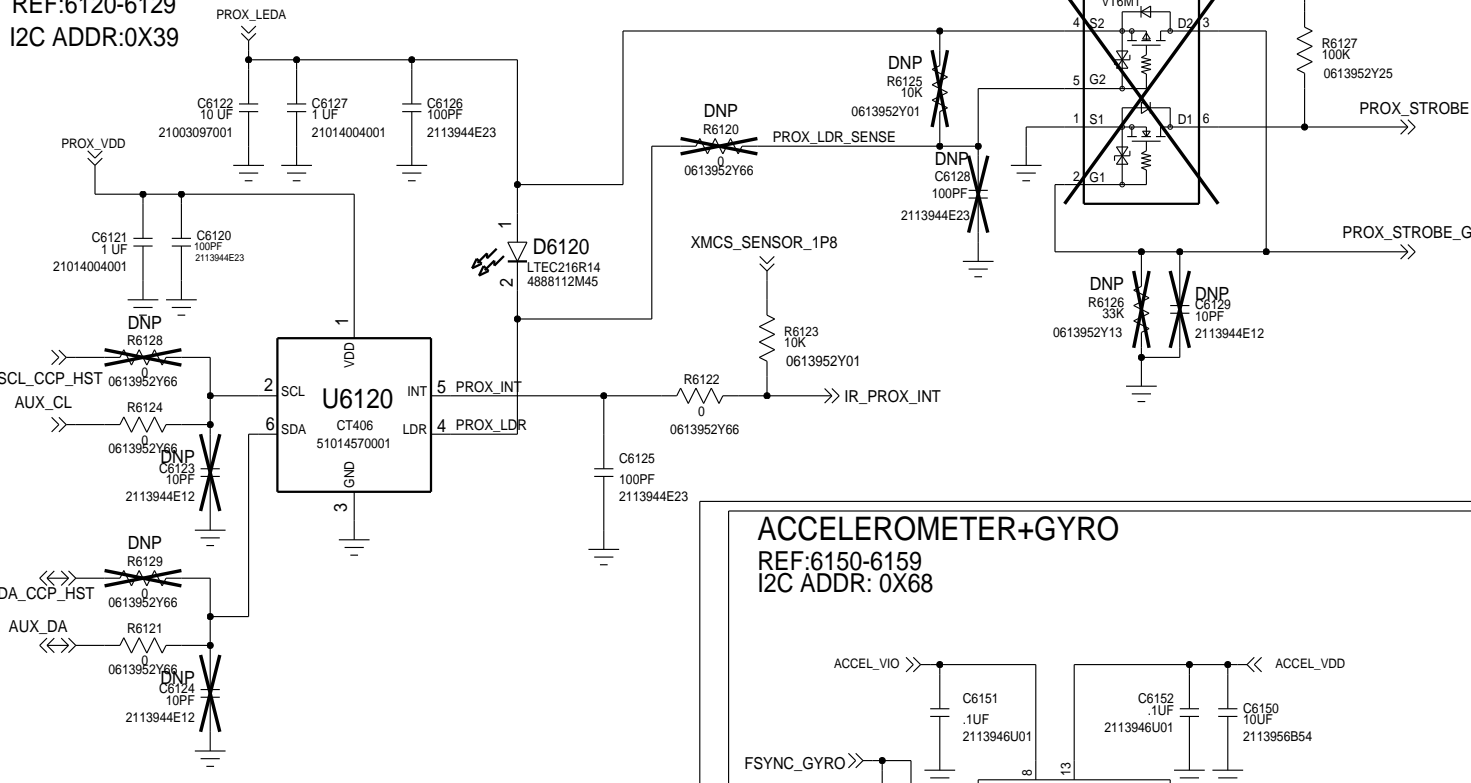
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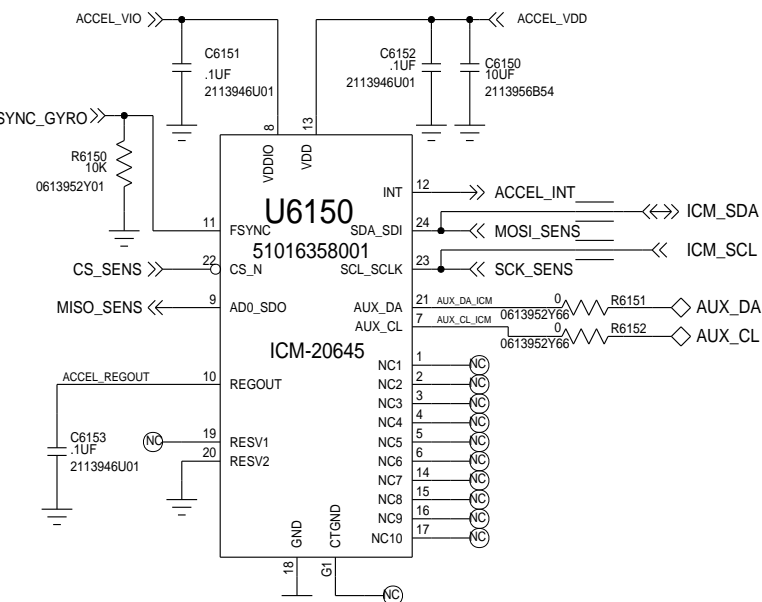
XMCS: SENSORS

REF: 6100-6199

PROX SENSOR/ALS
REF:6120-6129
I2C ADDR:0X39

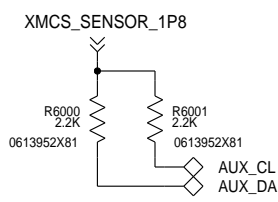


ACCELEROMETER+GYRO
REF:6150-6159
I2C ADDR: 0X68

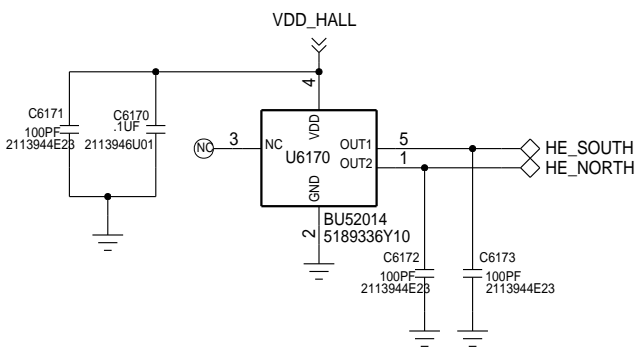


PRODUCTS MUST FOLLOW ABBAGOOCHIE PCB ORIENTATION

I2C SUPPORT

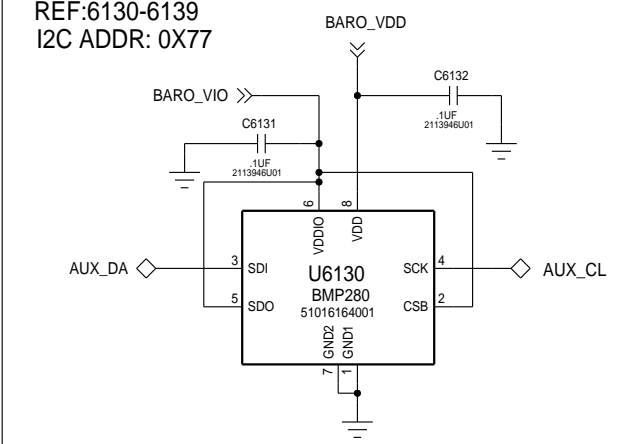


HALL EFFECT
REF:6170-6179

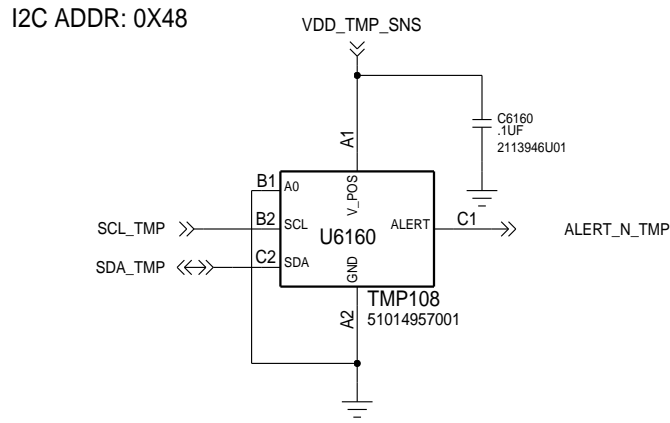


2ND HALL EFFECT FOR SMART COVER SUPPORT

BAROMETER
REF:6130-6139
I2C ADDR: 0X77



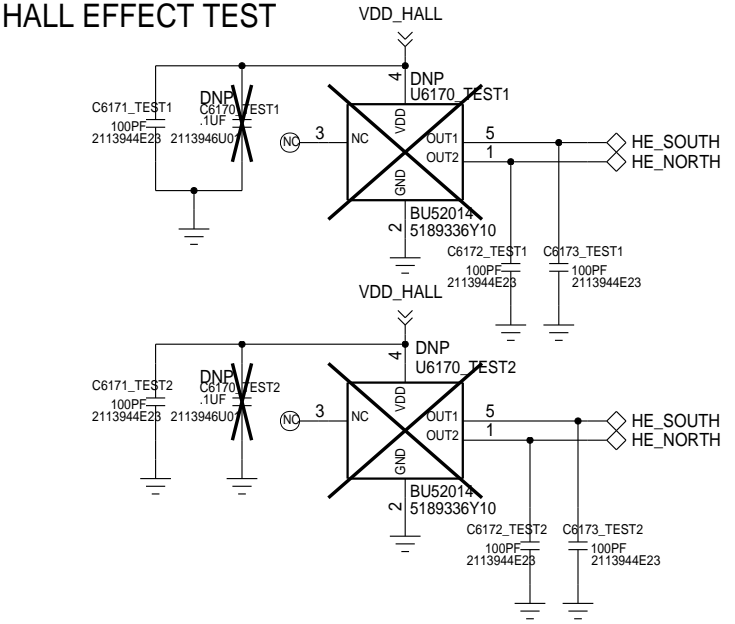
BOARD TEMP SENSORS
REF:6160-6169



I2C ADDR: 0X49

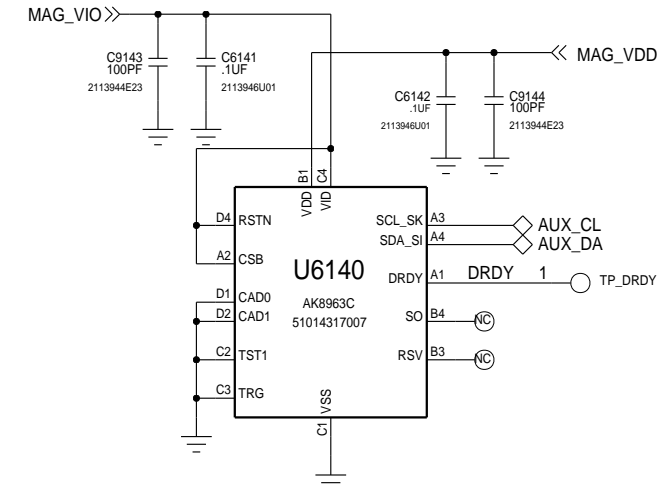
U6161 DELETED

HALL EFFECT TEST



MAGNETOMETER
REF:6140-6149

I2C ADDR: 0X0C



JOHN GORSICA/NATE CONNELL
APPROVALS

ENGINEER: C HERNANDEZ

LAST MODIFIED BY: C HERNANDEZ
Fri Sep 26 06:12:46 2014

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BLOCK TITLE:

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PROJECT NAME
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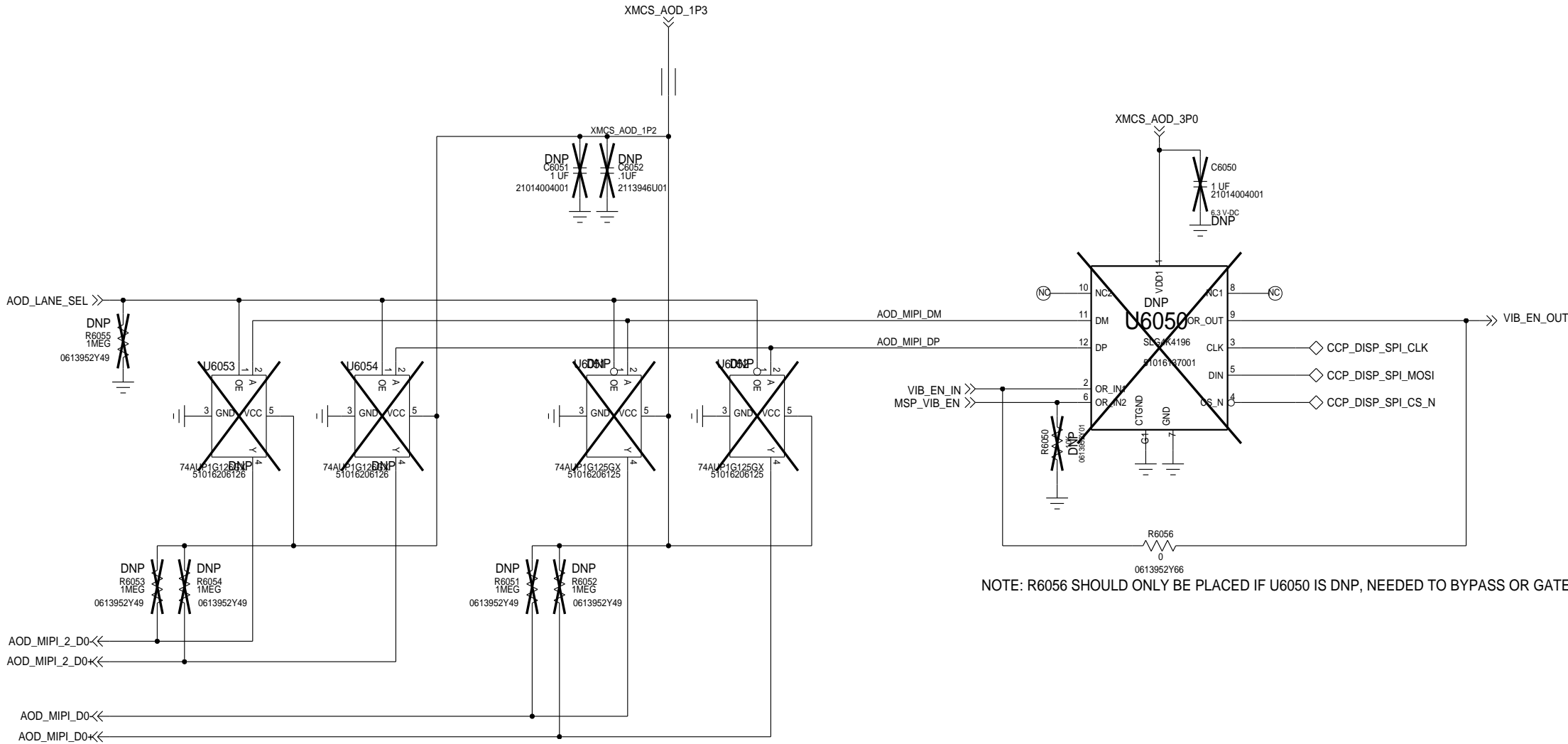
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CAD FILE NAME:


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XMCS: AOD

REF: 6050-6059

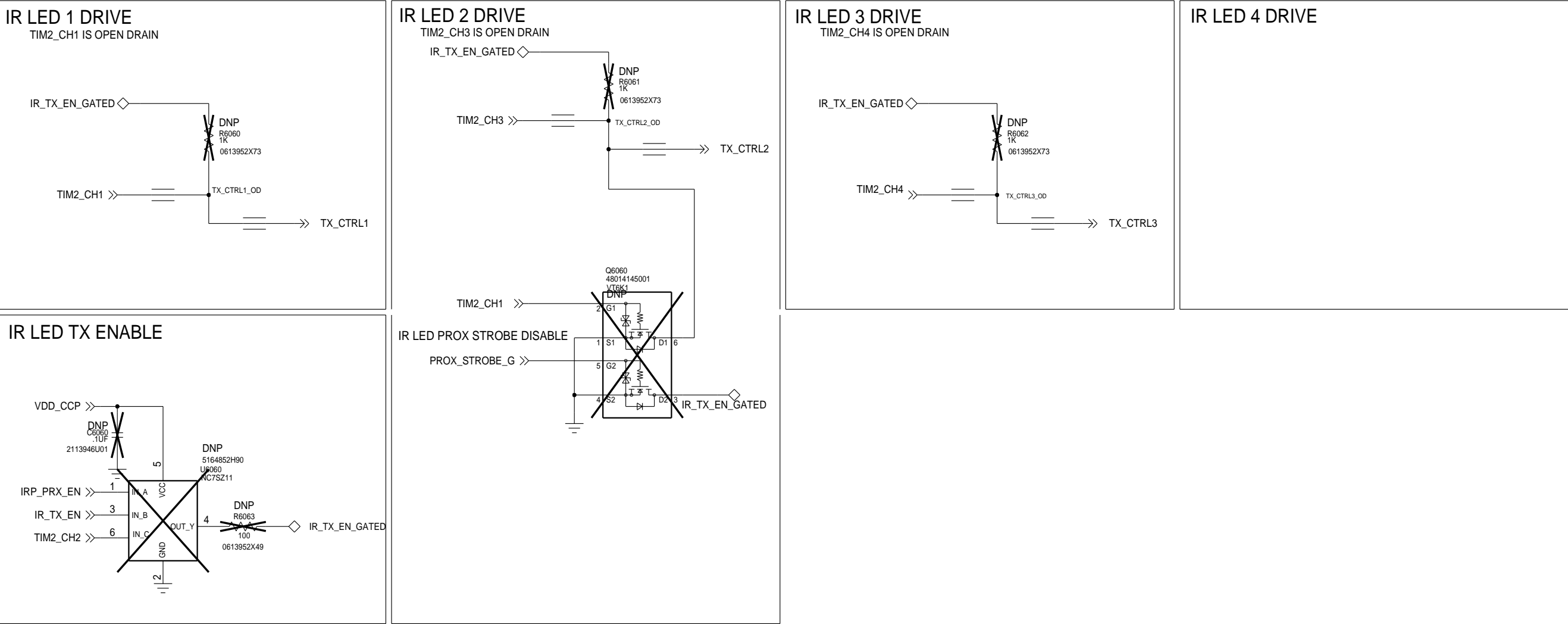


NOTE: R6056 SHOULD ONLY BE PLACED IF U6050 IS DNP, NEEDED TO BYPASS OR GATE.

APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE:	
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		CAD FILE NAME:	
		REV 01	
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XMCS: NATIVE IR

REF: 6060-6069



APPROVALS		MOTOROLA MOBILE DEVICES	
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		B 84016396001_a.cpm	01
		CAD FILE NAME:	
		PAGE 51 OF 80	

8	7	6	5	4	3	2	1
<div><div>XMCS: FPGA IR</div><div>REF: 6500-6599</div></div> <div>FPGA IR DELETED</div>							
D							D
C							C
B							B
A							A
8	7	6	5	4	3	2	1

APPROVALS		<div> MOTOROLA</div> <div>MOBILE DEVICES</div>	
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		REV 01	
		CAD FILE NAME:	
		PAGE 52 OF 80	

XMCS: IR LED

REF 6500-6549

IR TRANSMITTER

IR TX 1 TOP RIGHT

IR TX 2 BOTTOM LEFT

IR TX 3 BOTTOM RIGHT

IR RECEIVER

GESTURE BOOST

FOR 10V OPERATION, R6592 = 140K (0613952W15)
FOR 12V OPERATION, R6592 = 113K (0613952W06)

MOTOROLA MOBILITY CONFIDENTIAL RESTRICTED-MDB TRACKED TRACKEDANDBLOCKED

APPROVALS

ENGINEER: MATT MOTTIER

LAST MODIFIED BY: UDI ELRAN
Fri Sep 26 06:12:48 2014

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PROJECT NAME

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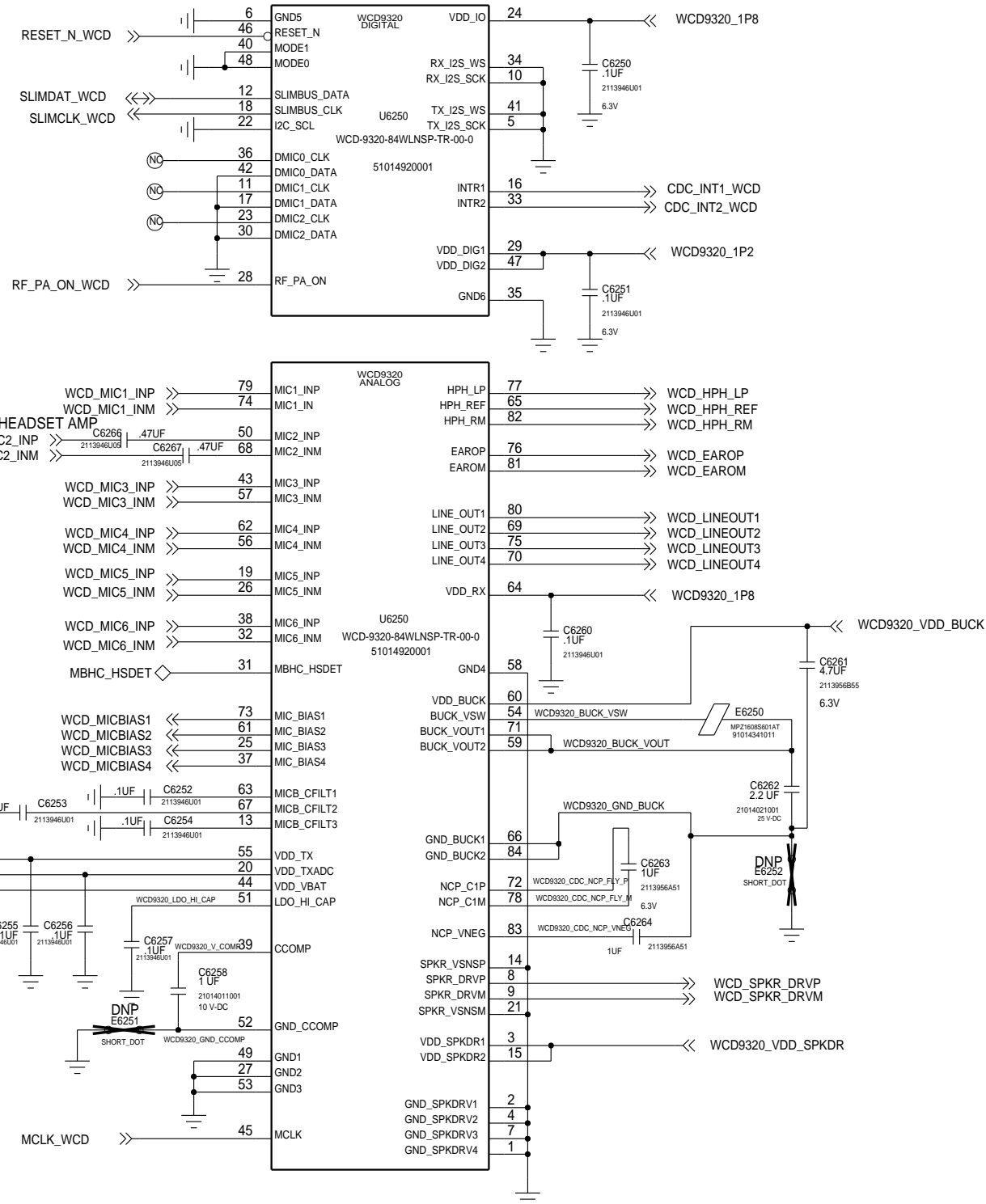
REV

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CAD FILE NAME:

PAGE 53 OF 80

REF 6250-6299



APPROVALS

ENGINEER:

LAST MODIFIED BY:

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BLOCK TITLE:

xmcs

PROJECT NAME

B	84016396001_a.cpm
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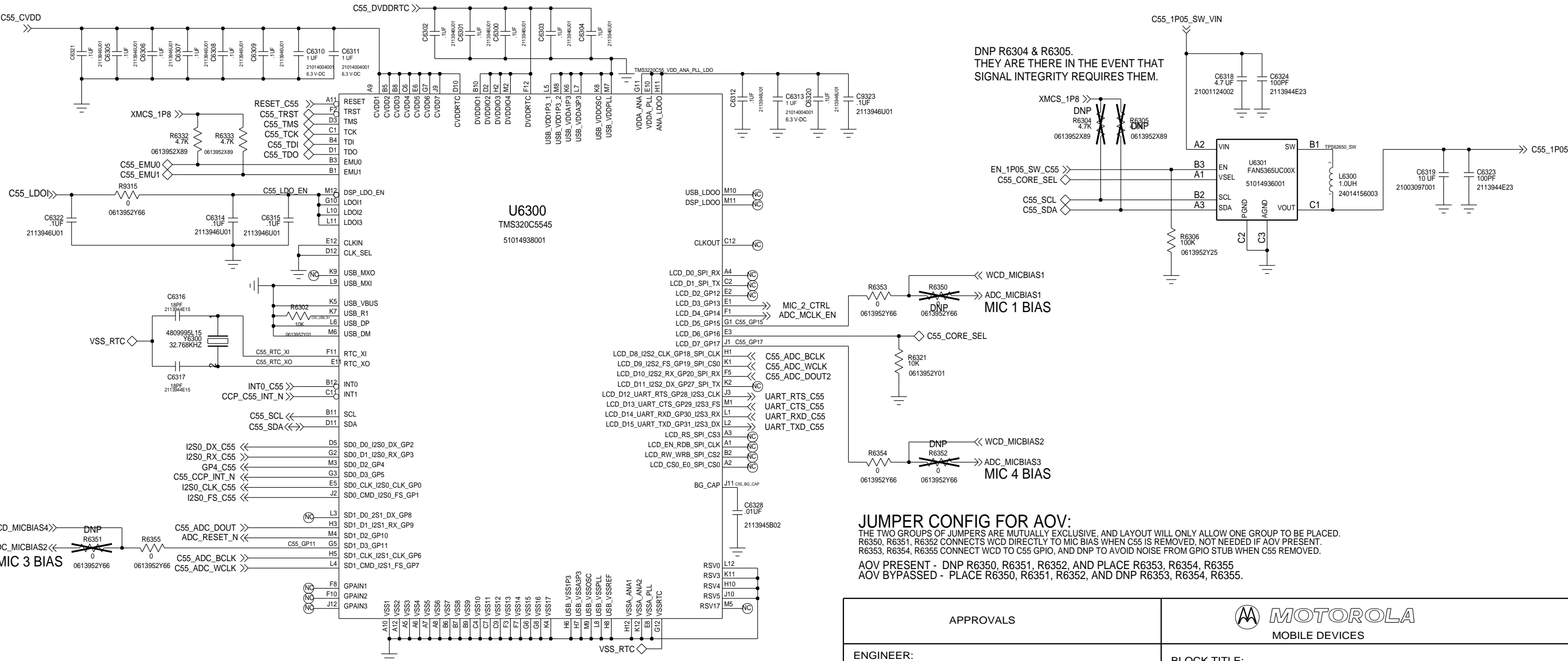
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PAGE 54 OF 80


XMCS: DSP

REF 6300-6399



ANALOG GND LAYOUT NEEDS TO FOLLOW TI RECOMMENDATION

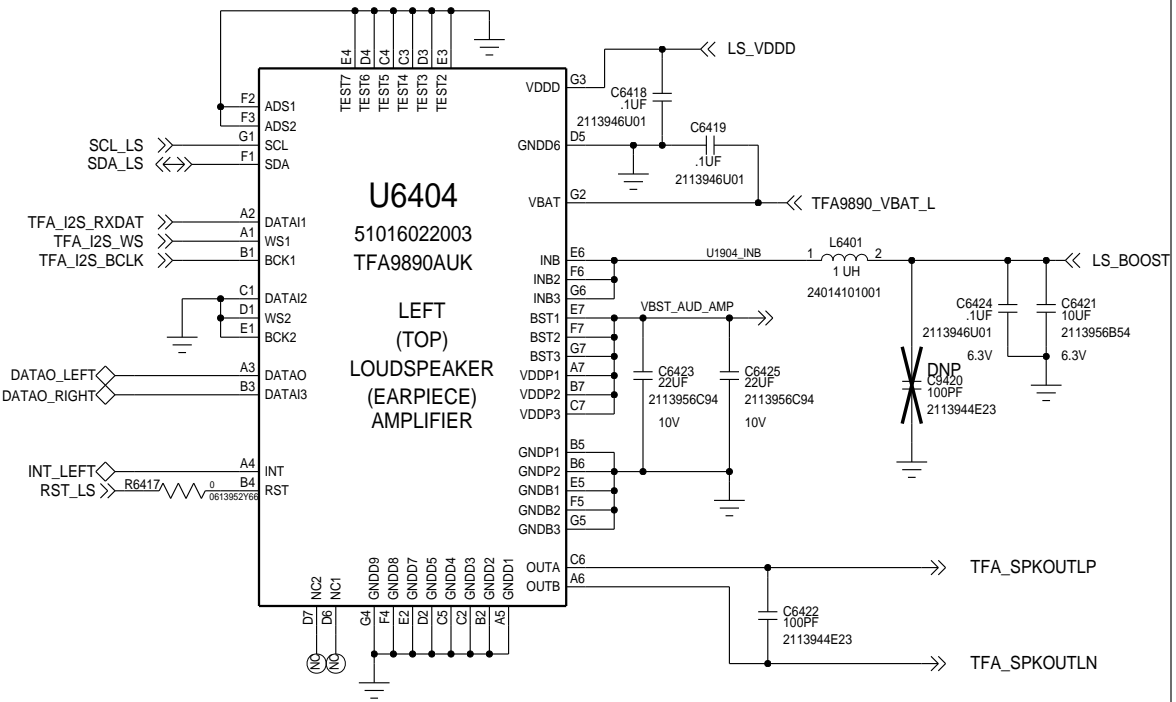
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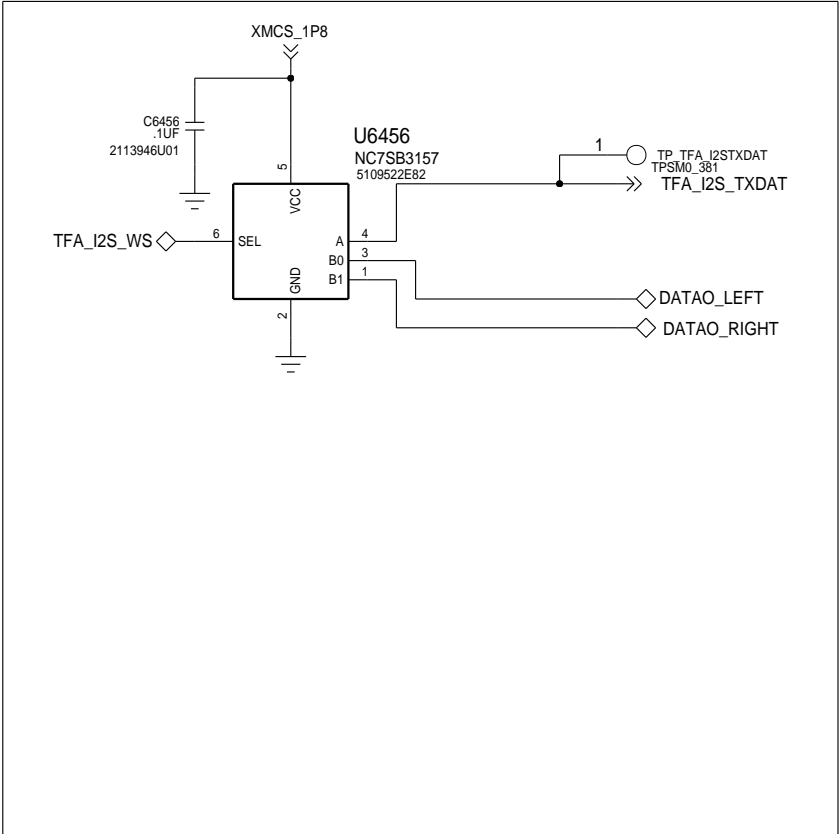
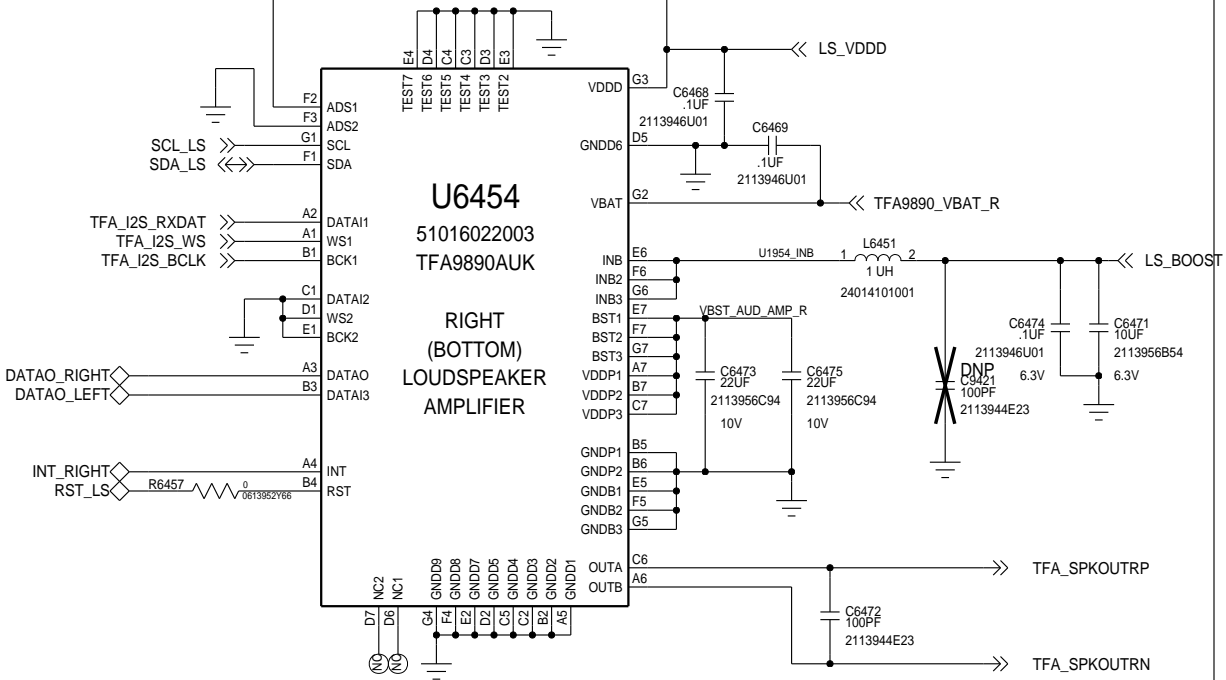
XMCS: LOUD SPEAKER

REF 6400-6499


I2C ADDR: FOR WRITE 0X68
I2C ADDR: FOR READ 0X69



I2C ADDR: FOR WRITE 0X6A
I2C ADDR: FOR READ 0X6B



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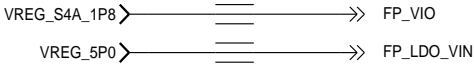
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87654321

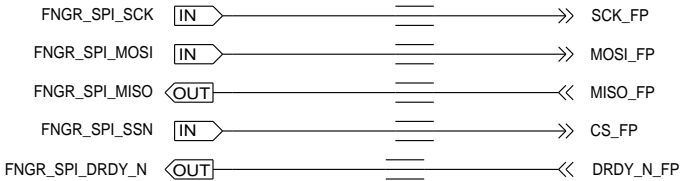
USR: PORTS

REF: 800-999

SUPPLIES



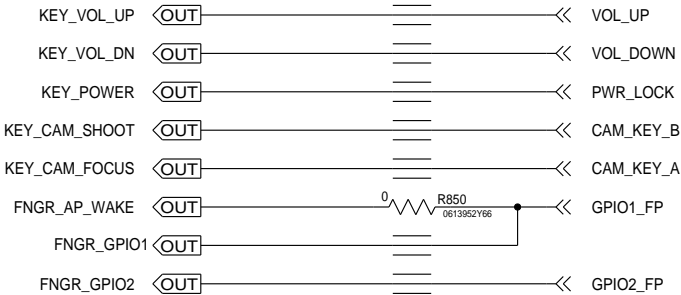
INTERFACES



GPIO - INPUT



GPIO - OUTPUT

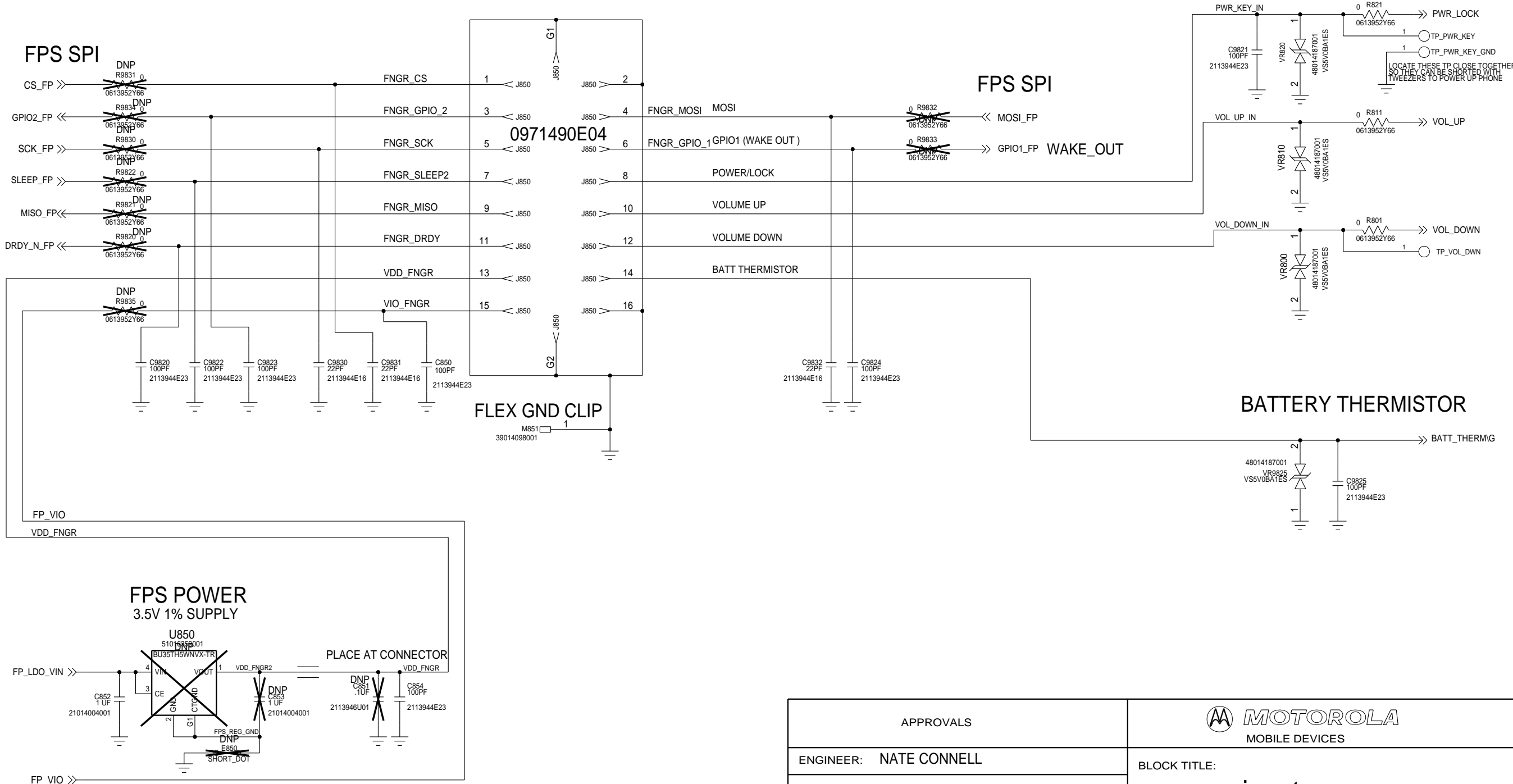



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LAST MODIFIED BY: NATE CONNELL Fri Sep 26 06:12:45 2014			
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		CAD FILE NAME: <div></div>	PAGE 57 OF 80

USR: INPUT

REF: 800-899

BRIDGE FLEX CONNECTOR

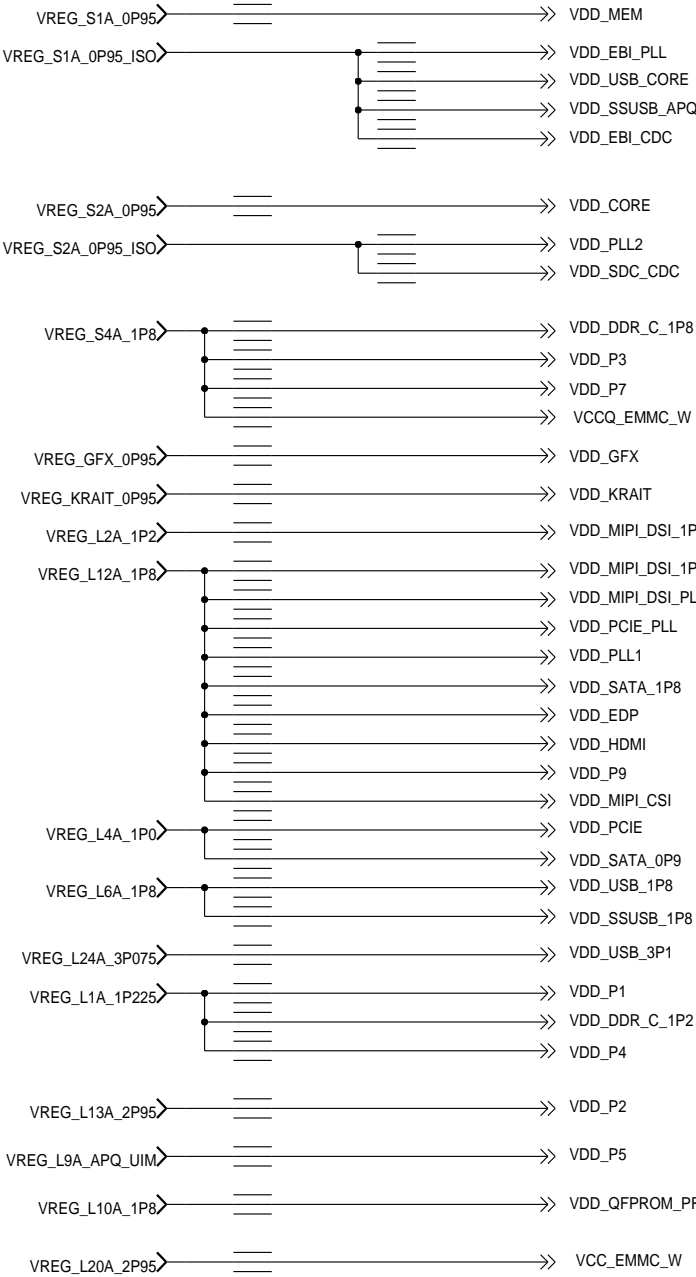



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LAST MODIFIED BY: NATE CONNELL Fri Sep 26 06:12:45 2014		PROJECT NAME 84016396001_a.cpm	
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		PAGE 58 OF 80	

APQ: POWER

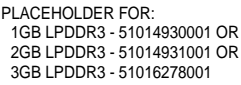
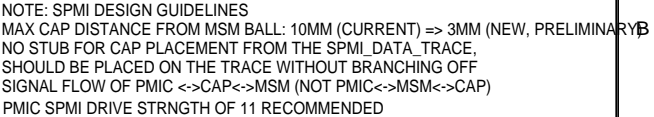
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SUPPLIES



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		B 84016396001_a.cpm	REV 01
CAD FILE NAME:			PAGE 59 OF 80

REF 1000-1099

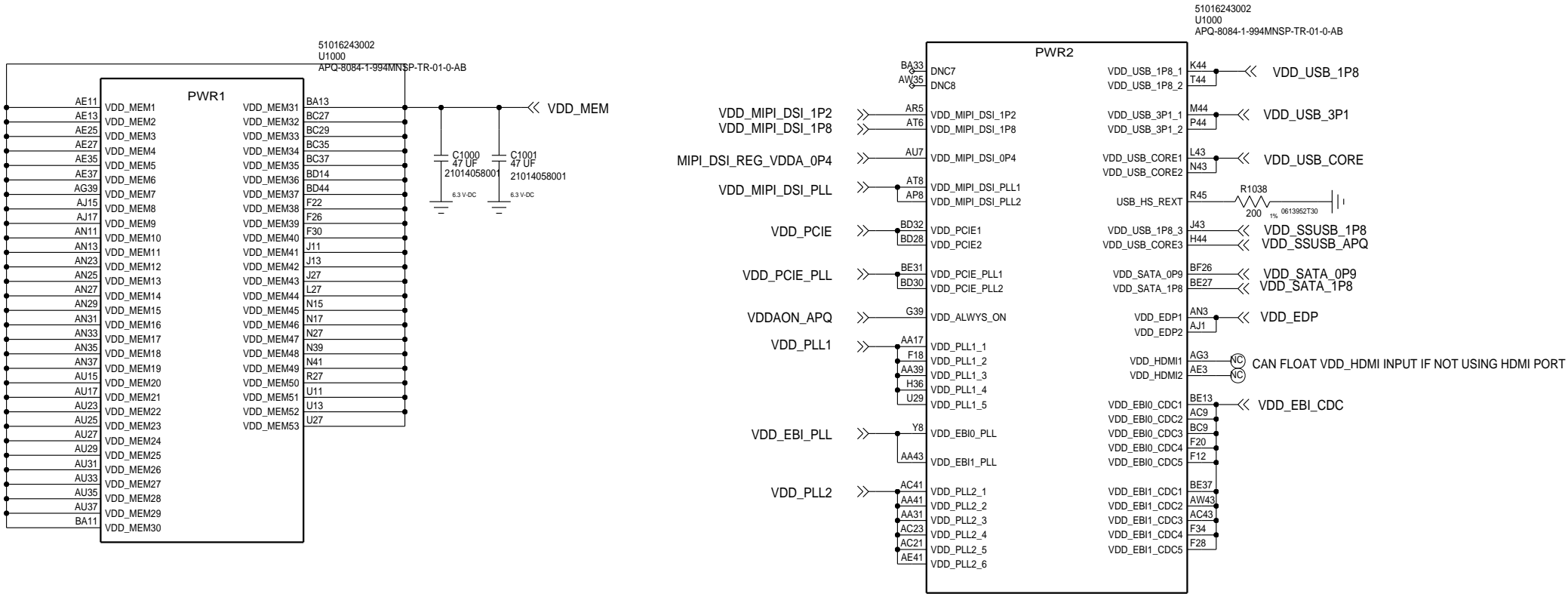
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REF 1030-1039




8	7	6	5	4	3	2	1
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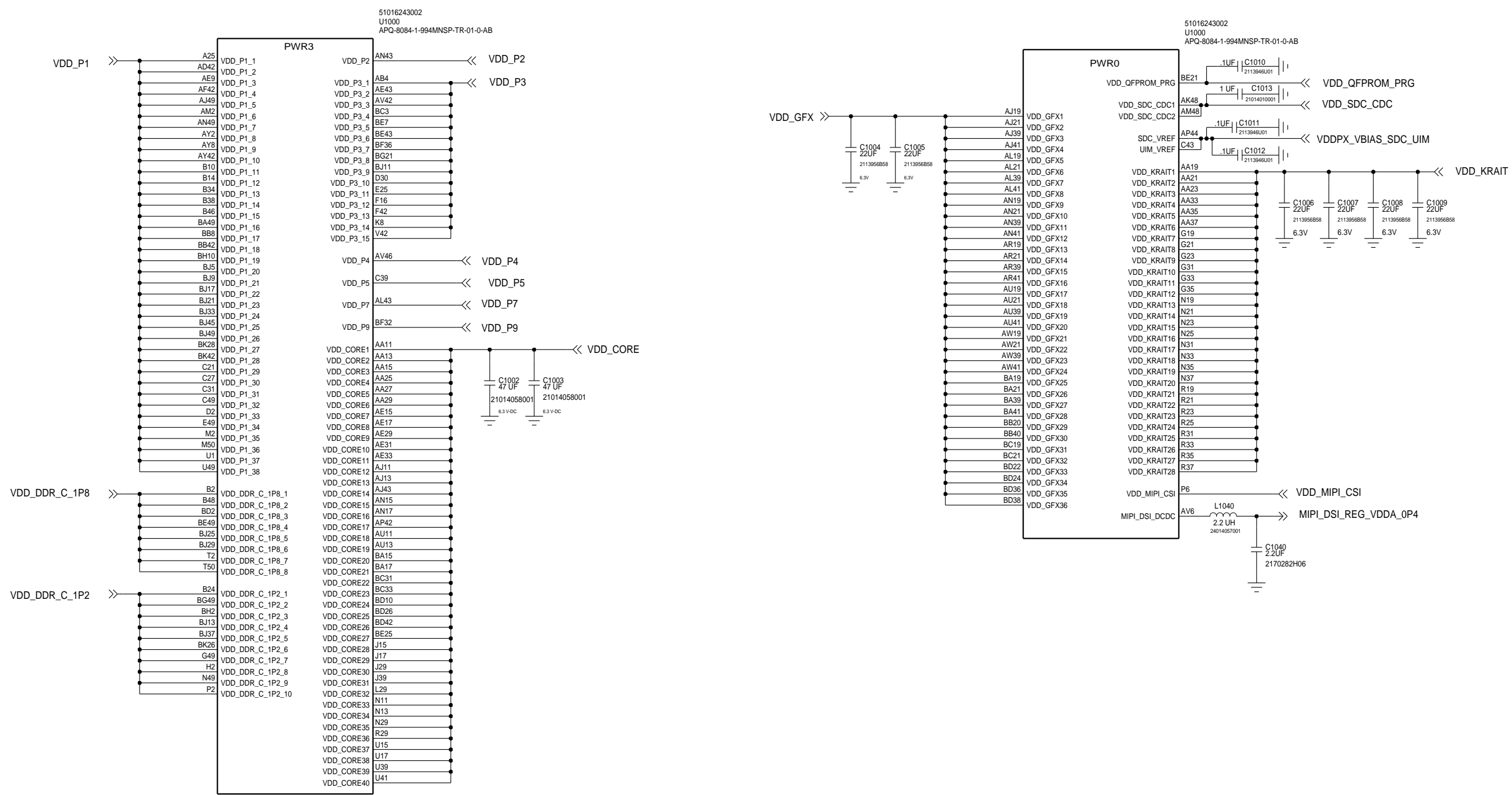
APQ: POWER 1




MOTOROLA MOBILITY CONFIDENTIAL RESTRICTED-MDB TRACKED TRACKEDANDBLOCKED

APPROVALS		 MOTOROLA MOBILE DEVICES	
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LAST MODIFIED BY: MOTOROLA Fri Sep 26 06:12:41 2014		ap_pmic	
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		CAD FILE NAME:	
		PAGE 62 OF 80	

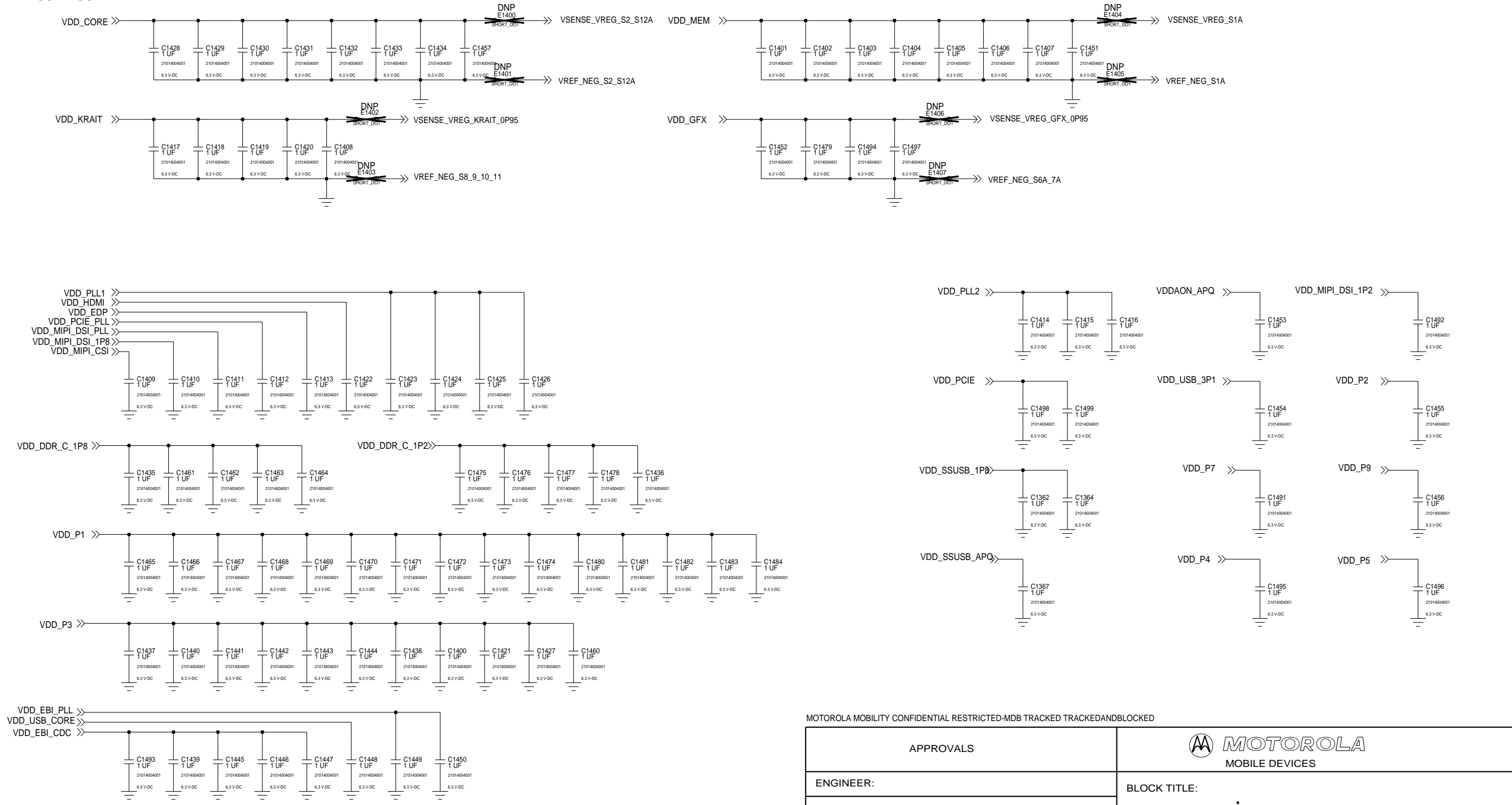
APQ: POWER 2




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CAD FILE NAME:		PAGE 63 OF 80	

APQ: BYPASS CAPS

REF 1400-1499



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APPROVALS		 MOTOROLA MOBILE DEVICES	
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		CAD FILE NAME:	
		PAGE 64 OF 80	

APQ: GND/NC

51016243002
U1000
APQ-8084-1-994MNSP-TR-01-0-AB

51016243002
U1000
APQ-8084-1-994MNSP-TR-01-0-AB

51016243002
U1000
APQ-8084-1-994MNSP-TR-01-0-AB

51016243002
U1000
APQ-8084-1-994MNSP-TR-01-0-AB

VSS1


VSS2

VSS3

NC

BASED ON 80-NA437-41 REV. G

MOTOROLA MOBILITY CONFIDENTIAL RESTRICTED-MDB TRACKED TRACKEDANDBLOCKED

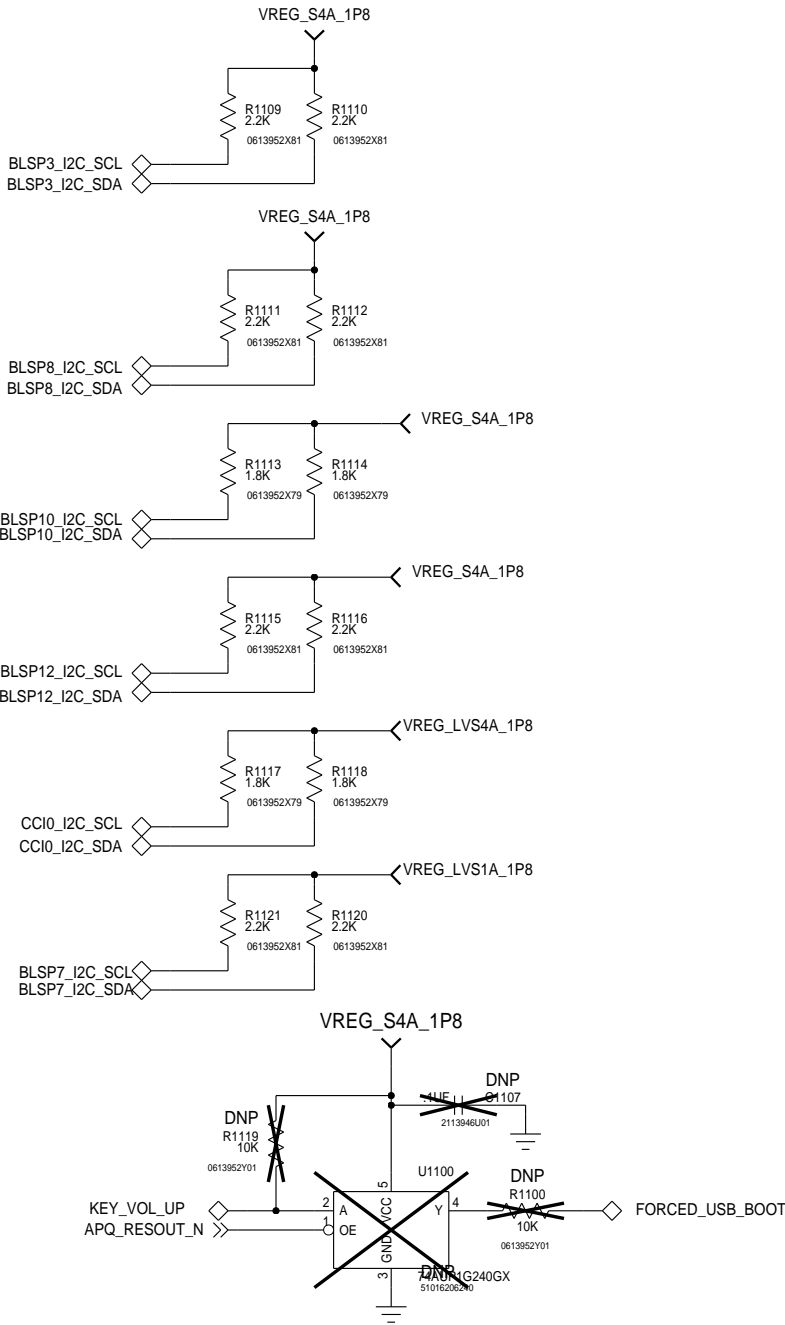
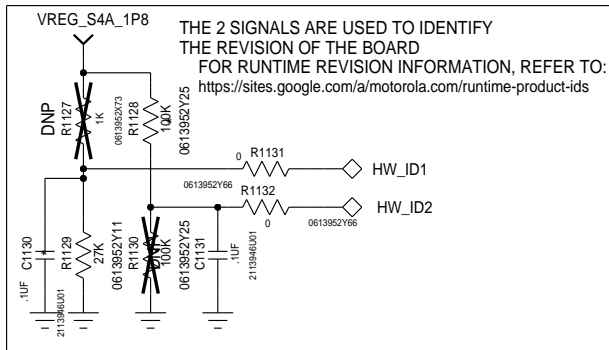
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		B 84016396001_a.cpm	REV 01
CAD FILE NAME:			PAGE 65 OF 80

APQ: GPIO MAP


PURPLE=MOT CUSTOM
CYAN=QCOM REFERENCE

PM GPIO

PM MPP



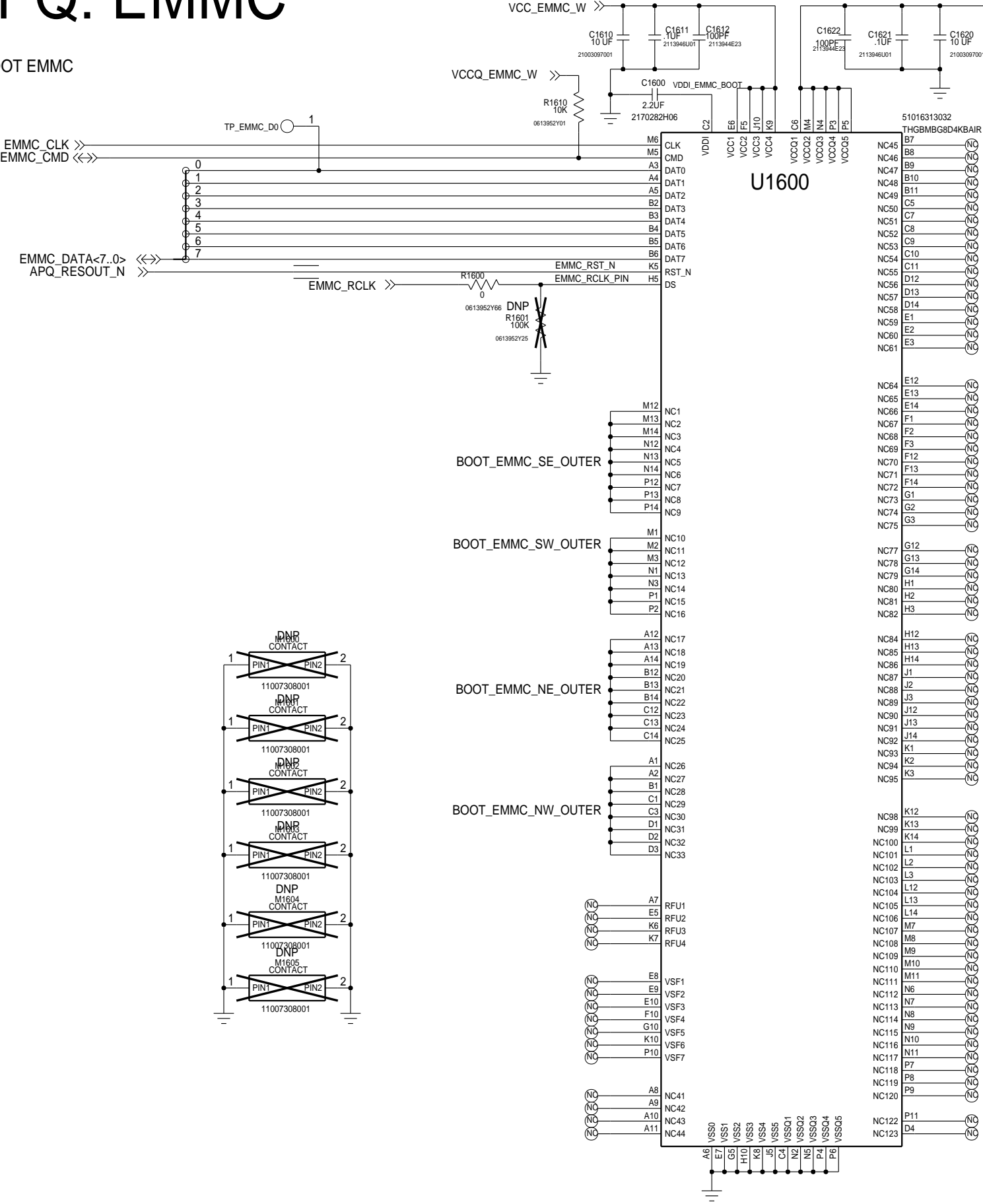
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CAD FILE NAME:		PAGE 66 OF 80	


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APQ: EMMC

BOOT EMMC



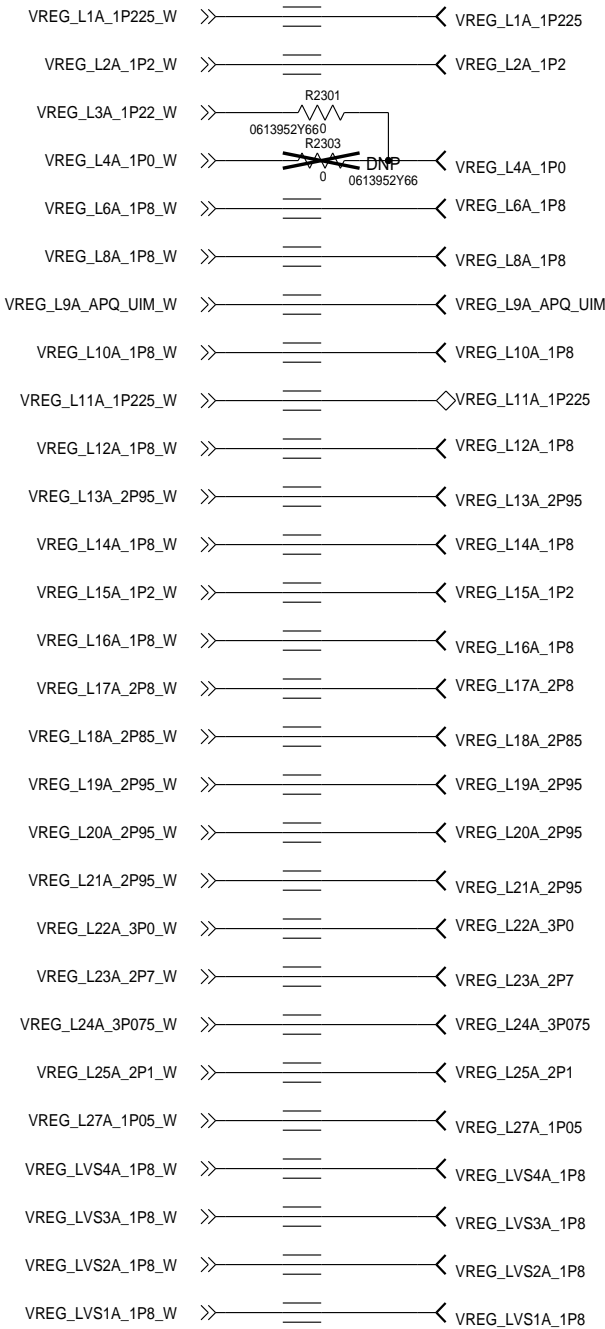
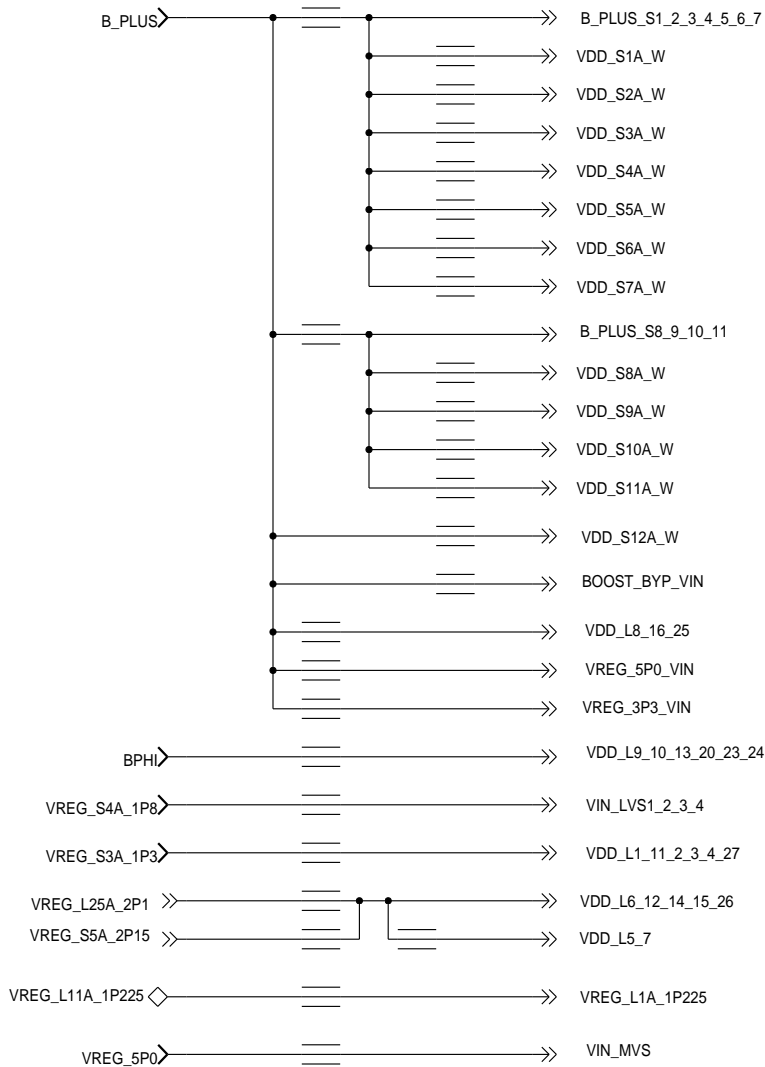
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		PAGE 67 OF 80	

PMA8084: POWER

REF: 2000-2999

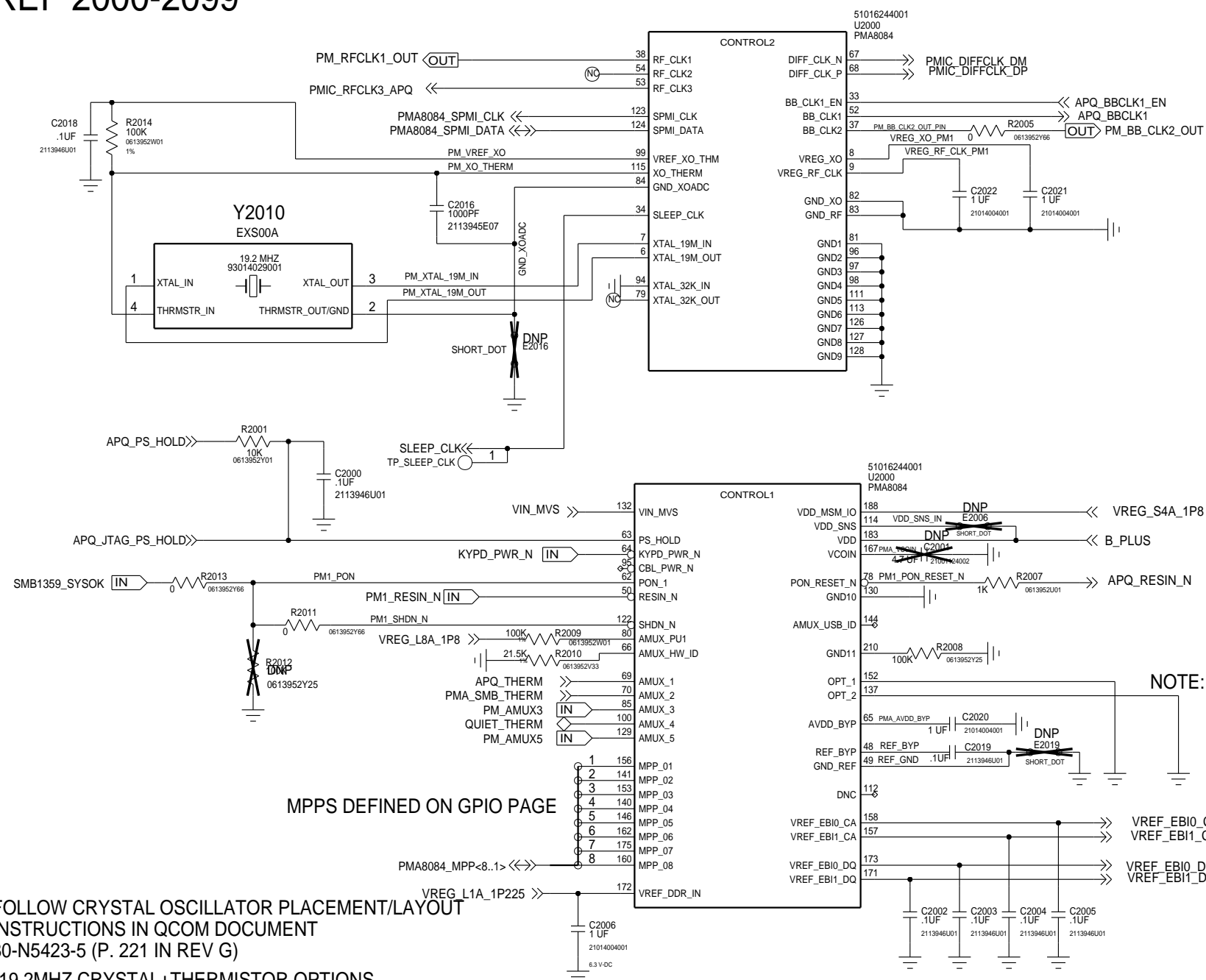
SUPPLIES



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		REV 01	
		CAD FILE NAME:	
		PAGE 68 OF 80	

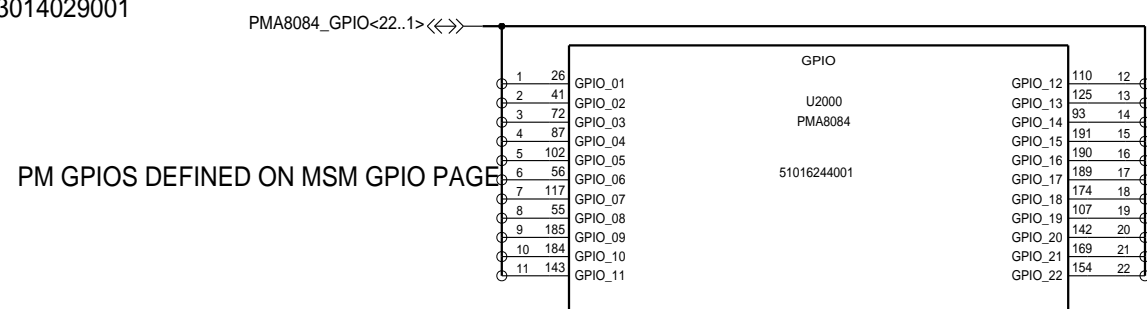
PMA8084: CLK/CTRL/GPIO

REF 2000-2099

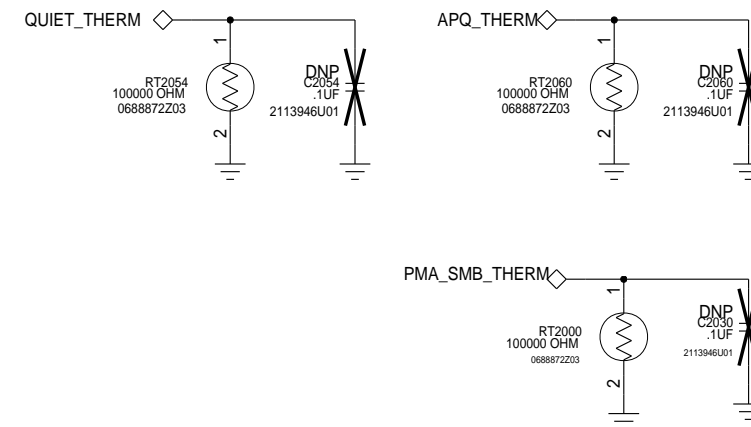


FOLLOW CRYSTAL OSCILLATOR PLACEMENT/LAYOUT
INSTRUCTIONS IN QCOM DOCUMENT
80-N5423-5 (P. 221 IN REV G)

*19.2MHZ CRYSTAL+THERMISTOR OPTIONS	
PACKAGE PART NUMBER	
2520	93014012001
2016	93014029001




BASED ON 80-NA437-41 REV. G

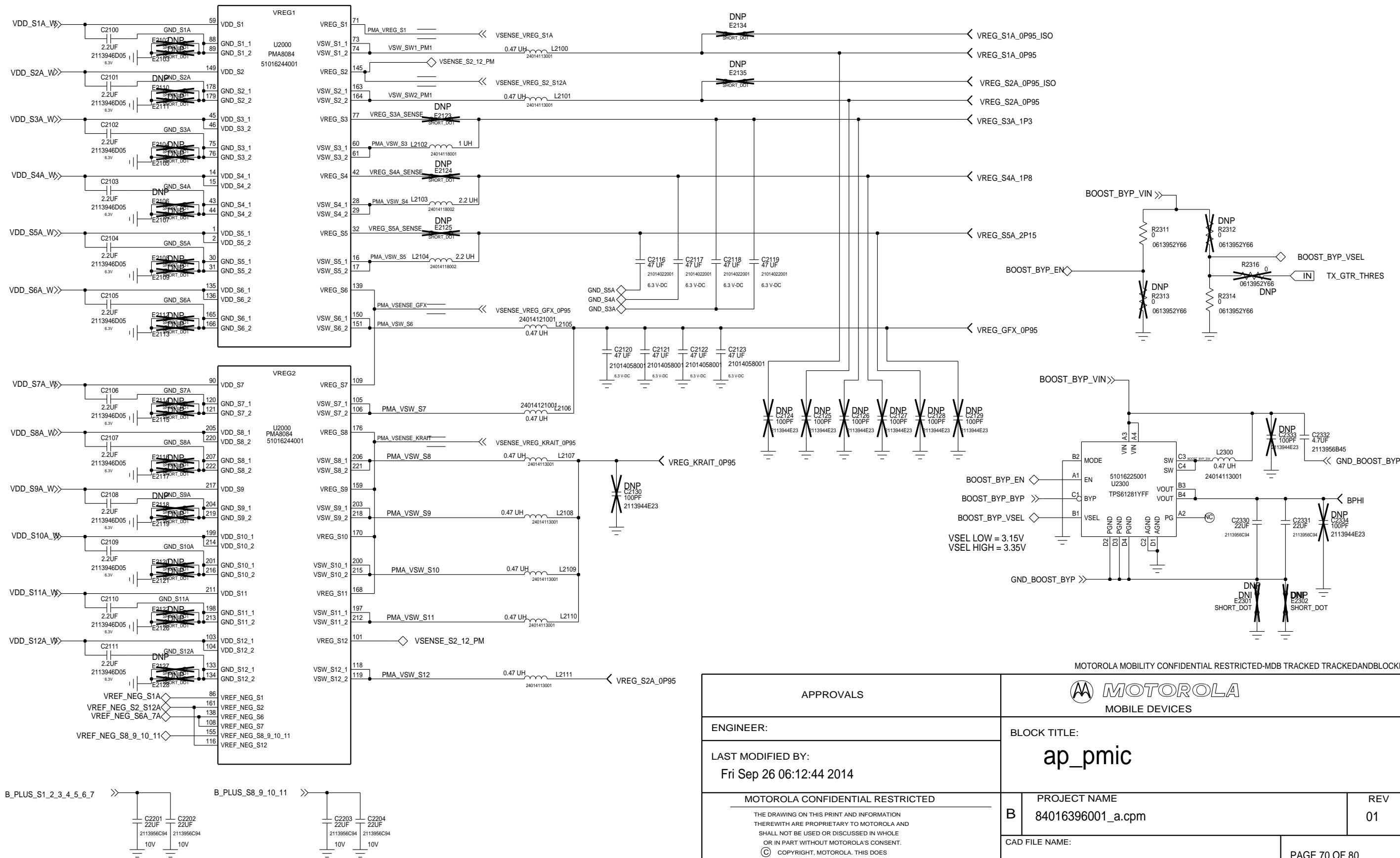


NOTE: OPT_1&OPT_2 = GND FOR APQ8084

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APPROVALS		 MOTOROLA MOBILE DEVICES	
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LAST MODIFIED BY: Fri Sep 26 06:12:43 2014			
MOTOROLA CONFIDENTIAL RESTRICTED		PROJECT NAME B 84016396001_a.cpm	REV 01
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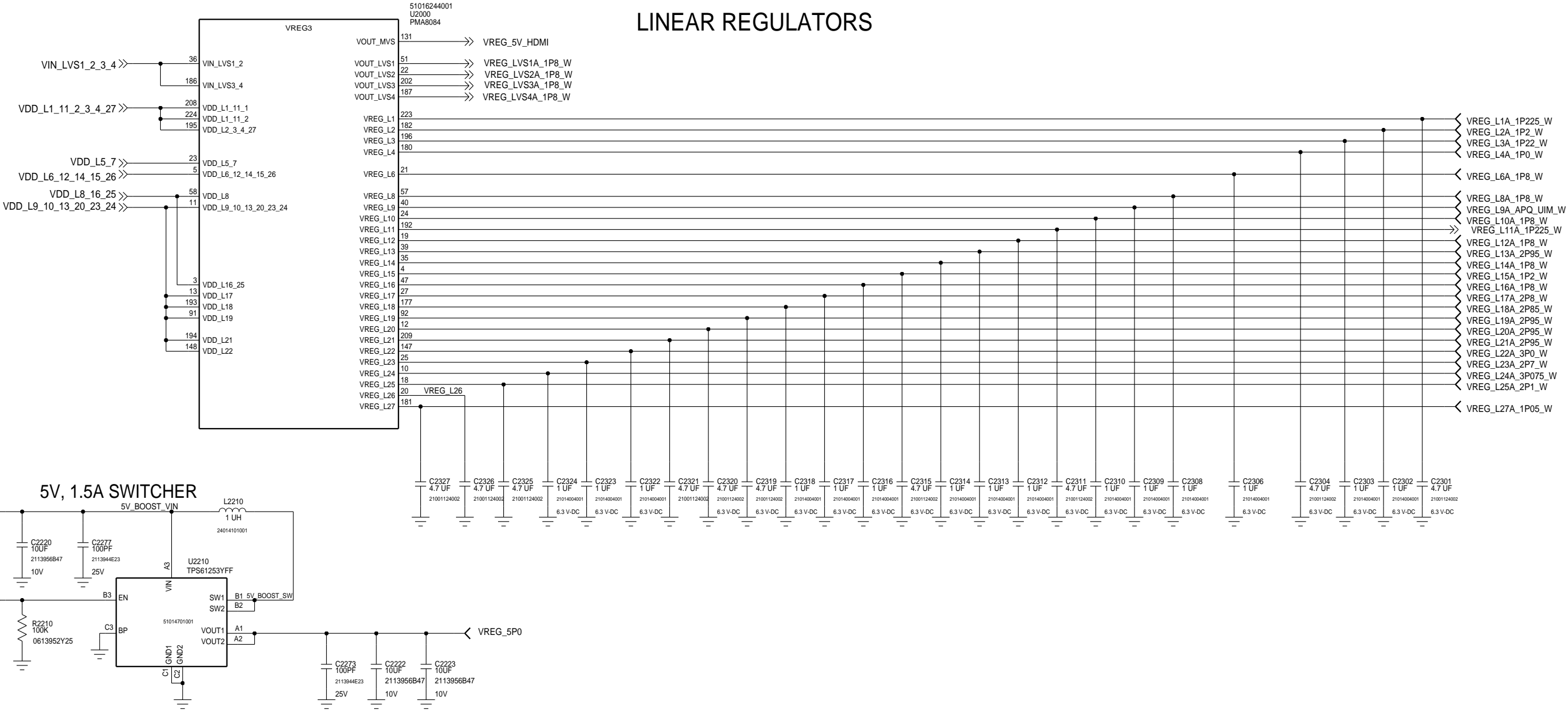
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REF 2200-2299




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LAST MODIFIED BY:		ap_pmic	
Fri Sep 26 06:12:44 2014			
MOTOROLA CONFIDENTIAL RESTRICTED		PROJECT NAME	REV
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PMA8084: VREGS

REF 2300-2349

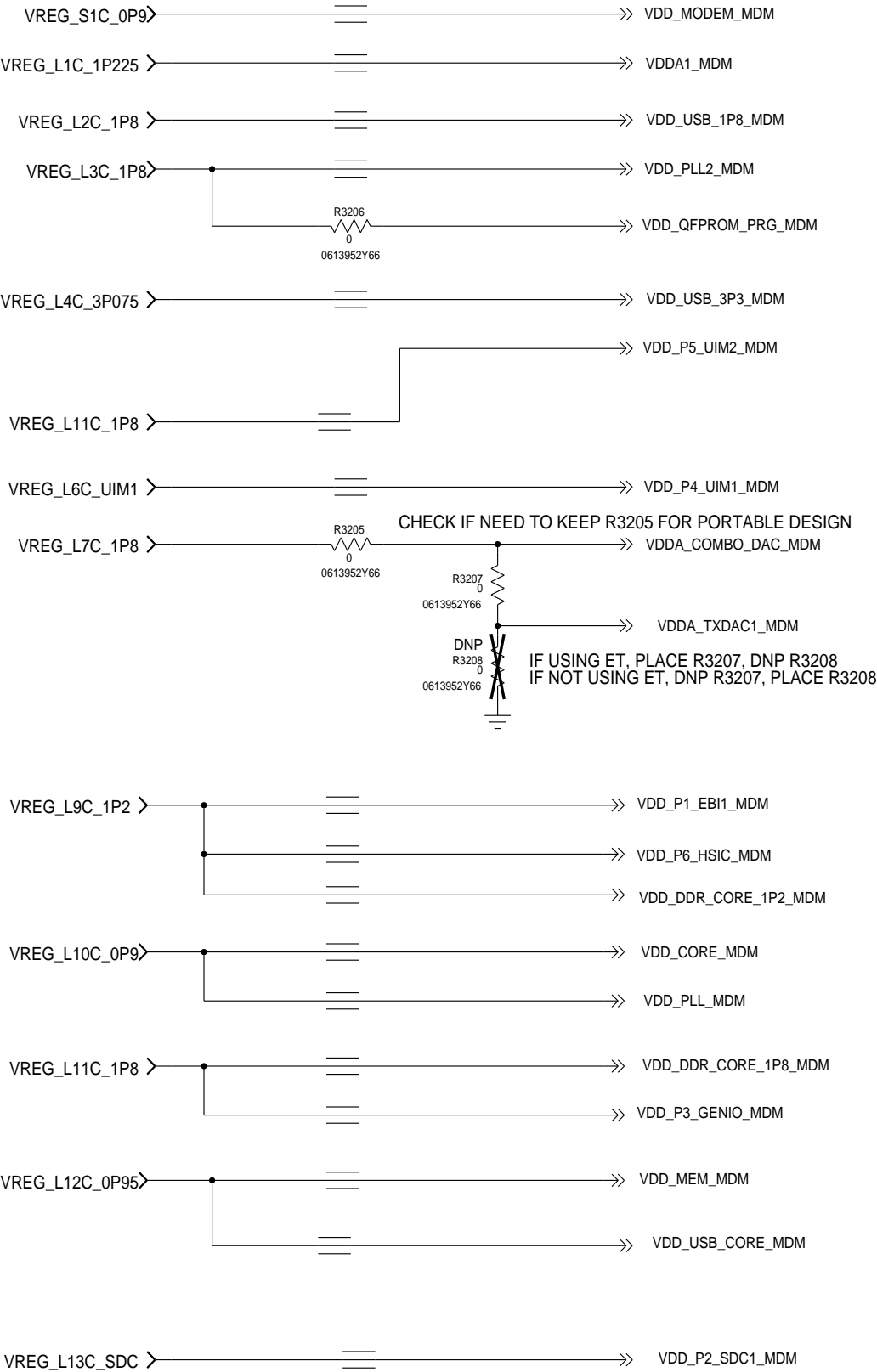


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APPROVALS		 MOTOROLA MOBILE DEVICES	
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LAST MODIFIED BY:		ap_pmic	
Fri Sep 26 06:12:44 2014			
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MDM9x25M POWER

REF 3200-3249



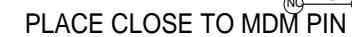
BASED ON 80-NJ851-41 REV.A

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		B 84016396001_a.cpm	REV 01
		CAD FILE NAME:	
		PAGE 72 OF 80	

REF 3300-3399




IF USING ET, PLACE R3308 & R3305, DNP R3209
IF NOT USING ET, DNP R3308 & R3305, PLACE R3309 AND REPLACE C3304 W/ 00HM

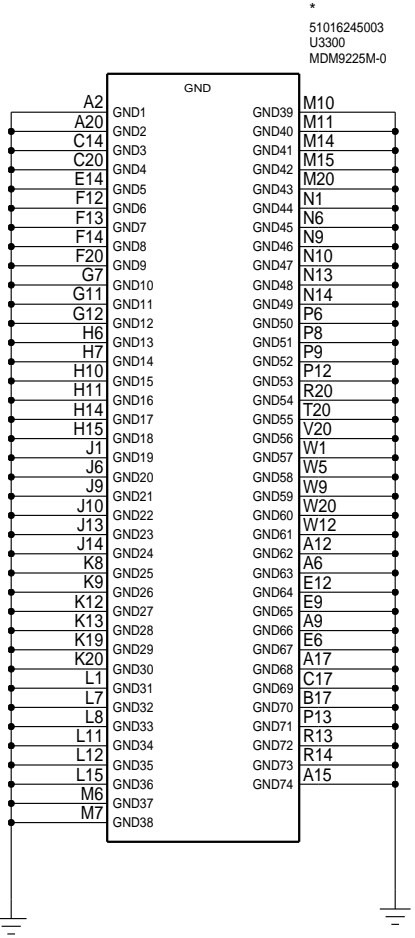
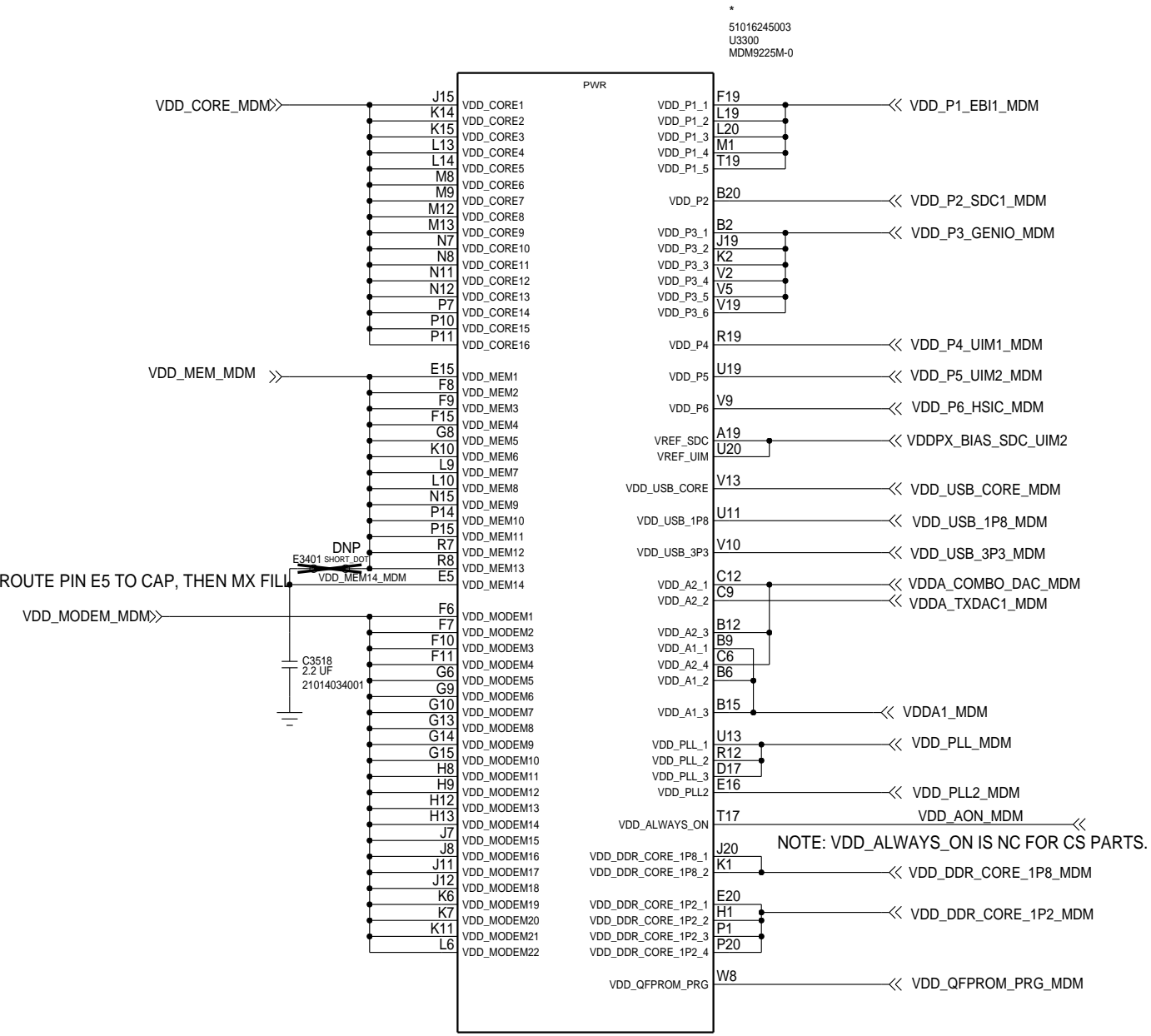


DNI JTAG CONNECTOR AND SUPPORT COMPONENTS FOR PRODUCTION


BASED ON 80-NJ851-41 REV.A

APPROVALS		 MOTOROLA MOBILE DEVICES	
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LAST MODIFIED BY: THIREN RAYAMAJHI Fri Sep 26 06:12:38 2014			
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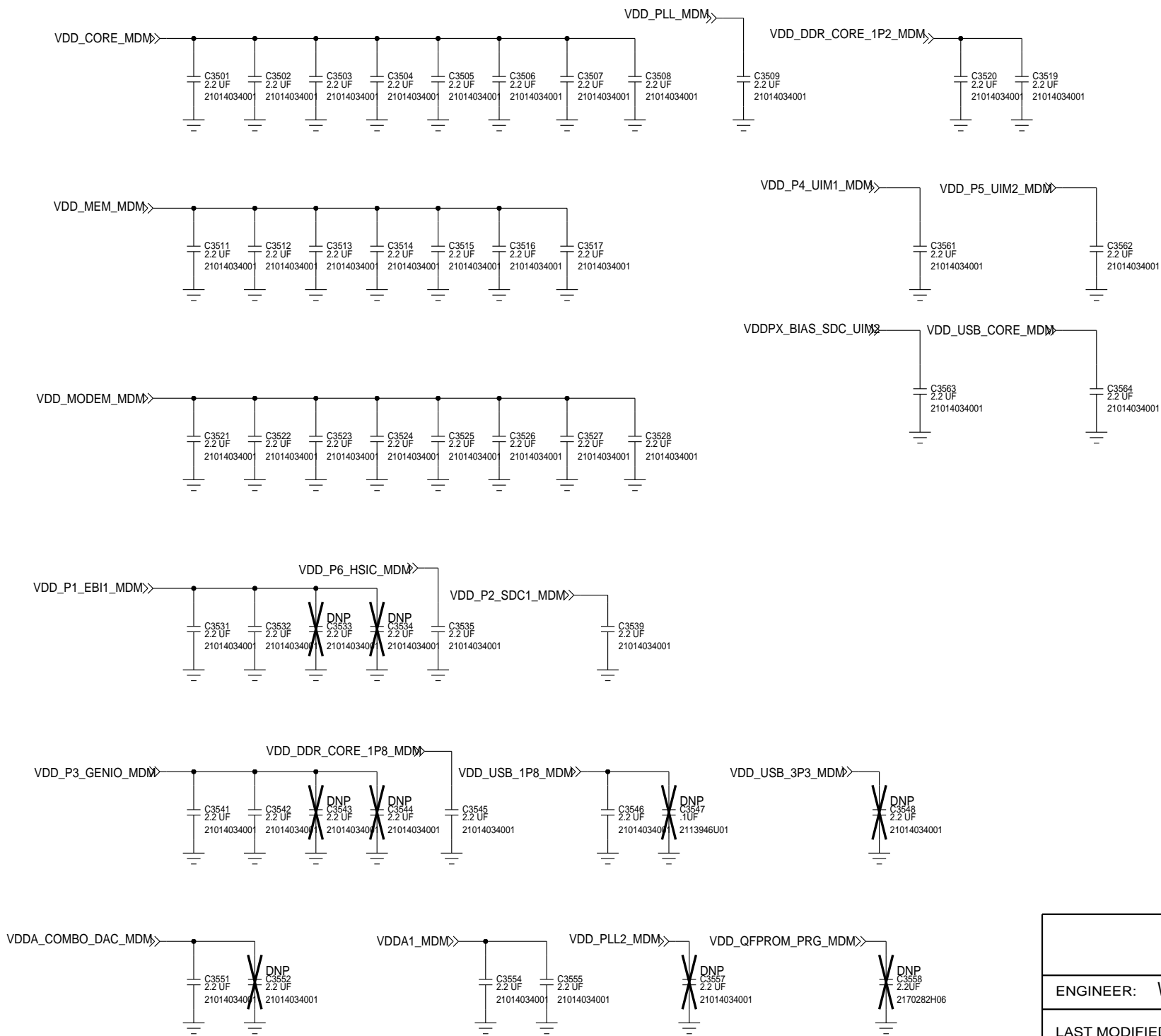
MDM9x25M POWER1/2
REF 3400-3499




BASED ON 80-NJ851-41 REV.A

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		PAGE 74 OF 80	

MDM9x25M POWER 2/2
REF 3500-3599



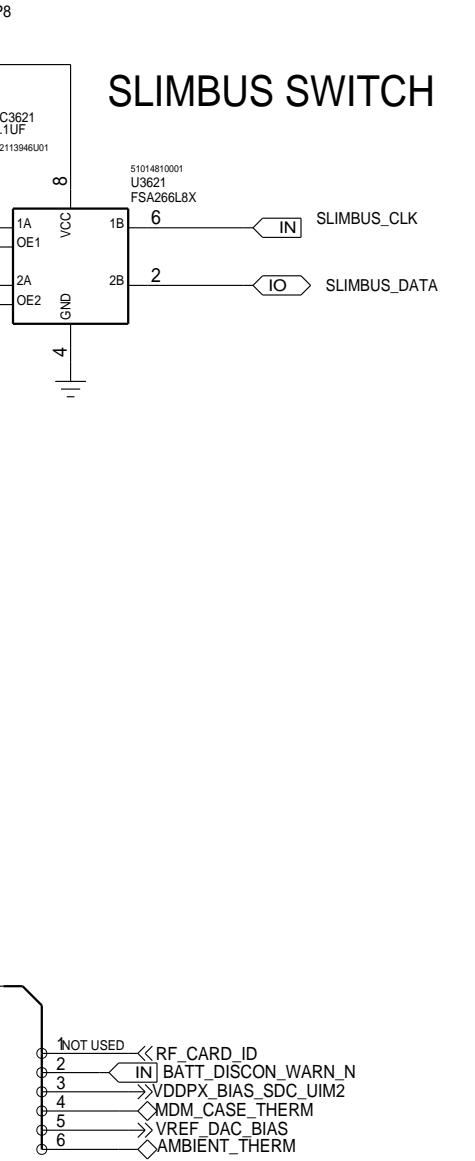
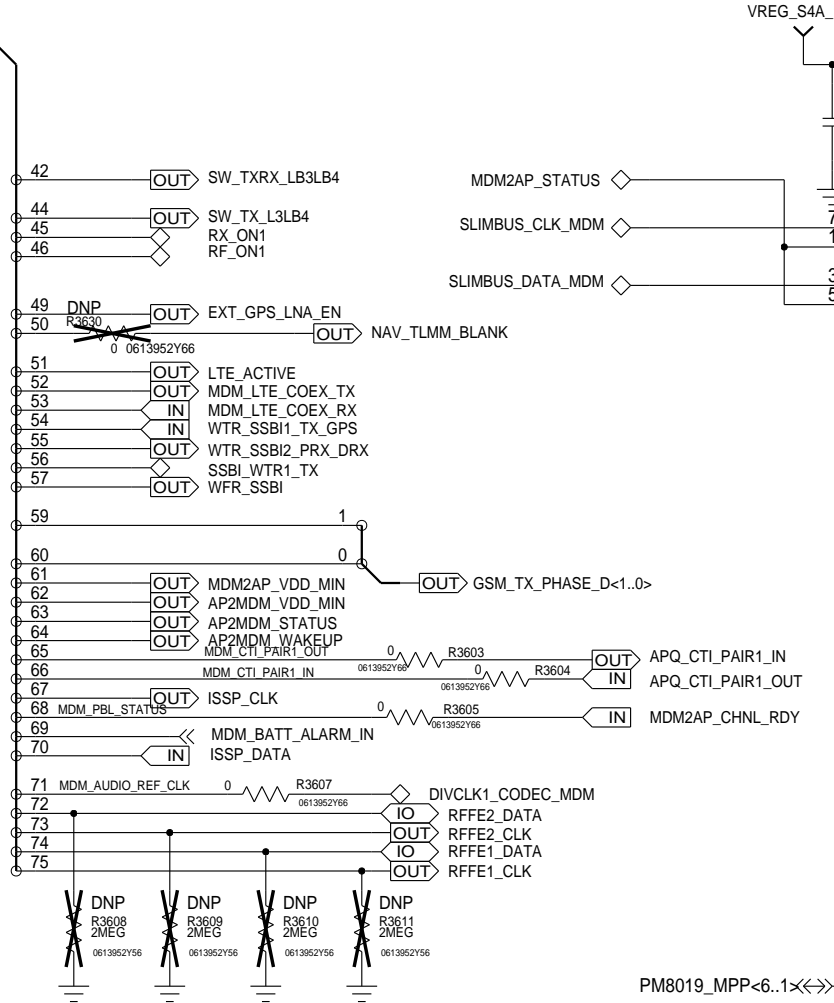
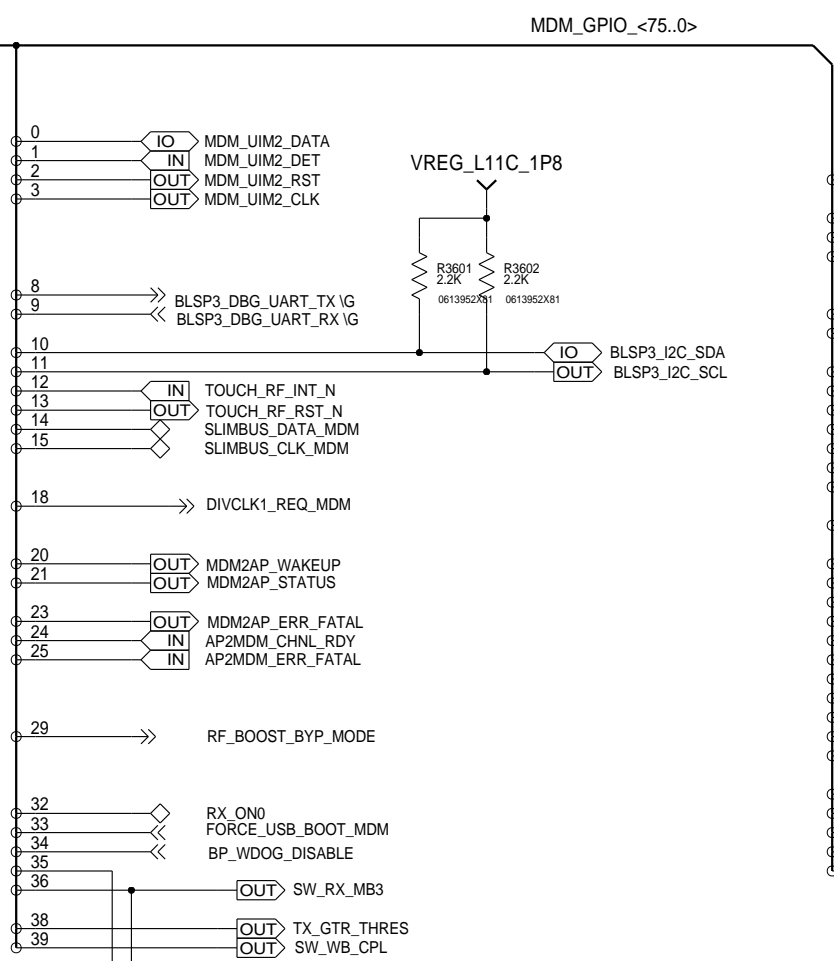
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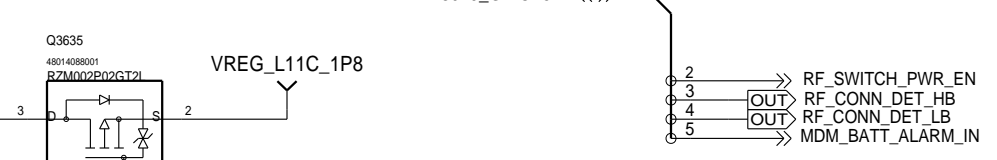
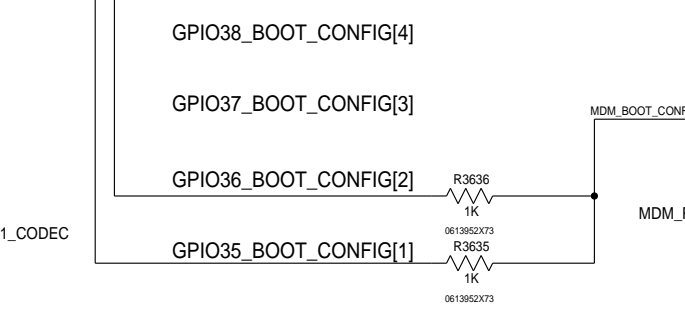
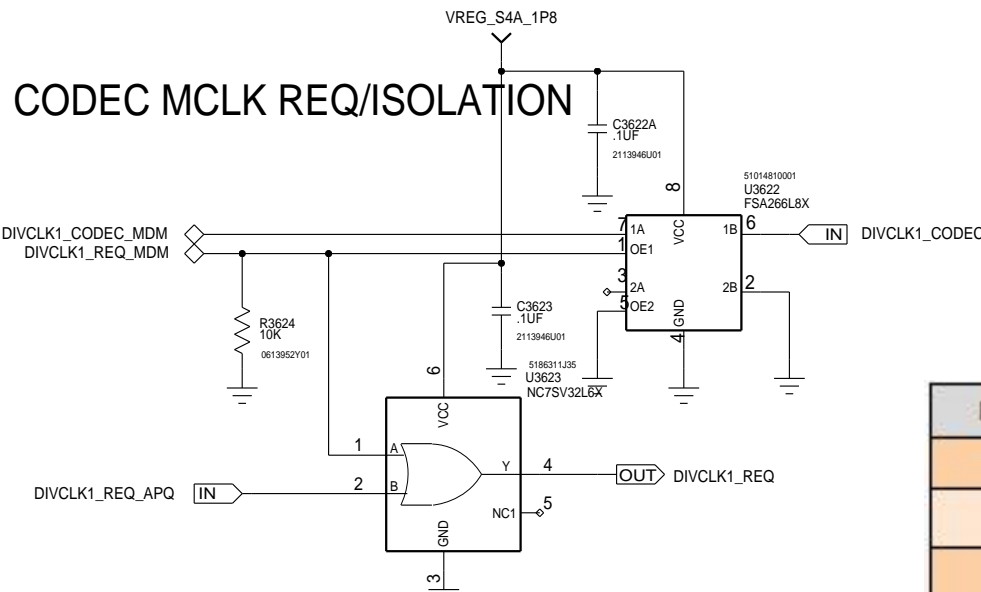
MDM9x25M GPIO MAP

REF 3600-3699

GPIO MDM9225M-0			
0	R18	GPIO_0	GPIO_38
1	U18	GPIO_1	GPIO_39
2	T18	GPIO_2	GPIO_40
3	P18	GPIO_3	GPIO_41
4	U15	GPIO_4	GPIO_42
5	U14	GPIO_5	GPIO_43
6	V14	GPIO_6	GPIO_44
7	U16	GPIO_7	GPIO_45
8	U3	GPIO_8	GPIO_46
9	U4	GPIO_9	GPIO_47
10	W2	GPIO_10	GPIO_48
11	V3	GPIO_11	GPIO_49
12	V7	GPIO_12	GPIO_50
13	V6	GPIO_13	GPIO_51
14	W7	GPIO_14	GPIO_52
15	U8	GPIO_15	GPIO_53
16	M18	GPIO_16	GPIO_54
17	M16	GPIO_17	GPIO_55
18	N16	GPIO_18	GPIO_56
19	L16	GPIO_19	GPIO_57
20	D18	GPIO_20	GPIO_58
21	C18	GPIO_21	GPIO_59
22	E19	GPIO_22	GPIO_60
23	E18	GPIO_23	GPIO_61
24	P16	GPIO_24	GPIO_62
25	L18	GPIO_25	GPIO_63
26	L5	GPIO_26	GPIO_64
27	M3	GPIO_27	GPIO_65
28	K3	GPIO_28	GPIO_66
29	L3	GPIO_29	GPIO_67
30	M2	GPIO_30	GPIO_68
31	K5	GPIO_31	GPIO_69
32	B1	GPIO_32	GPIO_70
33	C2	GPIO_33	GPIO_71
34	J5	GPIO_34	GPIO_72
35	L2	GPIO_35	GPIO_73
36	J3	GPIO_36	GPIO_74
37	J2	GPIO_37	GPIO_75



CODEC MCLK REQ/ISOLATION



BOOT_CONFIG[4:1]	Boot device
0b00000	NAND followed by USB
0b00001	NAND
0b00010	USB
0b00011	HSIC

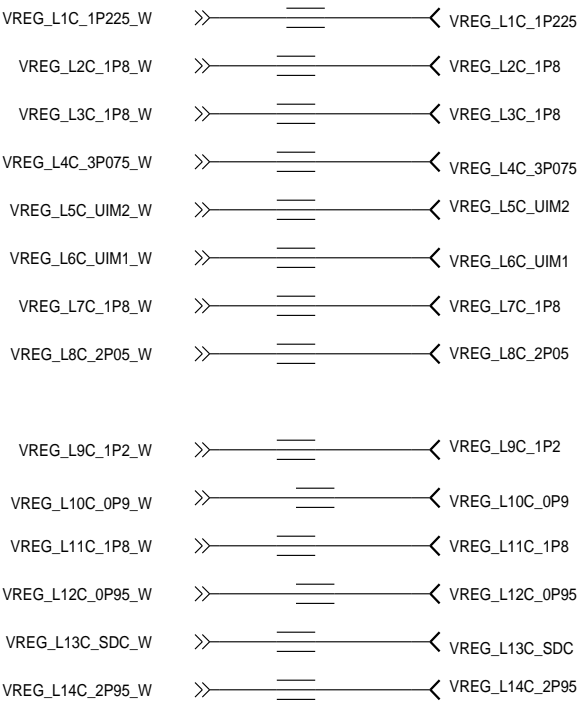
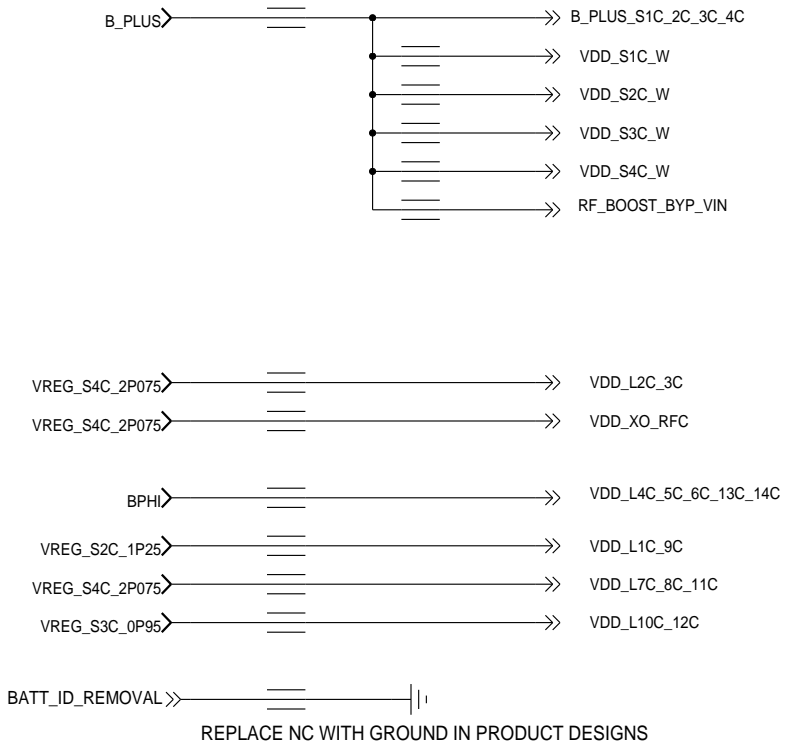
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CAD FILE NAME:		PAGE 76 OF 80	

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PM8019 POWER

REF 3900-3949

SUPPLIES

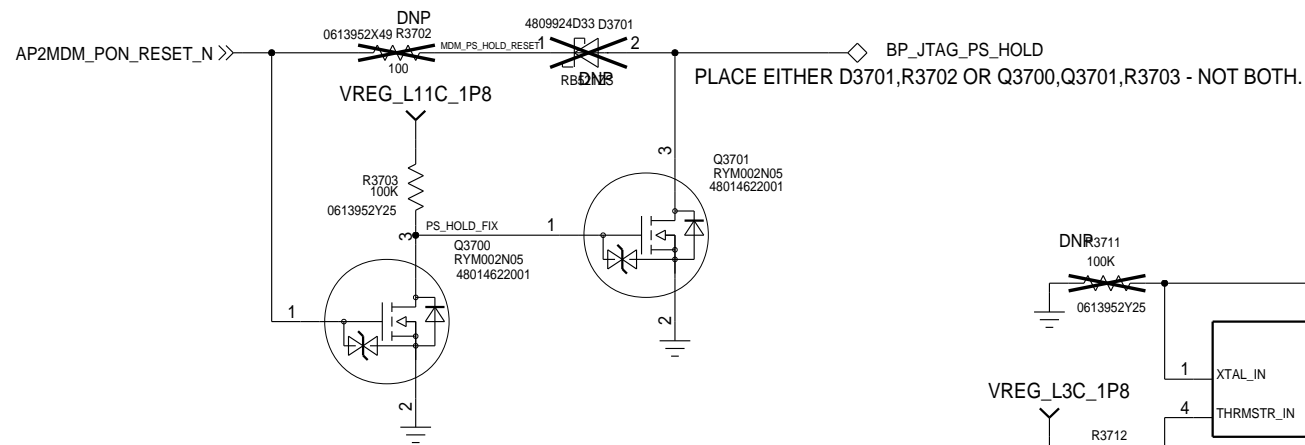


BASED ON 80-NJ851-41 REV.A

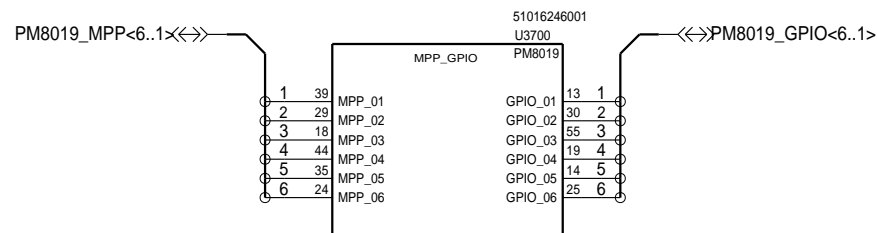
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REF 3700-3799

VREG_L11C_1P8

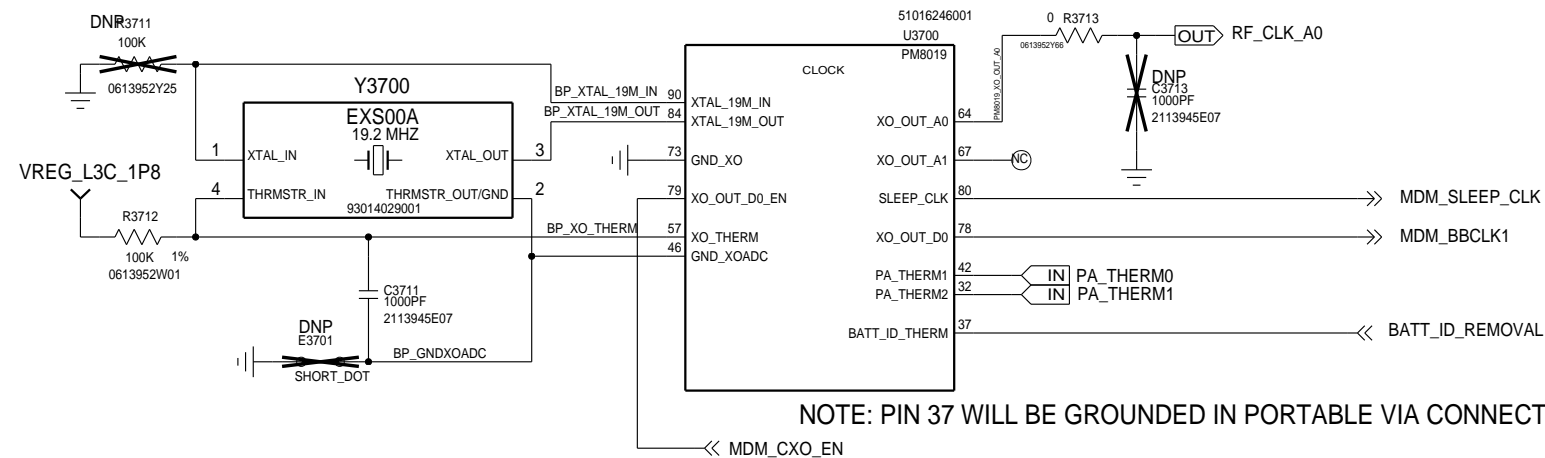


VREG_L3C_1P8





The schematic diagram shows the PM8019 PMIC (U3700) connected to various control and status signals. The PMIC is a 66-pin component with pins 66 through 76 visible. The connections are as follows:

- Control Signals (Pins 70-76):**
 - Pin 70: CBL_PWR_N (connected to MDM_PS_HOLD and BP_JTAG_PS_HOLD via a 2.2K resistor network).
 - Pin 71: PON_TRIG (connected to 2MDM_PON_RESET_N).
 - Pin 75: PON_RST_N (connected to _PMIC_PON_RESET_N).
 - Pin 65: PS_HOLD (connected to MDM_CBL_PWR_N).
 - Pin 20: RESIN_N (connected to BP_PMIC_RESIN_N).
 - Pin 81: SPMI_CLK (connected to BP_PMIC_SPMI_CLK).
 - Pin 76: SPMI_DATA (connected to BP_PMIC_SPMI_DATA).
- Status Signals (Pins 66-68):**
 - Pin 66: OPT (connected to B_PLUS via a 3706 DNP resistor).
 - Pin 89: GND1 (connected to GND).
 - Pin 56: GND2 (connected to GND).
 - Pin 45: GND3 (connected to GND).
- Other Components:**
 - A 0613952X81 resistor is connected between MDM_PS_HOLD and BP_JTAG_PS_HOLD.
 - A 33PF capacitor (C3701) is connected between BP_PMIC_SPMI_CLK and GND.
 - A 1MEG resistor (R3704) is connected between BP_PMIC_SPMI_DATA and GND.
 - A 2113944E17 resistor is connected between BP_PMIC_SPMI_DATA and GND.

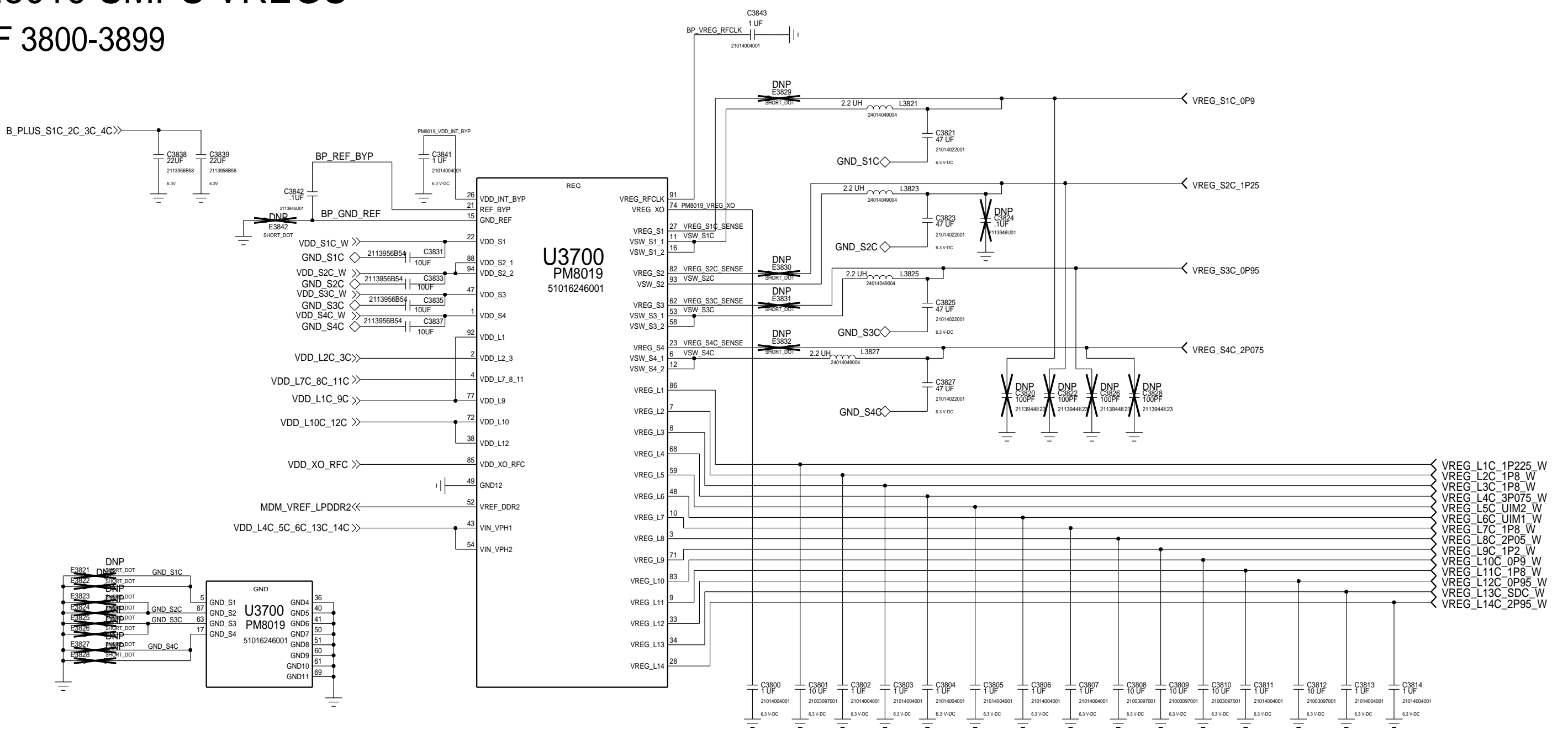


NOTE: PIN 37 WILL BE GROUNDED IN PORTABLE VIA CONNECTION ON PM8019 POWER SHEET


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ENGINEER: W00281	BLOCK TITLE: block_mdm9x25m_pm8019		
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PM8019 SMPS VREGS

REF 3800-3899

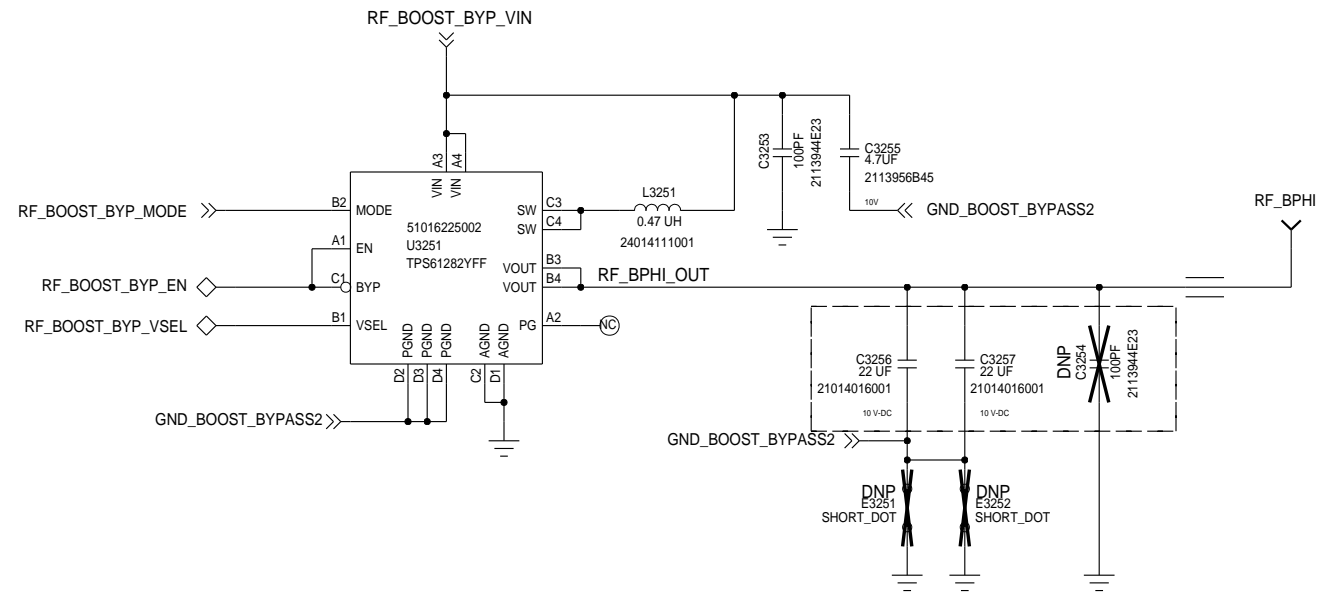
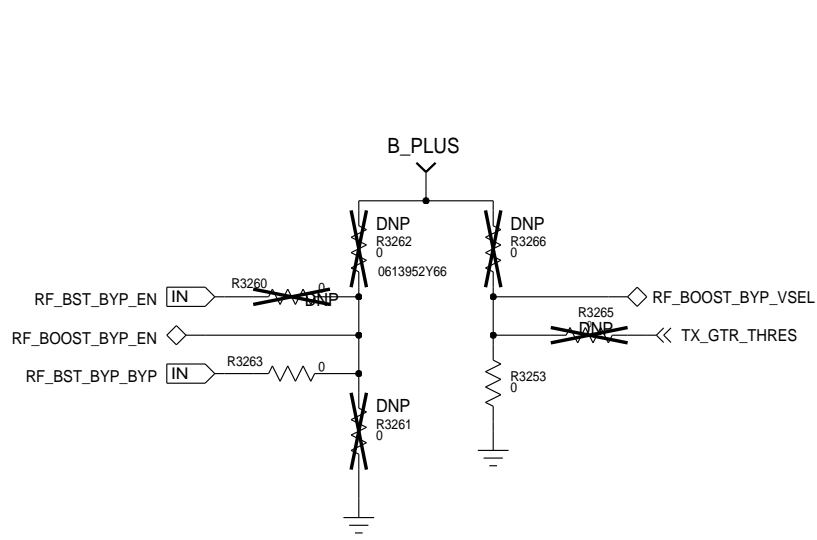



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PM8019 BST/BYP

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