

Signal Name	From	To	Signal Description
EARN	U3	Voice Receiver	Negative Audio Voice to Receiver
EARP	U3	Voice Receiver	Positive Audio Voice to Receiver
DAC	U3	R1	Used to allow a path to earth for VBAT through the Vibrator motor
MICBIAS	R28	R8	Microphone bias supply(2V)
MICIN	MIC1	U3	Negative analogue uplink audio from on board Microphone
MICIP	MIC1	U3	Positive analogue uplink audio from on board Microphone
BATTEMP	U3	R11	NTR connect of the Battery
VCCS	R77	U3	Charging current sense
VBATS	R75	U3	Charging current sense
VCHG	J13	U5	Adaptor DC input
VCHG1	U4	U4/R78/R49	Power Supply of Charge system
VCHG2	R78	U3	Power Supply of U3
PCHG	R17	U3	Battery Pre-charge Path
LEDC	D1	U3	Charge indicator LED control signal
ICTL	U3	U4	Charge current control signal
VRSIM	U3	CONN1	SIM card power supply
VRIO_2.8V	U3	U10/LCM/U7/U11/U12/U15	Power supply of U10, LCM, U7, U11, U12, U15
VRDBB_1.5V	U3	U7	Power supply of U7
UPR	U3	U3	Power Supply of U3
VADCID	U3	TP29	Cable detection
TEMP_SEN	U15	U3	Reserve
BS_TPO	U3	TP6A	Reserve
MODE_DETECT	U3	R13/R20	EarJack function detection
TDO	U7	U3/TP14	Data output of JTAG
TMS	TP16	U3/U7	JTAG mode selection
TCK	TP15	U3/U7	CLK of JTAG
SWITCHONOFF	U3	U7/U105	ABB Regulators ready
RESPWRONZ	U3	U7	G2 power on reset
DLPWR	TP11	R14	Remote Power ON
CLK32K_OUT	U7	U3	32Khz clock
CLK13M_OUT	U7	U3/U10	13Mhz clock
VBACKUP	C38	U3	RTC battery Voltage Back up
VRRTC_1.5V	U3	U7	Regulator RTC output
VRSIM	U3	CONN1	For SIM card power supply
VRRAM_2.8V	U3	U104	For SRAM power supply
VRMEM_2.8V	U3	U104	For Flash power supply
VRABB_2.8V	U3	U3	For U3 power supply

VRMEM_2.8V	U3	U104	For Flash power supply
PWON	S1	R15	ON button
TXIP/TXIN	U3	R73	In-phase baseband codec uplink signals
TXQP/TXQN	U3	R76	Quadrature baseband codec uplink signals
RXIP/RXIN	U15	U3	In-phase baseband codec downlink signals
RXQP/RXQN	U15	U3	Quadrature baseband codec downlink signals
AUXI	R51	U3	Headset Microphone
HSO	U3	C94	Headset 32 ohm driver (single ended)
HSMICIP	C98	U3	Headset Microphone amplifier input (single ended)
HSMICBIAS	U3		Reserve
SCLK3	U7	U3	Sim Clock output from Processor
SIO3	U3/U7	U7/U3	SIM Data Communication between Processor and Power Control IC
SRST3	U7	U3	SIM Reset from Processor
SCLK5	U3	CONN1	SIM data Clock
SIO5	U3/CONN1	CONN1/U3	Data Communications path between SIM card and Power Control IC
SRST5	U3	CONN1	SIM Reset from power Control IC
APC	U3	R66	Automatic Power Control - Sets the PA output level
AFC	U3	R71	Automatic Frequency Control - Is used to ensure accurate frequency Information
EXT_FIQ	U3	U7	Fast external interrupt for ARM
EXT_IRQ	U3	U7	External interrupt for ARM
TDR	U7	U3	Time serial port input data
TEN	U7	U3	Time serial port enable
VCLKRX	U3	U7	Voiceband Data transfer Clock
VDR	U3	U7	Voiceband serial port receive data
VDX	U7	U3	Voiceband serial port transmit data
VFSRX	U3	U7	Voiceband Data Frame Synchronisation Signal
MCUDI	U3	U7	ARM Input serial data.
MCUDO	U7	U3	ARM Output serial data.
MCUEN0	U7	U3	ARM Configurable enable triggers (edge/level)
BDR	U3	U7	Downlink Processed Digital Baseband Audio Information
BDX	U7	U3	Uplink Processed Digital Baseband Audio Information
BFSR	U3	U7	Baseband Data Framesynch Receive
BFSX	U7	U3	Baseband Data Framesynch Transmit
D[0..15]	U7/U10/U104	U10/U104/U7	Data Bus for U10, U104 and U7
SPKN/SPKP	U10	BL2	Speaker Audio Amp output
SPVDD	L1	U10	Power supply of Speaker Audio Amp
RNW	U7	U10/U104	Read and Write - allows information to be written or read from the memory devices
nSC2	U7	U10	Used as Chip Enable for U10

LCDAO	U7	U10	U10 address signal
nFOE	U7	U10/U104	Flash and SRAM output Enable - Active Low
nIRQ_Melody	U10	U7	Melody IRQ request
nRESET	U7	U10/LCM	Reset of external peripheral
LED+	U103	LCM	LCM BackLight LED driver
LEDLCM_EN	U7	U103	LCM BackLight LED Enable
LCDID	U7	LCM	LCM mode selection
SCLK	U7	R9	I2C INTERFACE Master serial clock for LCM
SDO	U7	R8	I2C INTERFACE Serial bidirectional data for LCM
nSCS0	U7	R7	LCM chip select
DSR_MODEM	U7	TP5	Data Set Ready
DTR_MODEM	U7	TP6	Data Terminal Ready
RTS_MODEM	U7	TP2	Request To Send.
CTS_MODEM	U7	TP1	Clear To Send
RX_MODEM	U7	TP4/U11	Receive Data
TX_MODEM	U7	TP3/U12	Transmit Data
TX_IrDA	U7	TP17	Transmitted Infra-red information to an Infrared accessory
RX_IrDA	TP18	U7	Transmitted Infra-red information from an Infrared accessory
TDI	U3	TP13/U7	Data input of JTAG
TCXOEN	U7	U15	Xtal select(pull high for Crystal) and Xtal enable (VCXO and buffer supply)
S_PWCT	U7	R39	SIM power control
RTC_ALARM	U7	U3	Wakeup interrupt of real time clock
RF_LE	U7	U15	Transceiver / Processor Serial Communication Data Latch
RF_DAT	U7/U15	U15/U7	Transceiver / Processor Serial Communication Data
RF_CLK	U7	U15	Transceiver / Processor Serial Communication Data Clock
GSM_TXEN	U7	U17/U101	Used both within the RF switch and the TX VCO to select the GSM Frequency Band
TRENA	U7	U16	T/R switch enable
PAENA	U7	U101	Chip enable for PowerAmp IC
TSPACT0	U7	U15	RITA Serial interface reset
EAR_DETECT	R63	J5	Headset Detection
LEDB	U7	D4/D5/D6/D7/D8/D9/D10/D11	When this signal goes high, the Keypad backlights are illuminated
nSC0	U7	U104	Used as Chip Enable for the Flash Memory
nSC1	U7	U104	Used as Chip Enable for the SRAM
nBHE	U7	U104	Enable to address High Byte Information
nBLE	U7	U104	Enable to address Low Byte Information
A[1..22]	U7	U104	Address Bus of ARM memory Interface
COL[0..3]/ROW[0..4]	U7	Keypad Matrix	Forms part of Keypad Matrix
HS_EN	U7	U11/U12	Analog switch control(MODEM or Headset)

EAR_BIAS	U7	R64	Headset microphone bias
F_1.8V	U105	U104	Power supply of U104
DCS_PA	U101	U17	Amplified DCS PA output Transmit Frequency
GSM_PA	U101	U17	Amplified GSM PA output Transmit Frequency
DCS_TX	U15	U101	TX VCO generated transmit DCS Frequency
GSM_TX	U15	U101	TX VCO generated transmit GSM Frequency
REF_CLK	U14	U15	26MHz System Clock
DCS_RXN/DCS_RXP	U17	U15	Received DCS Antenna Frequency Signal
GSM_RXN/GSM_RXP	U17	U15	Received GSM Antenna Frequency Signal
VRF	U15	U15/U16	Power supply of RF module
FDP	U7	U104	The Flashreset/deeppower-downmode control
VBAT	J1	U3/U101/U15/U105/U10	Battery Voltage for supply purposes
FOLD	R69	R93	Detect folder open or close