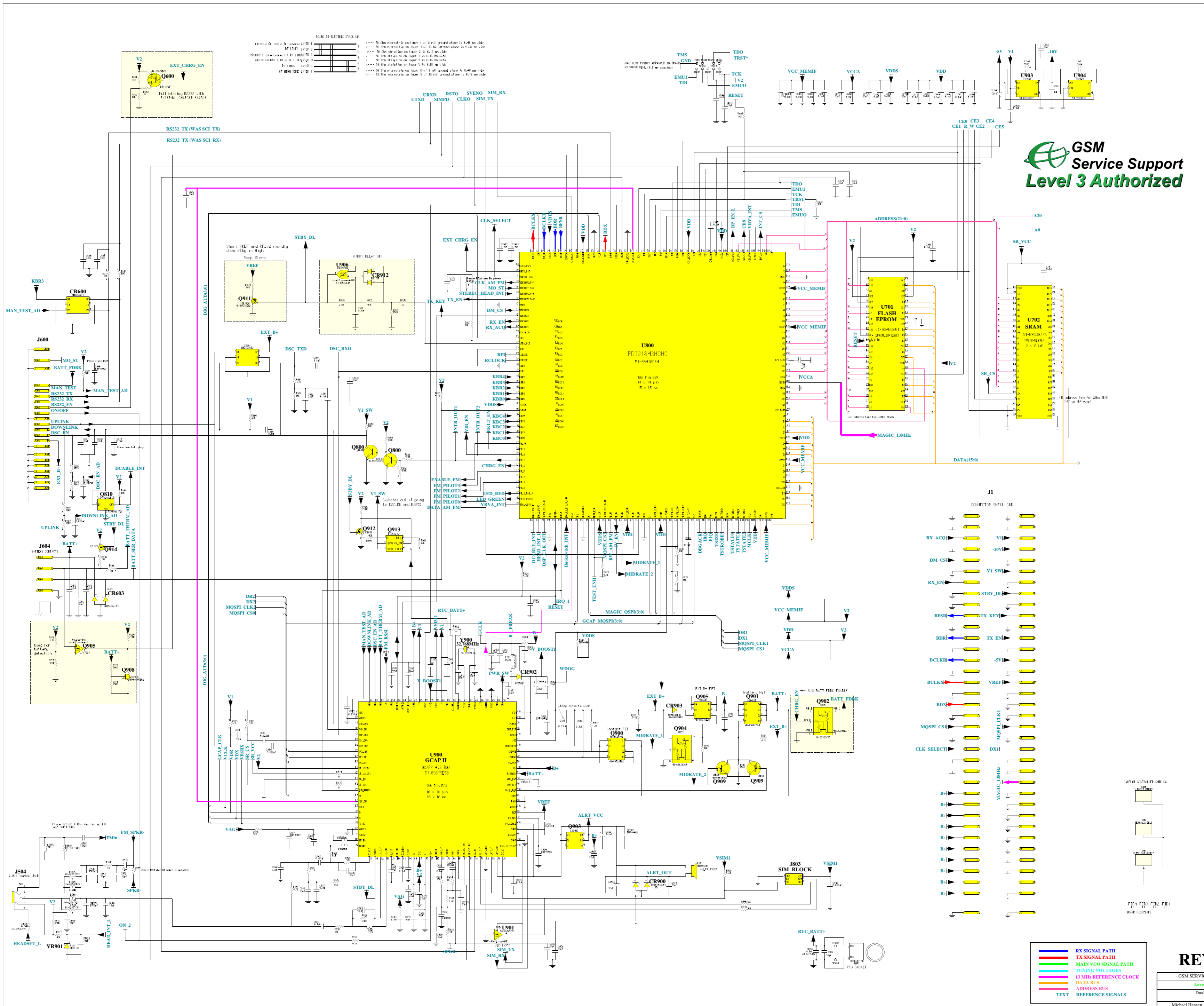


Dual Band Camelot_8486470K01_IP5



GSM Service Support
Level 3 Authorized

REV	DESCRIPTION	DATE
F6	Changed from EEP design to Liberate design and moved the RF section to the board connector. Changed board connector P1 to board connector P2.	11/11/03
F1	Removed dual audio jack. Changed from 3.5mm to 2.5mm.	11/11/03
F2	Removed single 3.5mm jack from RF section. Changed from 3.5mm to 2.5mm. Changed from 3.5mm to 2.5mm. Both changes made due to part shortages.	11/11/03
F2	Changed CR907 from 100k to 10k. Changed from 100k to 10k. Changed from 100k to 10k. Changed from 100k to 10k.	11/11/03
F3	Changed CR911 to 100k. Changed from 100k to 100k. Changed from 100k to 100k. Changed from 100k to 100k.	11/11/03
F4	Changed pin 2 and 4 on audio jack. Changed from 2 to 4. Changed from 2 to 4. Changed from 2 to 4. Changed from 2 to 4.	11/11/03
F5	Changed to add 10k resistor. Changed from 10k to 10k. Changed from 10k to 10k. Changed from 10k to 10k.	11/11/03
F6	Added 10k resistor. Changed from 10k to 10k. Changed from 10k to 10k. Changed from 10k to 10k.	11/11/03

REVISIONS

GROUP	DATE
GSM SERVICE SUPPORT GROUP	22.08.00
Level 3 Schematics	Rev. 1.0
Dualband Camelot	8486470K01_IP5
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- RX SIGNAL PATH
- TX SIGNAL PATH
- MAIN VDD SIGNAL PATH
- TUNING VOLTAGES
- 13 MHz REFERENCE CLOCK
- DATA BUS
- ADDRESS BUS
- TEXT REFERENCE SIGNALS