


L6 i-Mode Signal Description			Date: 31.05.2006 Version: 1.0 Service, Engineering & Optimization	
BRIT2 Main board				
Signal Name	from	to	Description	
/13MHZ_VCP	C506-1	C226-1	13MHz Master Clock for Video Co Processor	
/ABUS[1-23]	U500-R6	U200-C10	Adress Bus from 1 to 23 for memories and Video Co Processor memory	
/ADV	U200-E11	U201-E5	Adress Valid input for NOR flash memory	
/AIN1P	J402-2	C422-1	Audio input 1 positive voice band for DBB Typhoon (from microphone)	
/AIN2P	C410-1	U100-P15	Audio input 2 positive voice band for DBB Typhoon (from headset microphone through Enhanced Mini USB IC)	
/AIN3L	C322-2	U100-R13	Not used on BRIT2	
/AOUT1N	U303-7	U100-K15	Typhoon IC Audio output 1 negative output signal towards audio interface	
/AOUT1P	U303-3	U100-J15	Typhoon IC Audio output 1 positive output signal towards audio interface	
/AOUT3L	R409-1	U302-4	Typhoon IC Audio output 3 left channel output signal towards Enhanced Mini USB IC	
/ASDI	U200-C6	U100-E1	Audio serial port data input	
/ASDO	U200-B5	U100-D2	Audio serial port data output	
/ASFS	U200-B6	U100-F2	Audio serial port framing signal (active high)	
/ATSM	U200-A4	U100-J1	TX state machine control	
/BAND_SEL1	U600-2	U200-K3	RF band select 1 from MDA1+ to antenna switch module	
/BAND_SEL2	U600-1	U200-J5	RF band select 2 from MDA1+ to antenna switch module	
/BATT_FET	U400-36	Q403-1	Charging command signal	
/BATT_IO	R403-2	R404-2	Eprom BATT read for signal threshold	
/BP_FET	Q402-1	U400-34	Charging command signal	
/BSDI	U200-C8	U100-G2	Baseband serial port data input	
/BSDO	U200-E7	U100-E2	Baseband serial port data output	
/BSIFS	U200-A8	U100-F1	Baseband serial port input framing signal (active high)	
/BSOFS	U200-B7	U100-G1	Baseband serial port output framing signal (active high)	
/BURSTCLK	U200-C13	U201-C6	Synchronous (burst mode) clock for NOR flash memory	
/CAM_CAMCLK	U500-J13	R512-1	Camera master clock 13MHz	
/CAM_HSYNC	U500-G3	J501-16	Camera horizontal synchro lines 15kHz	
/CAM_PCLK	U500-G13	C534-1	Camera pixel clock 52MHz	
/CAM_PWRDN	R511-1	U500-J2	Camera power down signal	
/CAM_RESET	U500-H2	R510-1	Camera reset signal	
/CAM_VSYNC	U500-H1	J501-15	Camera vertical synchro lines 15Hz	
/CHRGCTRL	TP406-1	U400-30	Charge control circuit gate driver	
/COL[0-5]	J402-17	SW400-4	Keypad column scan input [0-5]	
/CSDI	U200-A7	U100-H2	Control serial port data input	

/CSDO	U200-A6	U100-H1	Control serial port data output
/CSFS	U200-C7	U100-L2	Control serial port framing signal
/CTS1	U700-A7	C710-1	Flow control UART signal from DBB MDA1+ UART1 interface to bluetooth IC UART
/DBBON	U200-E2	TP101-1	Main power control from DBB MDA1+ to ABB Typhoon
/DBUS[0-15]	U500-P14	U200-A12	Datas BUS from 0 to 23 for memories and Video Co Processor memory
/DCS_PCS_PA_IN	R601-1	R602-1	RF TX signal in DCS PCS band for PA input
/DM	D400-1	R412-1	bidirectionnal data minus of DM pin USB interface
/DP	D400-3	C401-1	bidirectionnal data positive of DP pin USB interface
/EMU_PWR_ON	U400-33	Q200-1	External Enhanced Mini USB IC power on indication to DBB MDA1+
/EN_BL1	U500-B13	U102-10	Enable signal 1 for LCD backlight driver
/EN_BL2	U500-E13	U102-1	Enable signal 2 for LCD backlight driver
/EN_BT	C723-1	U701-6	Enable power source for bluetooth
/EN_FEM	U600-27	U200-T1	Enable front end module
/EN_LDO	U501-6	R122-1	Enable low dropout regulator for USB_VDD, 2.8VD, 1.5VD,2.8VANA_CAM power source
/EN_PU_USB	Q404-2	U200-G2	Enable pull-up resistor on USB DP line
/EN_RCVR	U300-C3	U200-U7	Enable receiver audio amplifier
/EN_SENSOR	J402-25	U200-R2	Enable light sensor signal
/EN_SPKR	R303-1	U301-C3	Enable speaker audio amplifier
/EN_VAUD	U200-P17	U108-5	Enable 1.5VAUD and 2.9VAUD regulator power source
/EN_VC XO	U602-6	R610-1	Enable Voltage Control (X)crystal Oscillator
/EN_VIB	U200-L3	U107-6	Enable vibrator signal
/FD[0-7]	U500-A3	FL501-4	Data BUS from Video Co Processor to LCD
/GSM850_900_PA_IN	U600-6	R604-1	RF TX signal in GSM850 900 band for PA input
/GSM_RX_IN	FL601-1	C602-2	RF RX signal input in GSM band
/HOOK_BIAS	Q405-2	U200-B16	Enable Hook Bias on AOUT3L Signal
/HW_MOD_ID0	U200-U10	R211-1	Hardware modem board identification 0
/IChRG	U400-5	U100-A9	Muxed output voltage proportional to the charge current or the ID voltage
/IN	U601-6	C106-2	RF I channel negative input/output
/INT_ABB	U200-G3	U100-D1	Interrupt signal to DBB MDA1+ from ABB Typhoon
/IP	U601-7	C106-1	RF I channel positive input/output
/ISENSE	U400-32	Q401-2	Current sense input to the Enhanced Mini USB IC charge control circuitry
/IT_EMU	U400-10	U200-W10	Interrupt signal to application processor from Enhanced Mini USB IC
/IT_MELODY	U304-D7	U200-V15	Interrupt signal to application processor from Melody IC
/LCD_CS	U500-C3	J500-15	LCD Chip select signal managed by Video Co Processor
/LCD_LED[1-3]	J500-19	U102-2	LCD backlight Led current measured through R104 R106 R107 in order to managed global LCD backlight adjustable
/LCD_RESET	RV500-1	J500-13	Reset signal managed by DBB MDA1+ for LCD
/LCD_RS	U500-C14	J500-14	LCD data / command select pin
/LCD_WR	U500-B3	R513-2	LCD read / write select pin

/LIGHT_ADC	J402-26	U100-B10	Signal from <b>Light</b> sensor interface to ABB Typhoon
/MASSE-ANTENNE	C706-1	L700-1	Ground for <b>antenna</b>
/MCLK	R202-1	U100-L1	13MHz <b>master clock</b> from DBB MDA1+ to ABB Typhoon
/MCLKEN	U200-C4	U100-J2	ABB Typhoon produces an output <b>clock enable</b> signal indicating whether an active MCLK clock is required
/MCLK_MELODY	C307DNP-1	C309-1	13MHz <b>Master Clock</b> for <b>MELODY</b> IC
/MIC_BIAS	J402-4	C423-1	<b>2.5V</b> microphone power source
/MUX_USB	U401-2	U401-8	Enable <b>multiplexeur USB</b> switch
/NBOOT	C438-1	TP_BOOT-1	active low ( <b>N</b> ) select download mode by UART for DBB MDA1+
/NCSRAM	U200-F18	U201-D6	active low ( <b>N</b> ) <b>Chip Select PSRAM</b> memory
/NCSROM	U200-F17	U201-K1	active low ( <b>N</b> ) <b>Chip Select NOR</b> flash memory
/NCS_MELODY	U304-G5	U200-R18	active low ( <b>N</b> ) <b>Chip Select</b> for Serial Port Interface 1 I/F of <b>MELODY</b> IC
/NCS_VCP	U500-R8	U200-B13	active low ( <b>N</b> ) <b>Chip Select</b> for <b>Video Co Processor</b>
/NIT_JTAG	TP405-1	U200-V5	active low ( <b>N</b> ) <b>Interrupt</b> for <b>JTAG</b> interface
/NIT_VCP	C516DNP-1	U500-N10	active low ( <b>N</b> ) <b>Interrupt</b> for <b>Video Co Processor</b>
/NLB	U500-N9	U200-L17	PSRAM <b>Lower Byte Enable</b>
/NPWRKEY	Q200-3	U200-F5	active low ( <b>N</b> ) power key out signal generate by ABB Typhoon
/NRD	U500-P10	U200-G15	active low ( <b>N</b> ) read mode from application processor to Video Co Processor and memories
/NRESET	C225DNP-1	U200-A17	active low ( <b>N</b> ) general <b>reset</b> managed by ABB Typhoon IC
/NRESET-DBB_OUT	U200-C19	U201-F4	active low ( <b>N</b> ) <b>reset</b> from DBB MDA1+ <b>digital baseband output</b> to ABB Typhoon and NOR flash memory
/NRST_BT	U700-E6	C705-1	active low ( <b>N</b> ) <b>Reset</b> for <b>bluetooth</b> IC
/NRST_EMU	U400-11	C416DNP-2	active low ( <b>N</b> ) <b>Reset</b> for <b>Enhanced Mini USB</b> IC
/NRST_MELODY	U304-A5	U304-D6	active low ( <b>N</b> ) <b>Reset</b> for <b>MELODY</b> IC
/NRST_VCP	R502-1	U200-C5	active low ( <b>N</b> ) <b>Reset</b> for <b>Video Co Processor</b>
/NSIMRST	J402-29	U200-U5	active low ( <b>N</b> ) for <b>SIM</b> card interface <b>reset</b>
/NUB	U500-K14	U200-L19	PSRAM <b>Upper Byte Enable</b>
/NWR	U500-R9	U200-G17	active low ( <b>N</b> ) NOR Flash <b>WR</b> ite enable
/PA_RAMPDAC	U600-26	C631-1	RF <b>Power amplifier</b> control output
/PCMCLK	U700-B6	C701-1	<b>Pulse code modulation data clock</b> (audio bus for bluetooth IC)
/PCMDI	U700-D5	C702-1	<b>Pulse code modulation data input</b> (audio bus for bluetooth IC)
/PCMDO	U700-B7	C703-1	<b>Pulse code modulation data output</b> (audio bus for bluetooth IC)
/PCMSYNC	U700-C5	C704-1	<b>Pulse code modulation data synchro</b> (audio bus for bluetooth IC)
/PDNB	U601-9	C621DNP-2	Power source of transceiver alim
/PWM_KEY_BL	C511DNP-1	U500-B12	enable <b>power management keypad backlight</b>
/PWRKEY	C435-1	TP_PWRON-1	<b>power key</b> signal directly managed by END key (not managed by keypad row column matrix)
/QN	U601-4	U100-A14	RF <b>Q</b> channel <b>negative</b> input/output
/QP	U601-5	U100-A15	RF <b>Q</b> channel <b>positive</b> input/output
/RCVON	FL400-A1	U302-1	From audio power amplifier to <b>receiver output Negative</b>
/RCVOP	FL400-A3	U302-9	From audio power amplifier to <b>receiver output Positive</b>

<b>/RFCLK</b>	U601-1	U200-P5	<b>RF</b> control serial interface <b>clock</b> out (Transceiver bus)
<b>/RFSDATA</b>	U601-3	U200-R3	<b>RF</b> control serial interface <b>data</b> out (Transceiver bus)
<b>/RFSENB</b>	U601-2	U200-T2	<b>RF</b> control serial interface <b>enable</b> (Transceiver bus)
<b>/RF_CLK_OUT</b>	C610-2	U200-B12	26MHz <b>clock output</b> signal from <b>RF</b> interface to DBB MDA1+
<b>/ROW[0-4]</b>	J402-11	U200-V13	Keypad <b>row</b> scan output <b>[0-4]</b>
<b>/RTCK</b>	TP404-1	C440-1	<b>Return Test clock</b> signal from the target JTAG port to ARM debugger
<b>/RTS1</b>	U700-C4	C711-1	Flow control UART signal from DBB MDA1+ UART1 RTS output to bluetooth UART CTS input
<b>/RXD0</b>	C436-1	TP_RXD-1	Used for software download from TP_RXD test point to DBB MDA1+ UART0 data input
<b>/RXD1</b>	U700-D4	C709-1	from bluetooth UART data output to DBB MDA1+ IC UART1 data input
<b>/RXON</b>	U200-E6	U100-K1	The RXON signal is used to enable the receive section. The duration of the receive interval is controlled by RXON
<b>/SCL</b>	J501-2	R508-1	I2C serial clock bus from DBB MDA1+ to Enhanced Mini USB IC and camera
<b>/SDA</b>	R509-1	J501-4	I2C serial data bus from DBB MDA1+ to Enhanced Mini USB IC and camera
<b>/SIMCLK</b>	J402-33	C428-1	<b>SIM Clock</b> signal for SIM card interface
<b>/SIMDAT</b>	J402-34	C427-1	<b>SIM data</b> signal for SIM card interface
<b>/SPEAKER_N</b>	J402-37	J402-36	Audio amplify negative signal to speaker
<b>/SPEAKER_P</b>	J402-39	C425-1	Audio amplify positive signal to speaker
<b>/SPI1_CLK_MELODY</b>	U304-G6	U200-P15	6.5MHz for Serial Port Interface 1 I/F <b>CLOCK</b> of <b>MELODY</b> IC
<b>/SPI1_RXD_MELODY</b>	U304-E4	U200-T18	Serial Port Interface 1 I/F for <b>RX</b> Datas of <b>MELODY</b> IC
<b>/SPI1_TXD_MELODY</b>	U304-F4	U200-R17	Serial Port Interface 1 I/F for <b>TX</b> Datas of <b>MELODY</b> IC
<b>/SW_RCVR_HS</b>	U302-8	U302-2	Switch audio signal command to receiver or headset
<b>/SW_RCVR_SPKR</b>	U303-8	U303-2	<b>Switch</b> AOUT1 to <b>RCVR</b> (1) or <b>SPKR</b> (0) amplifier output
<b>/TCK1_DAICLK</b>	TP400-1	C444-1	Signal from the target JTAG port to ARM debugger
<b>/TDI1_DAIDI</b>	C442-1	TP402-1	Signal from the target JTAG port to ARM debugger
<b>/TDO1_DAIDO</b>	C441-1	TP403-1	Signal from the target JTAG port to ARM debugger
<b>/THBAT</b>	TP_THBAT-1	R406-2	monitoring signal battery temperature
<b>/TH_BAT</b>	R406-1	C408DNP-1	monitoring signal battery temperature
<b>/TMS1_NDAIRST</b>	TP401-1	C443-1	Signal from the target JTAG port to ARM debugger
<b>/TXD0</b>	C437-1	TP_TXD-1	used for software downloaded from DBB MDA1+ UART0 data output to TP_RXD test point
<b>/TXD1</b>	U700-B5	C708-1	from DBB MDA1+ UART1 data output to bluetooth UART data input
<b>/TXON</b>	U200-B4	U100-K2	The TXON signal is used to enable the transmit section. The duration of the transmit interval is controlled by TXON
<b>/TX_RX</b>	U600-28	U200-H5	RF <b>TX</b> signal command for Antenna Switch Module
<b>/USB_D+</b>	R414-2	R415-1	<b>USB DP</b> signal from MUX USB to DBB MDA1+
<b>/USB_D-</b>	R416-1	R413-2	<b>USB DM</b> signal from MUX USB to DBB MDA1+
<b>/USB_ID</b>	U400-15	D400-6	<b>USB ID</b> signal from USB connector to Enhanced Mini USB IC
<b>/VABB</b>	C102-2	U100-H16	Analog <b>Baseband</b> power source
<b>/VBACK</b>	C103-1	U100-T4	2.85V <b>backup</b> battery supply
<b>/VBUS</b>	C402-1	U400-29	5V power source from USB charger input
<b>/VCXO</b>	U100-D16	C128-2	<b>Voltage Control (X)</b> crystal <b>Oscillator</b> power source

<b>/VM</b>	U400-14	U200-L5	Detection of the state of <b>VM</b> from the Enhanced Mini USB IC to the DBB MDA1+
<b>/VP</b>	U400-13	U200-L2	Detection of the state of <b>VP</b> from the Enhanced Mini USB IC to the DBB MDA1+
<b>/VVIB</b>	C119-1	U107-3	Power signal for <b>vibrator</b>
<b>/WAIT</b>	U200-E19	R207-2	NOR flash memory indicate to application processor when the data on the output bus are valid on synchronous mode
<b>/YUV[0-7]</b>	J501-6	U500-G1	Host interface video input from camera to Video Co Processor
<b>1.5VAUD</b>	C303-1	U304-A7	<b>1.5V</b> power source for <b>audio</b> interface
<b>1.5VD</b>	U500-C2	U500-H3	<b>1.5V</b> power source for <b>digital</b> interface
<b>1.8VD</b>	Q405-4	C218-1	<b>1.8V</b> power source for <b>digital</b> interface
<b>2.8VANA_CAM</b>	R506DNP-2	R515DNP-2	<b>2.8V</b> power source for <b>analog camera</b> interface
<b>2.8VD</b>	U601-11	C622DNP-2	<b>2.8V</b> power source for <b>digital</b> interface
<b>2.8VLCD</b>	C513-1	J500-16	<b>2.8V</b> power source for <b>LCD</b> interface
<b>2.9VAUD</b>	C308-1	U304-E1	<b>2.9V</b> power source for <b>audio</b> interface
<b>BATT_P</b>	U600-25	U600-3	<b>Battery (plus)</b> output/input, RF PA power source, input converter ADC
<b>USB_VDD</b>	Q404-4	C217-2	Power source for DBB MDA1+ USB interface and DP pull-up resistor
<b>VBAT</b>	U602-1	C614-1	Main source for the phone
<b>VBL</b>	J500-22	C512-1	<b>V</b> backlight power source interface adjust by EN_BL 1&2
<b>VBT1</b>	U700-B4	U700-F1	<b>2.8V Bluetooth</b> power source interface
<b>VEXT</b>	U303-5	C310-1	Power source for MUX audio interface
<b>VMEM</b>	U500-J15	U500-L2	<b>1.8V</b> power source for <b>memory</b> interface
<b>VRF1</b>	U602-3	C612-1	<b>2.8V</b> power source for <b>RF</b> interface
<b>VRTC</b>	R204-1	U200-F2	<b>1.8V</b> power source for <b>Real Time Clock</b> interface
<b>VSIM</b>	J402-30	U100-T6	<b>2.85V</b> power source for <b>SIM</b> card interface
<b>VUSB</b>	C445-2	R419DNP-2	<b>3.2V</b> power source for <b>USB</b> regulator (USB_VDD) and MUX USB