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|---------------------------|--|----------------|-------|-----------|
| Engineer Gary Quartana | Motorola Inc 8000 W Sunrise Blvd Plantation, Florida | | | |
| Drawn by e11248 | | | | |
| R&D CHK | TITLE Sirius_DB DaughterBoard PCB | | | Size D |
| DOC CTRL CHK | | | | |
| MFG CTRL CHK | | | | |
| QA CHK | REV P1 | Drawing Number | Sheet | of |

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|----------------------|--------------------------------------|---------------------|
| Changed by e11248 | Date Changed Friday, May 27, 2005 | Time 10:25:36 am |
|----------------------|--------------------------------------|---------------------|