

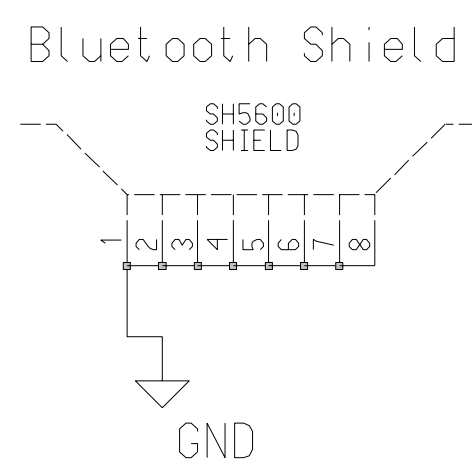
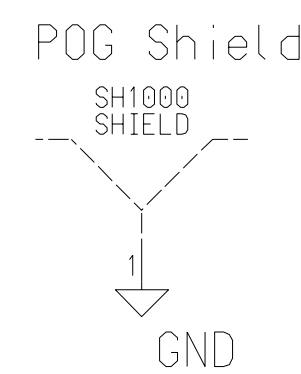
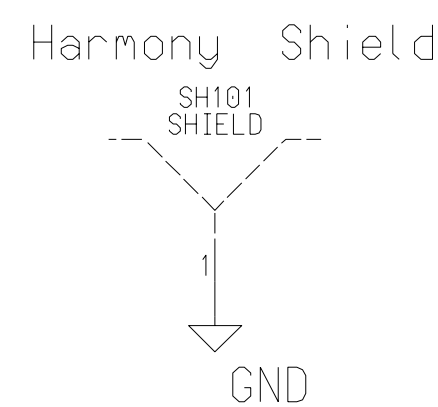
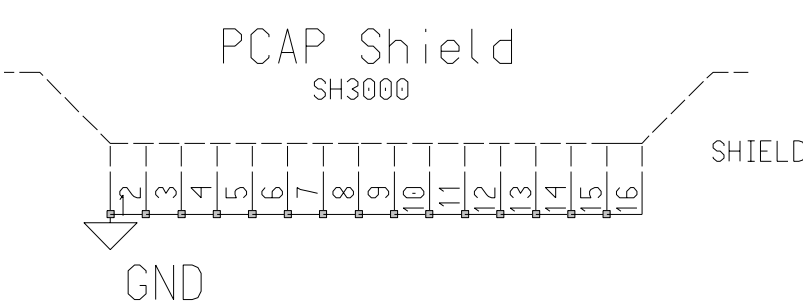
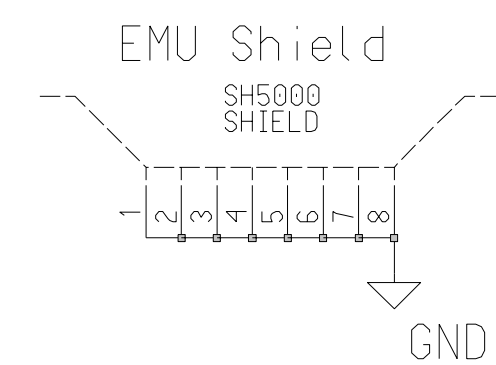
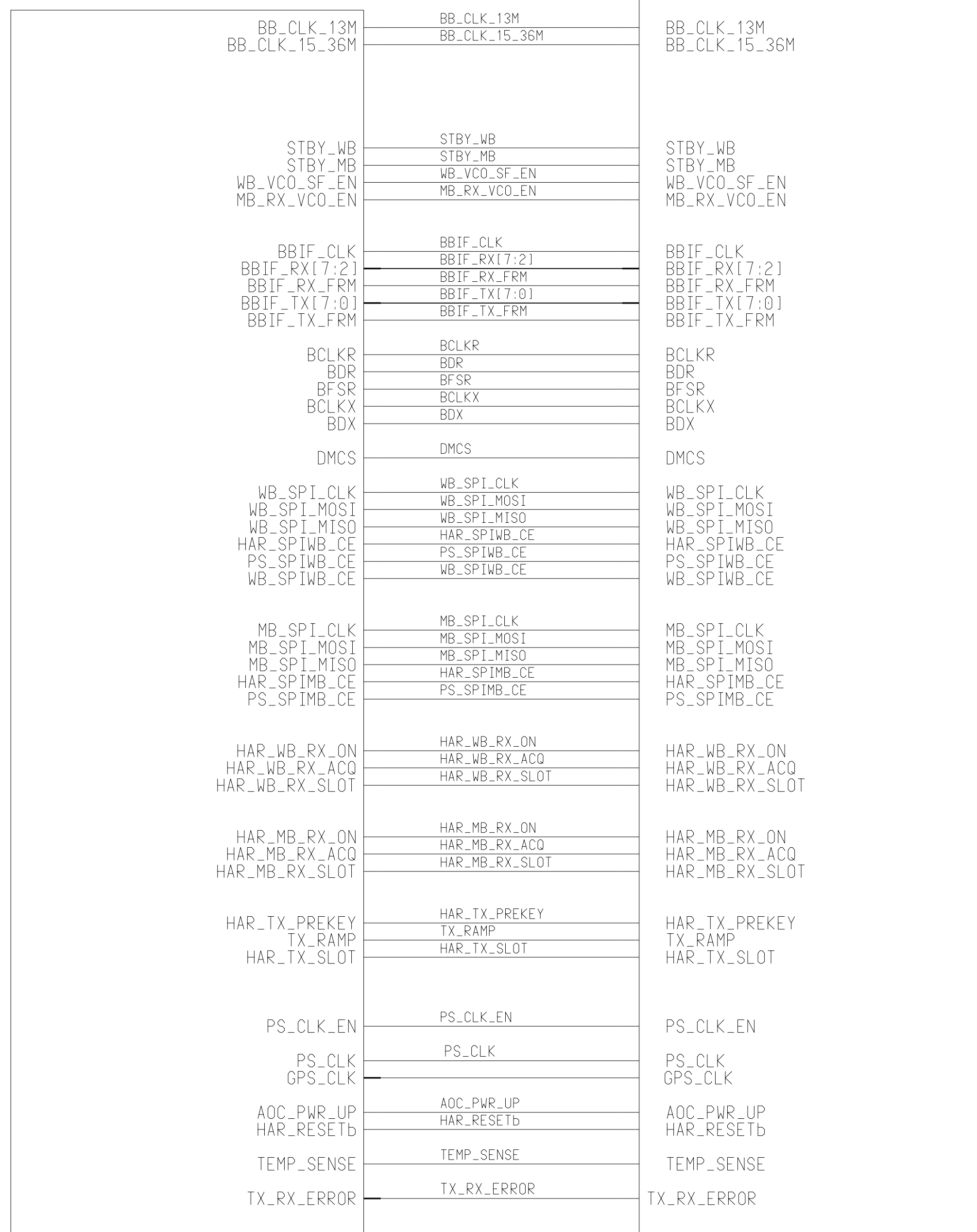
MOTOROLA CONFIDENTIAL PROPRIETARY

# Baseband

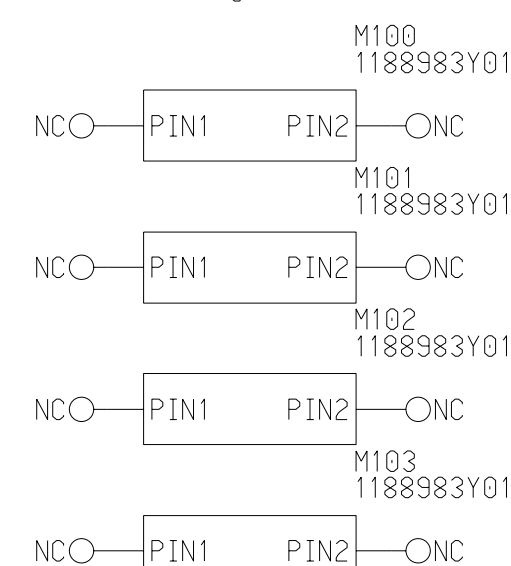
# RF

413 A

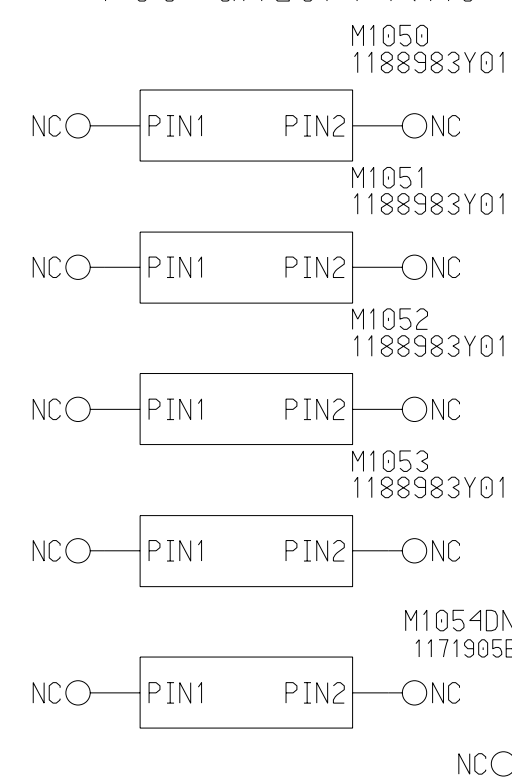
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Drawn by: Rajesh Verma			
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MFG CTRL CHK:	QA CHK: w16748	Of:	
Changed by:	Date: Tuesday, December 21, 2004	Time: 3:27:32 pm	



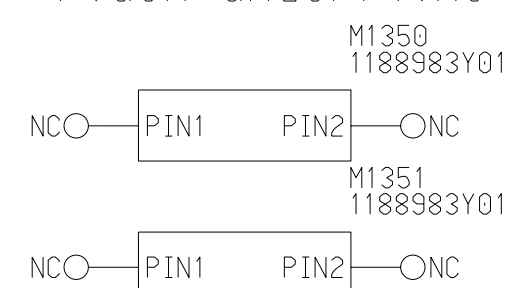
### Harmony underfilms



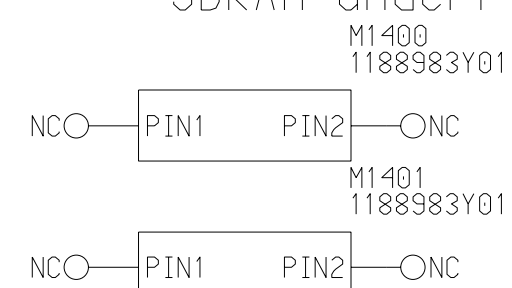
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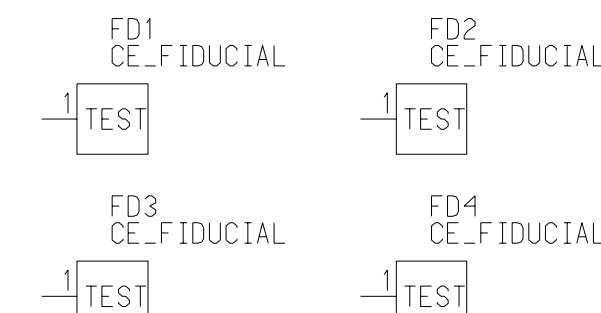
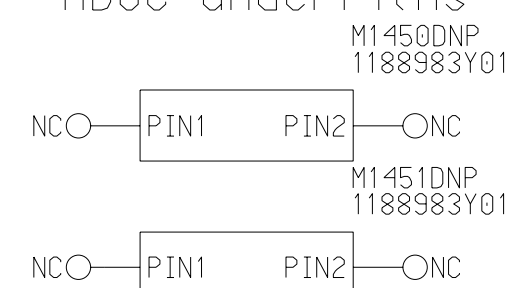
### Flash underfilms



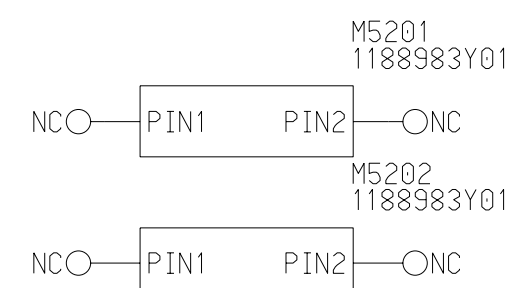
### SDRAM underfilms



### MDOC underfilms

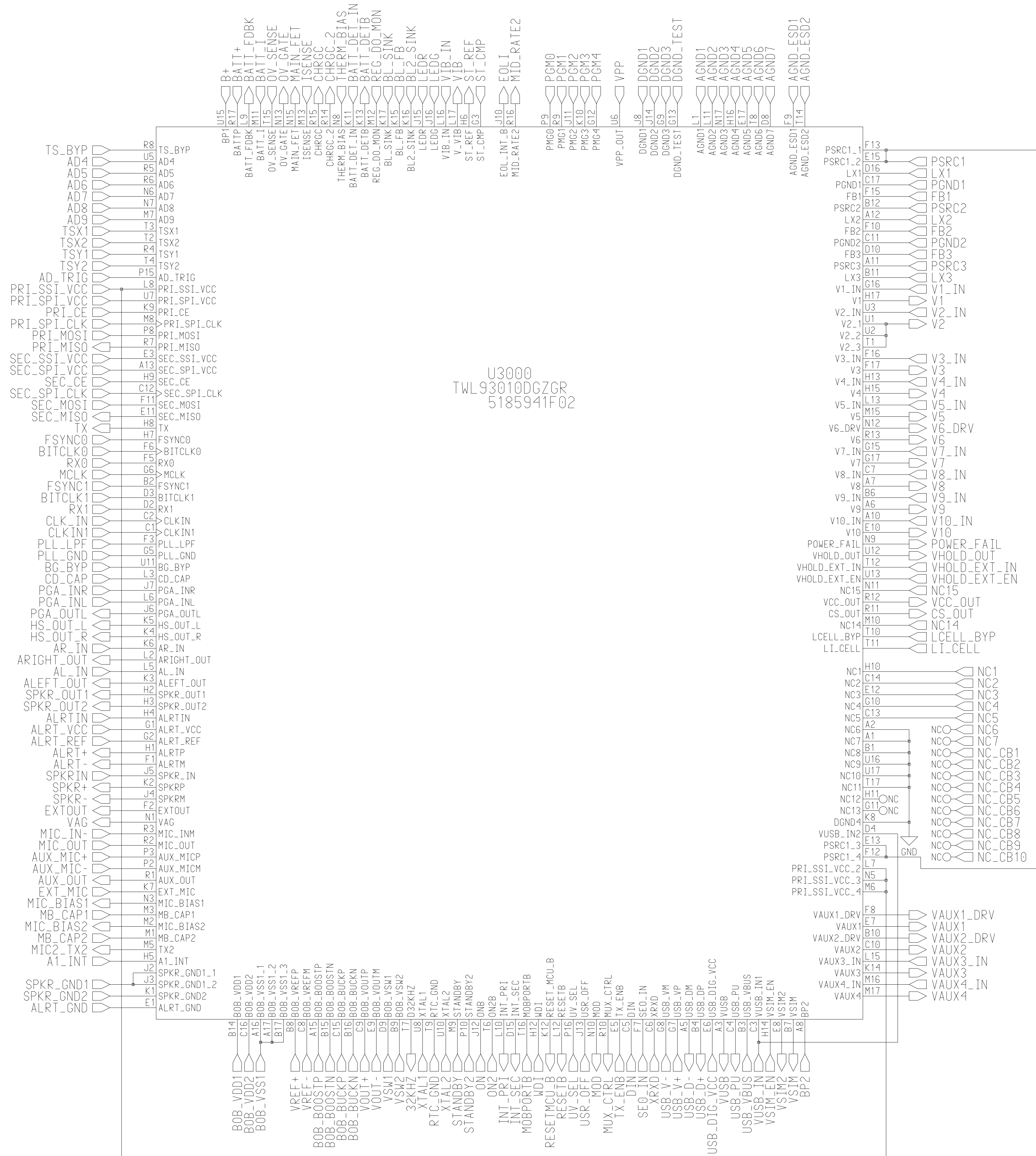


### GPU (nVidia) underfilms



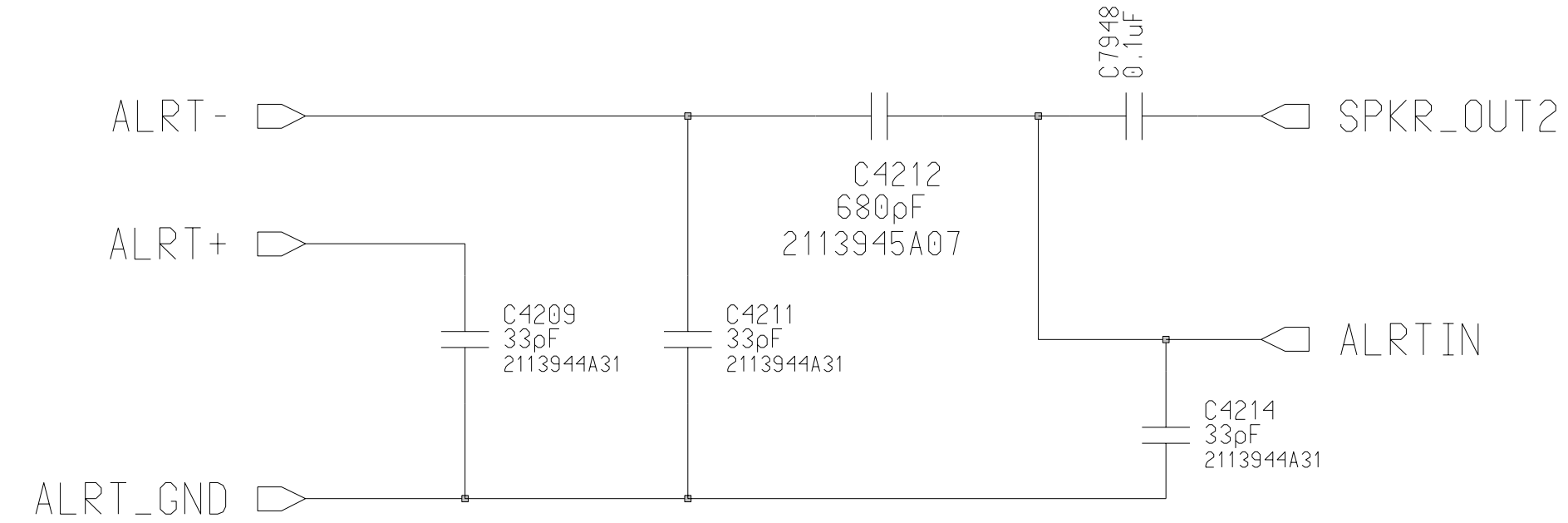
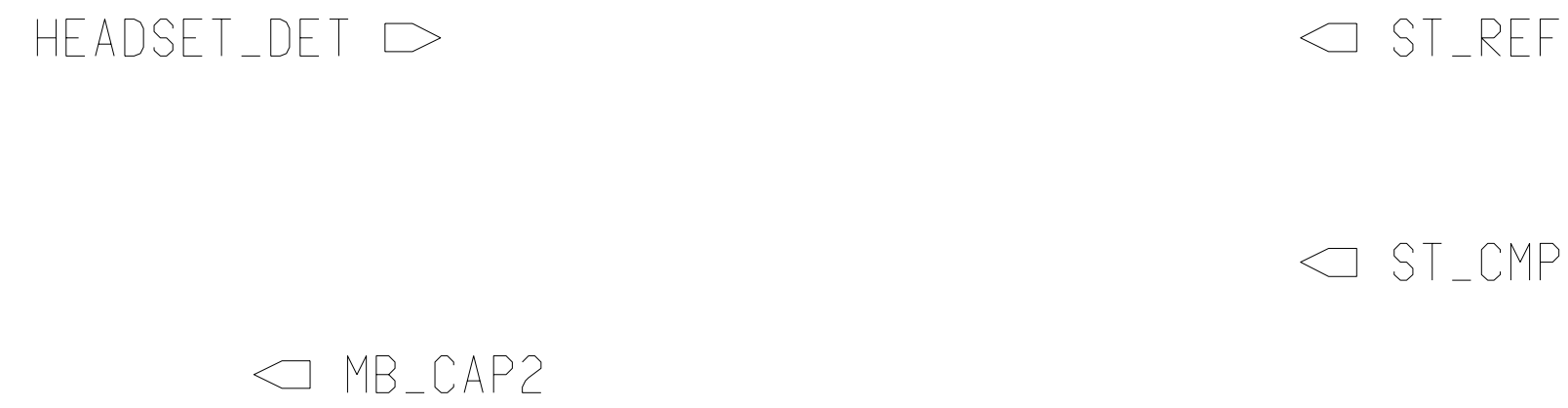
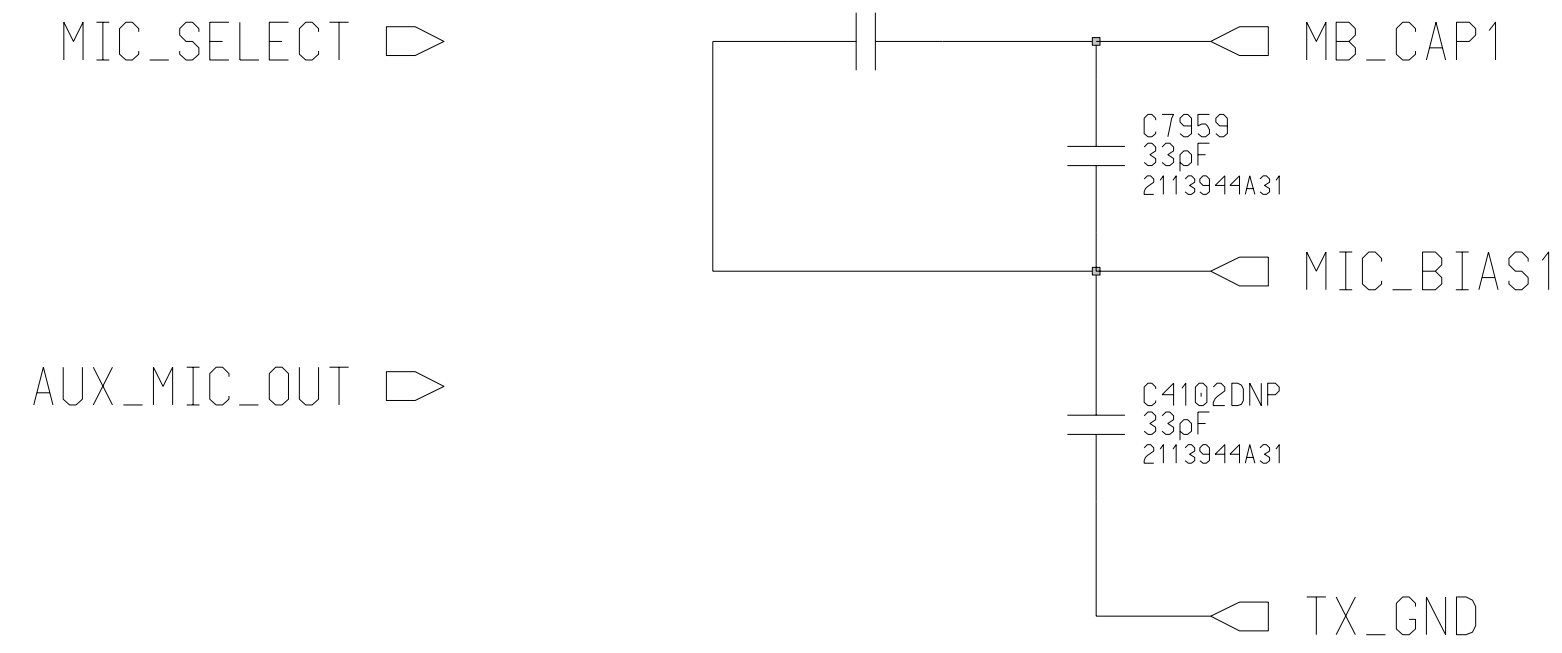
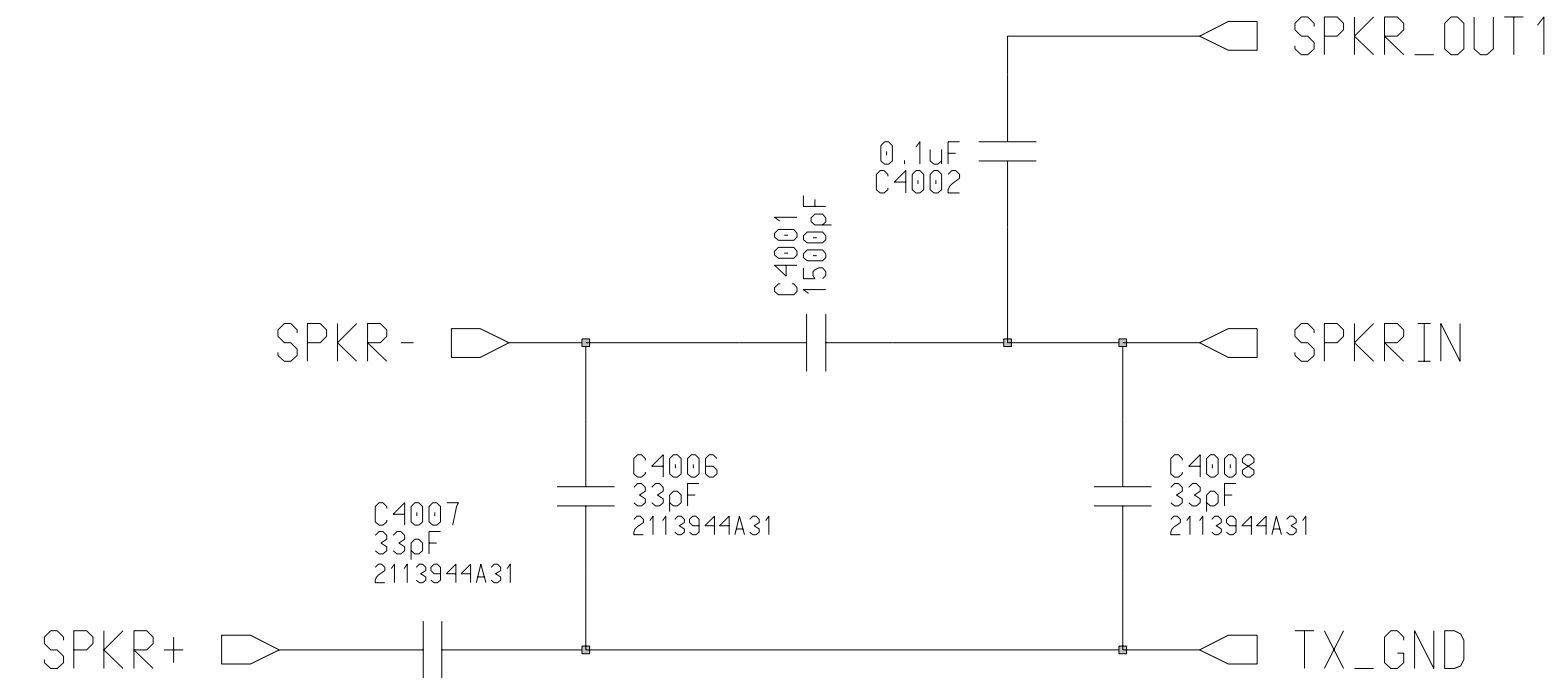
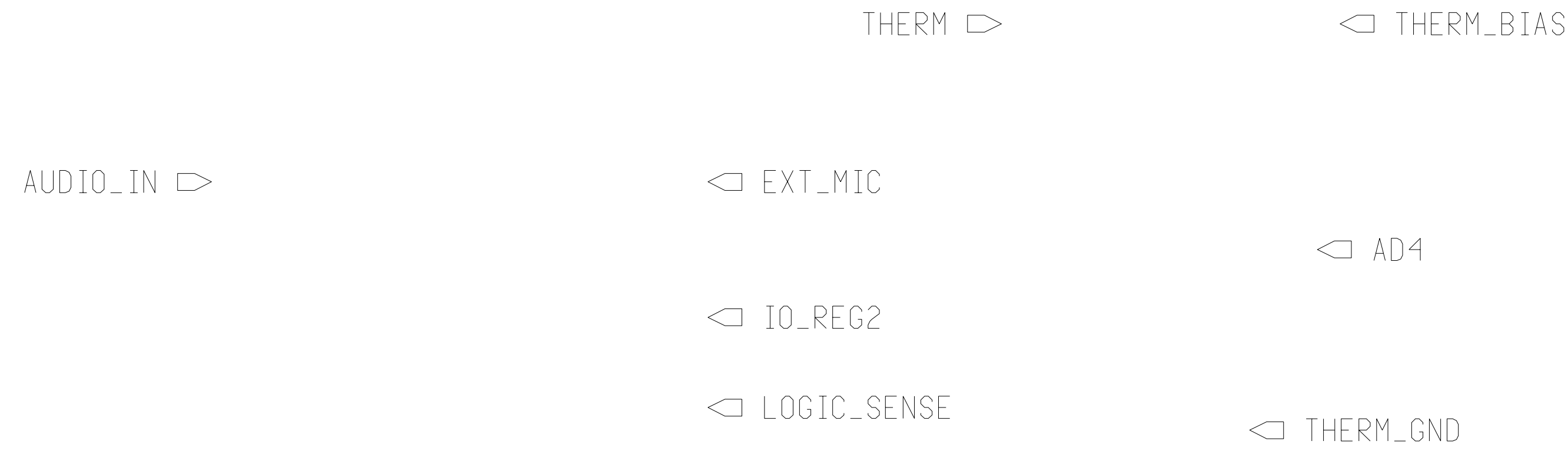
Engineer:			
Drawn by:			
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DOC CTRL CHK:	PCAP		
MFG CTRL CHK:	IC		
QA CHK:	REV: P9	Drawing Number: 8488453Y07	Page: Of:
Changed by:	Date:	Time:	

A  
B  
C  
D



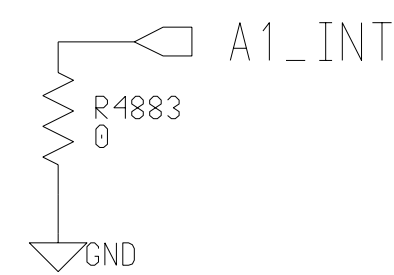
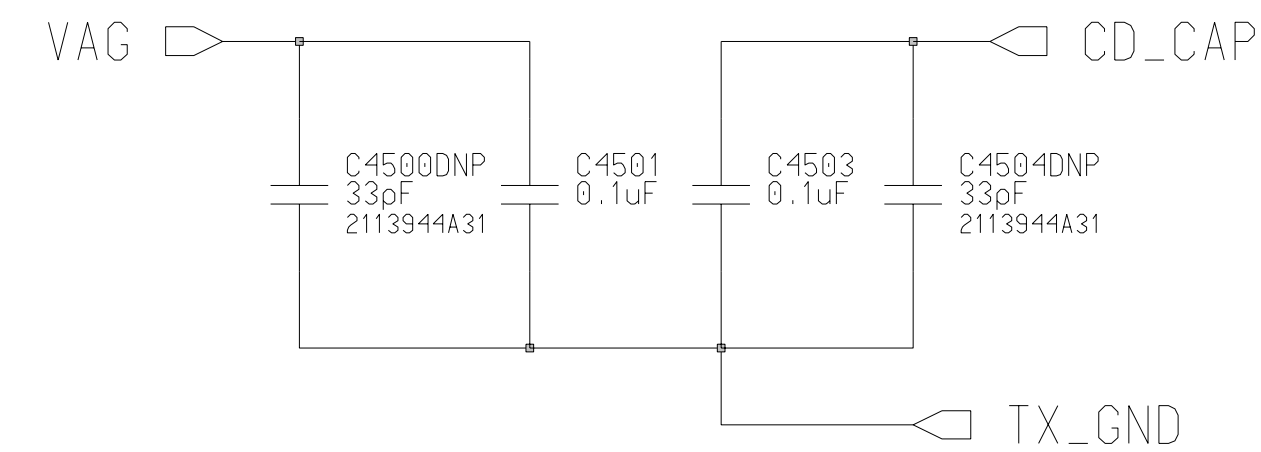
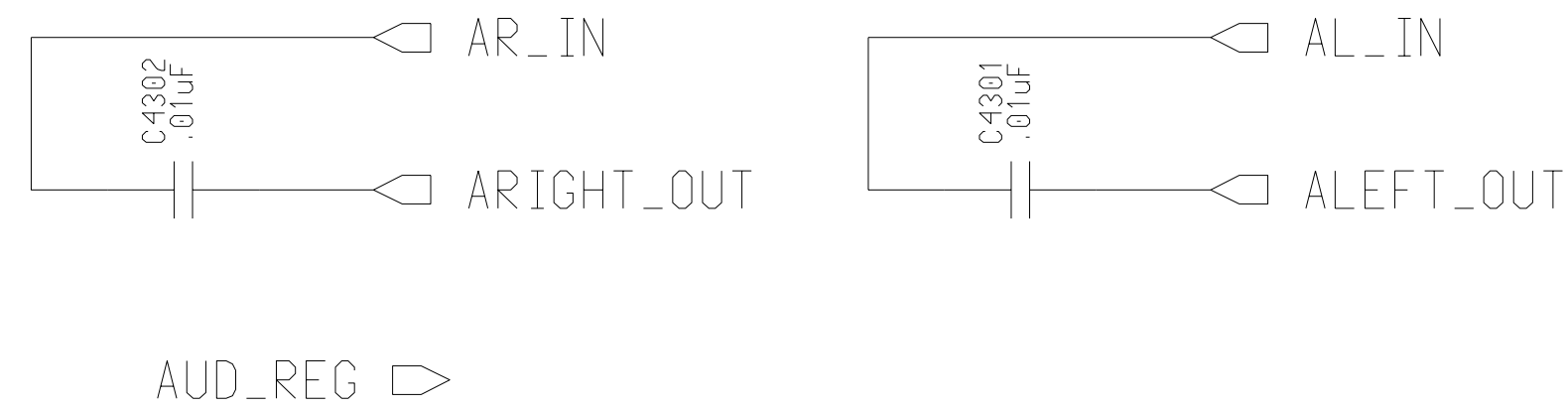
MOTOROLA CONFIDENTIAL PROPRIETARY

Engineer:	MOTOROLA INC.		
Drawn by:			
R&D CHK:	TITLE:	Size:	
DOC CTRL CHK:			
MFG CTRL CHK:			
QA CHK:	REV:	Drawing Number:	Page: Of:
Changed by:	Date:	Time:	



In case Audio Module is used:

- RX\_GND ▷
- NC1 ▷
- NC2 ▷

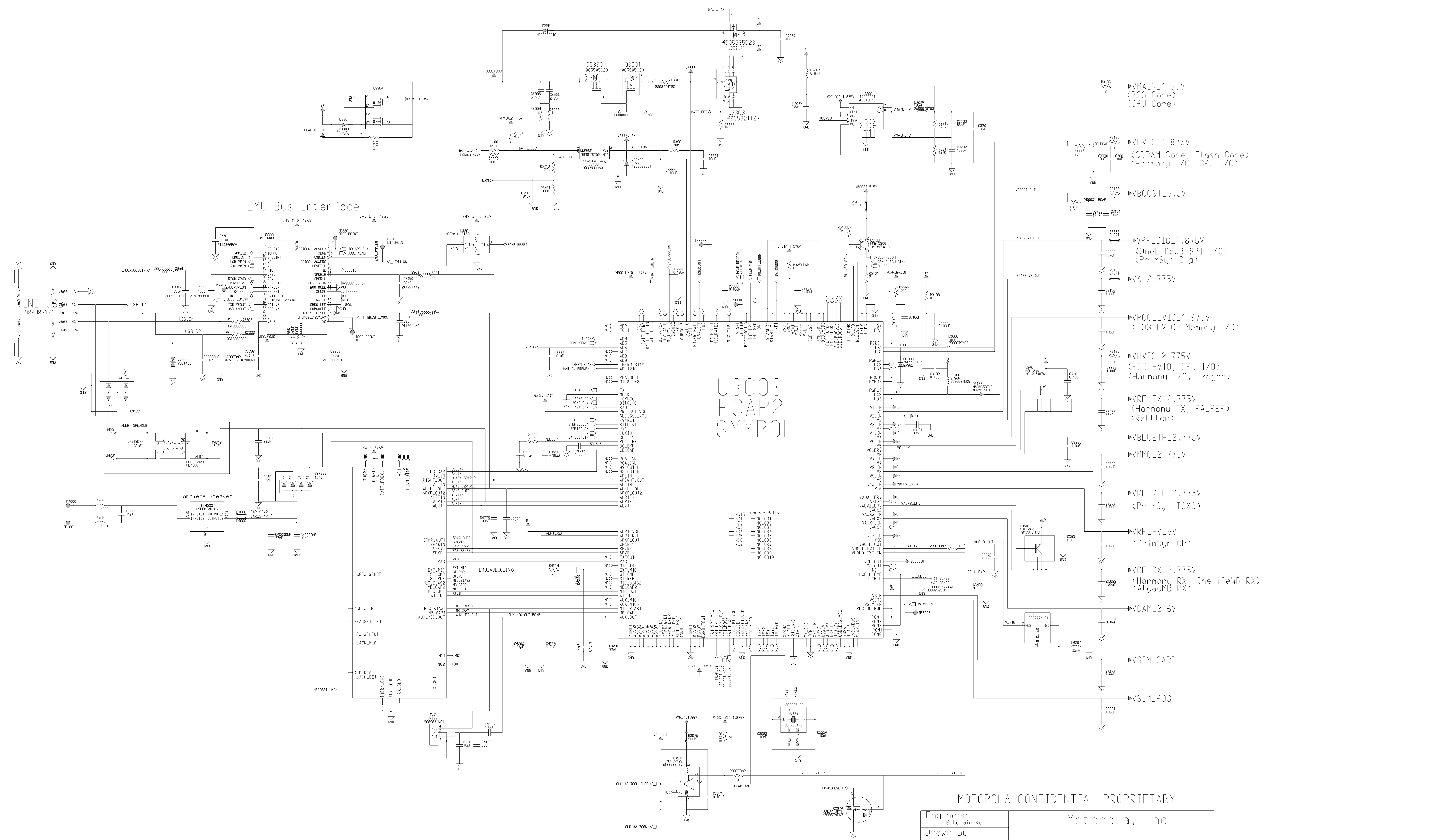


A

B

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D

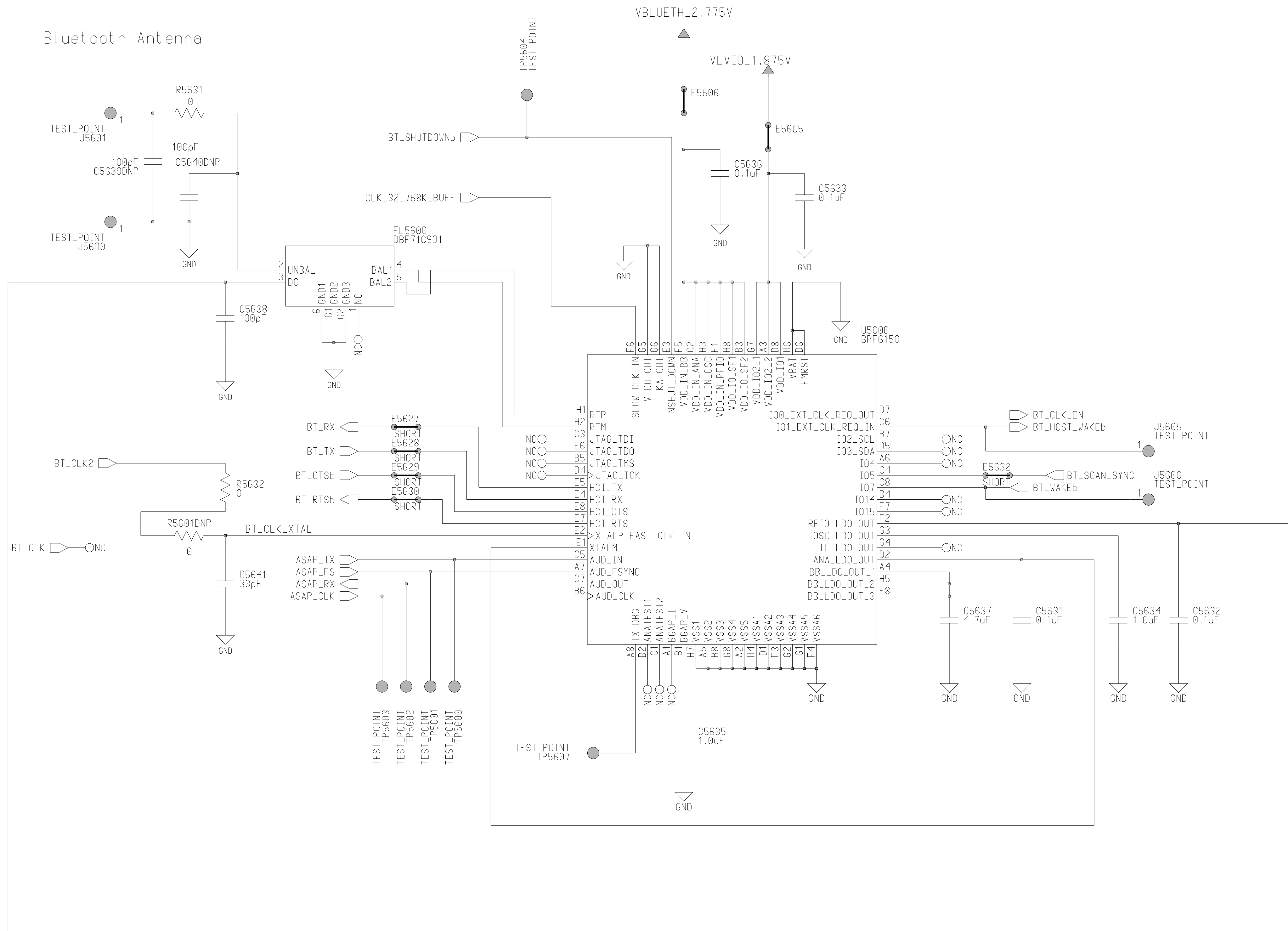


U3000  
PCAP2  
SYMBOL

MOTOROLA CONFIDENTIAL PROPRIETARY

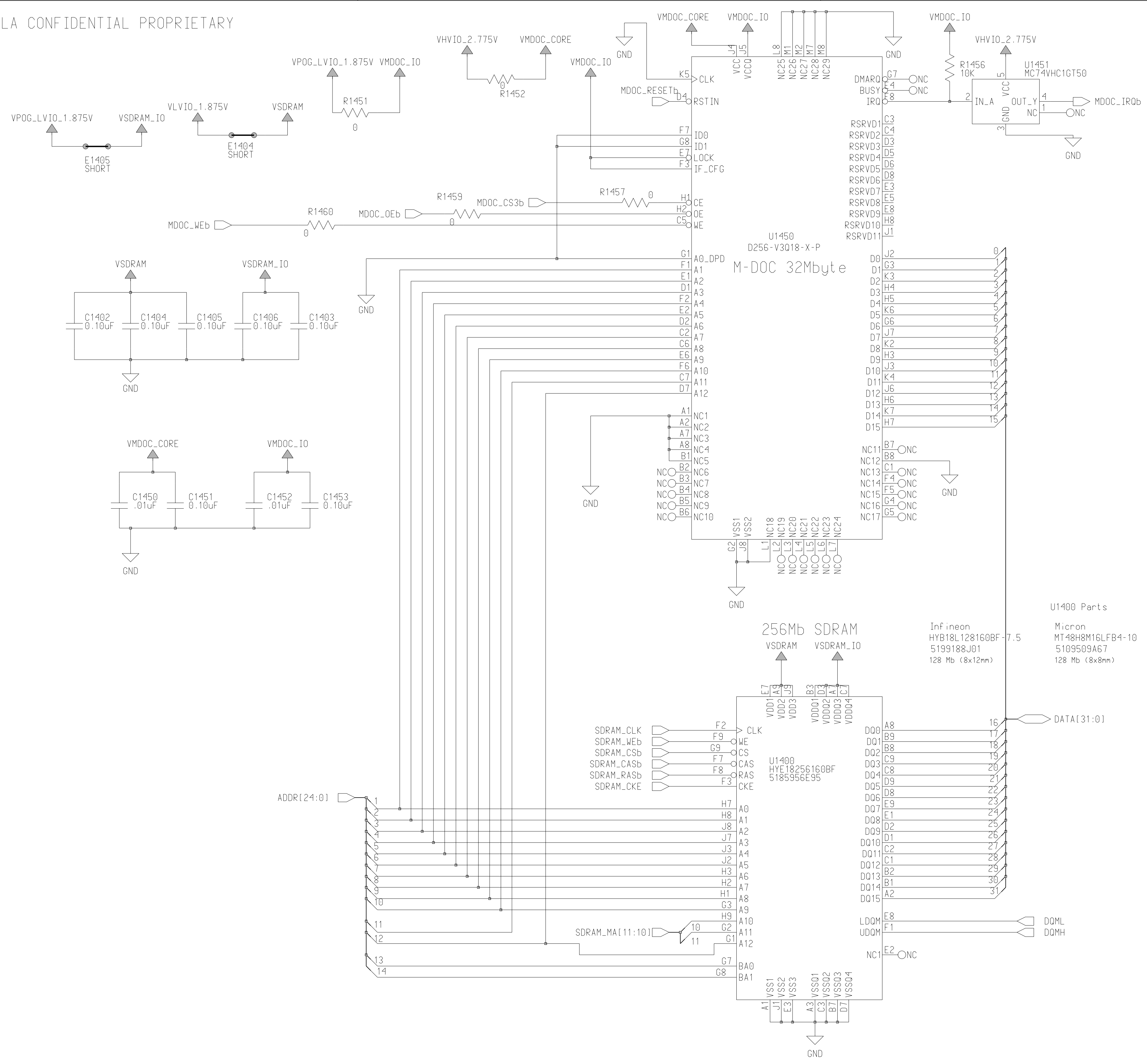
Engineer Bokcha in Koh	Motorola, Inc.	
Drawn by	Size D	
R&D CHK	TITLE SERPENS	
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MFG CTRL CHK	REV	Drawing Number
QA CHK	Sheet	of

Bluetooth Antenna



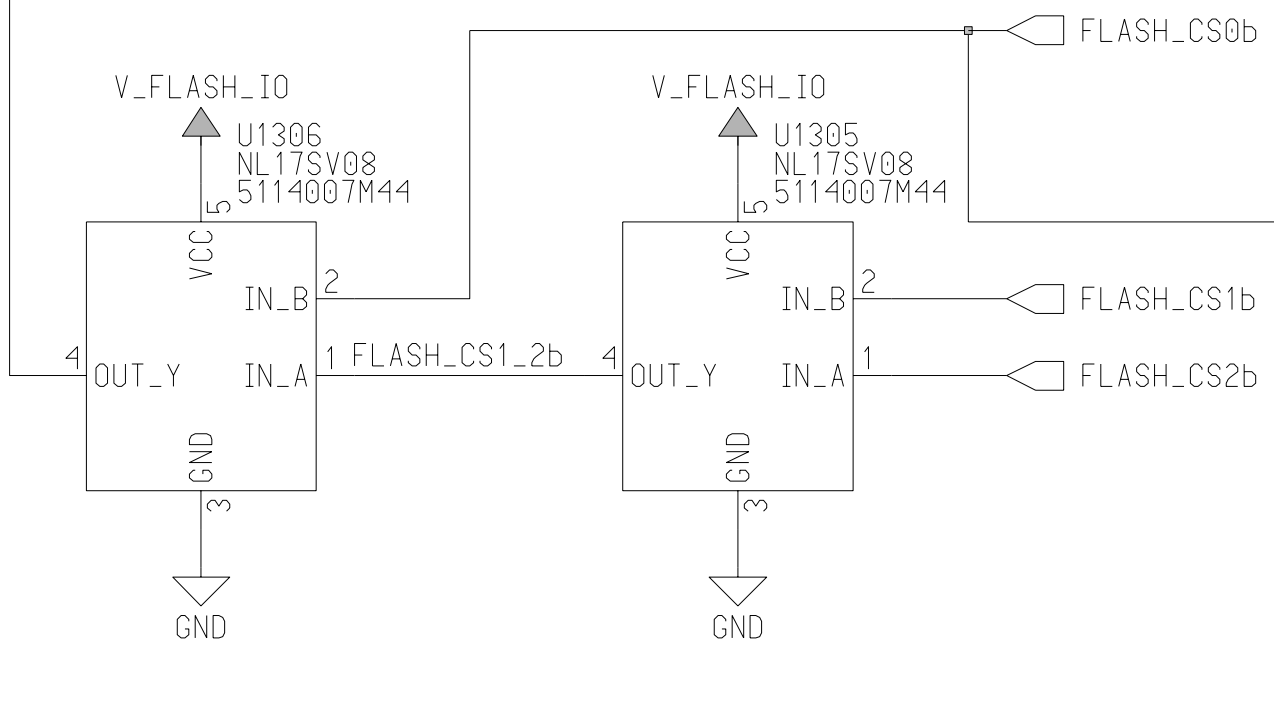
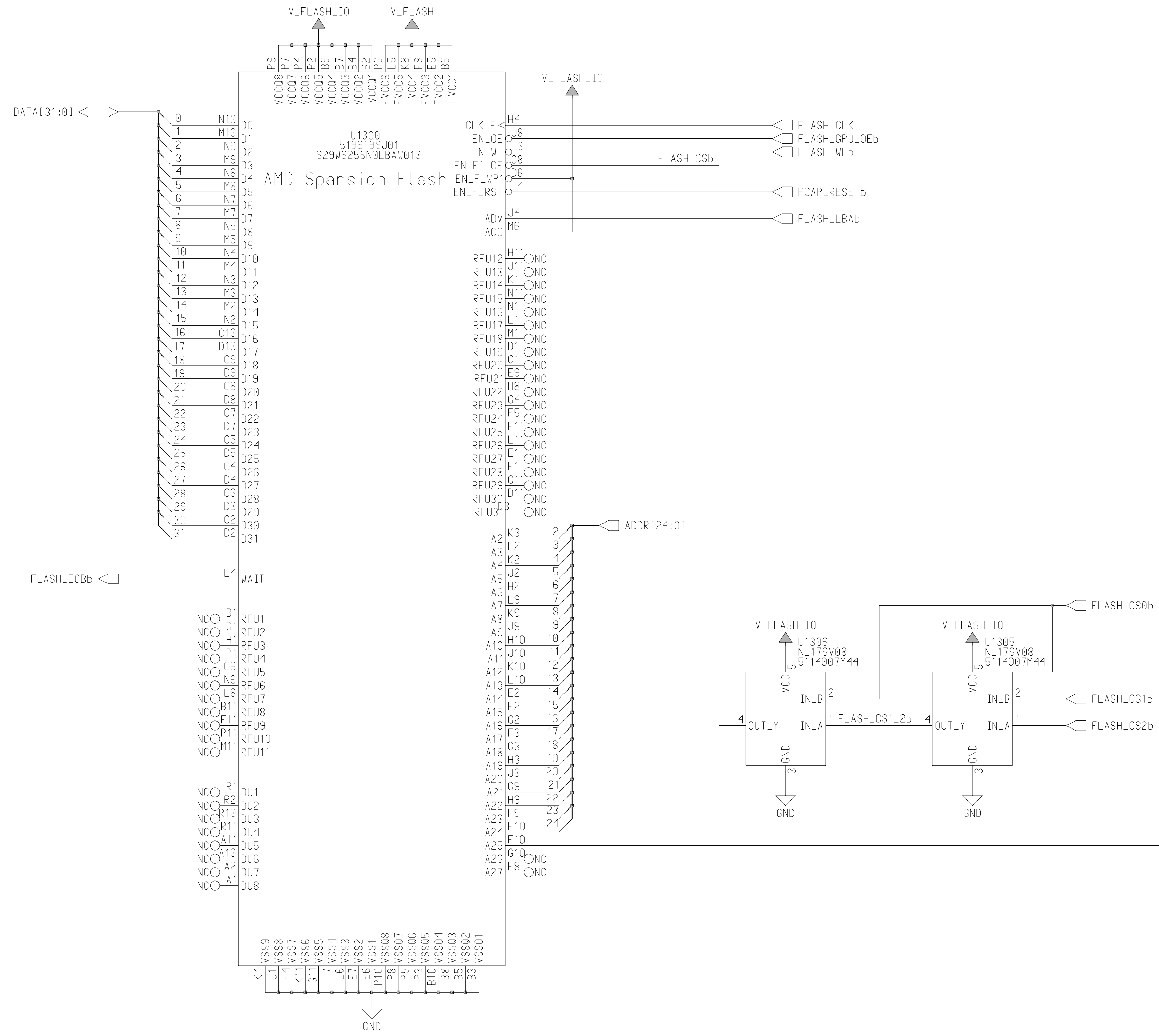
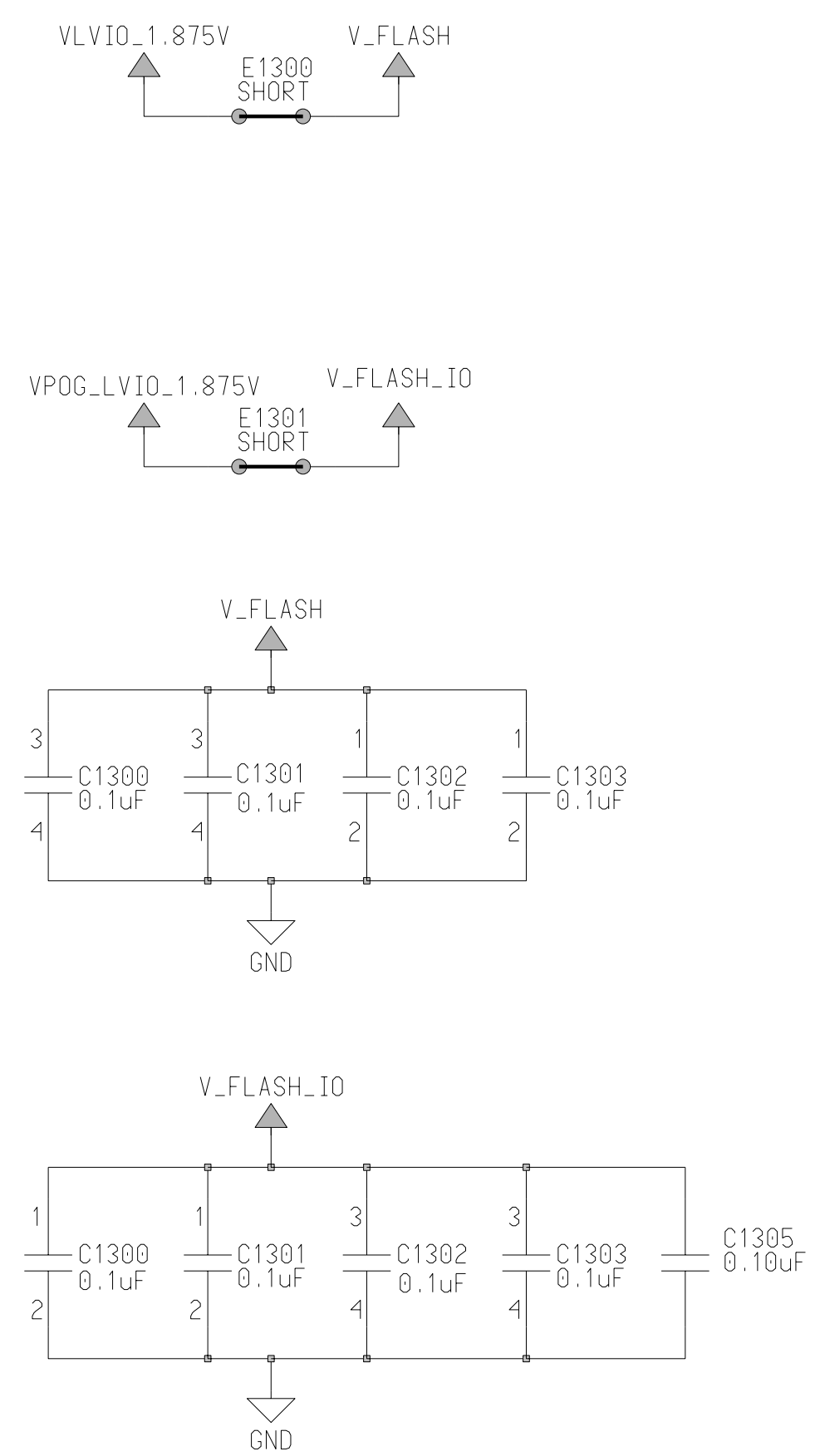
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MFG CTRL CHK:	REV: P9	Drawing Number: 8488453Y07	Of: 1
QA CHK:	Date:	Time:	
Changed by:			

A  
B  
C  
D



- U1400 Parts
- Infineon HYB18L128160BF-7.5 5199188J01 128 Mb (8x12mm)
  - Micron MT48H8M16LFB4-10 5109509A67 128 Mb (8x8mm)
  - Samsung K4M28163PF-BF1L Placeholder 5109509A67 128 Mb (8x8mm)
  - Hynix HY532B6DLFP-SE Placeholder 5109509A67 128 Mb (8x8mm)

Engineer:	MOTOROLA INC.		
Drawn by:			
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QA CHK:	Date:	Time:	
Changed by:			



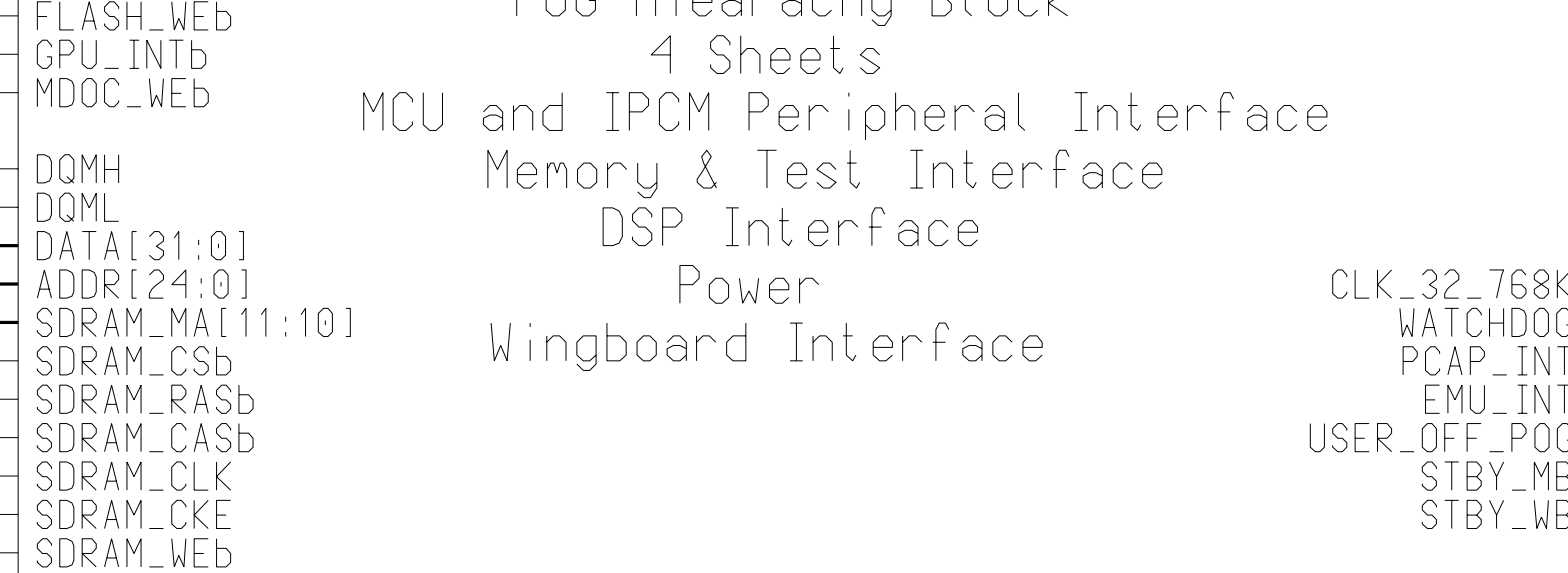
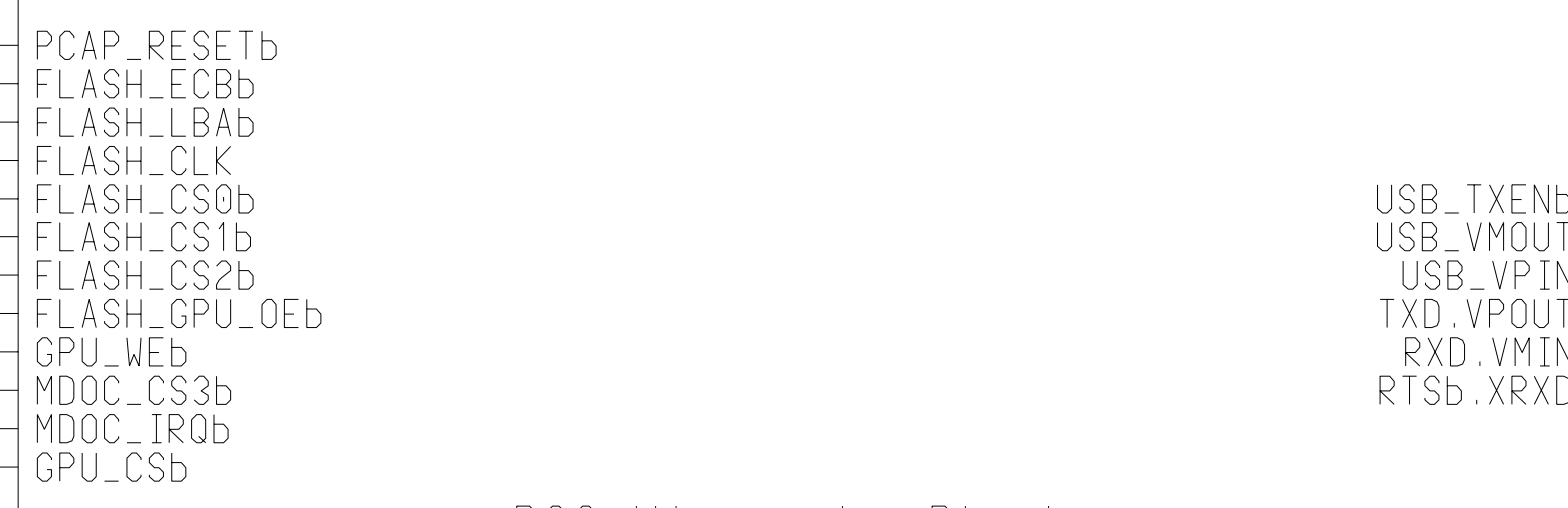
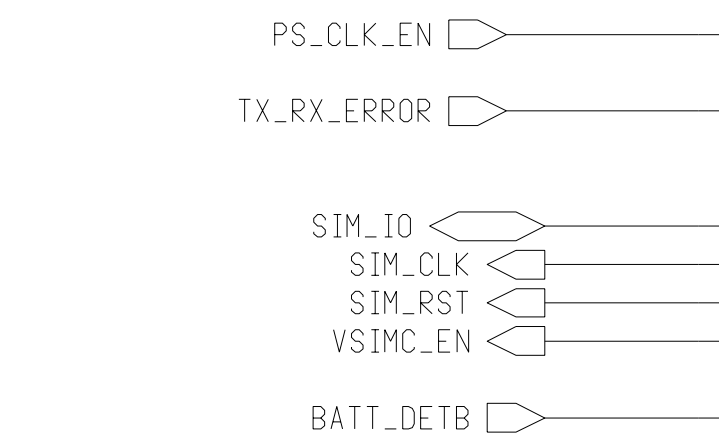
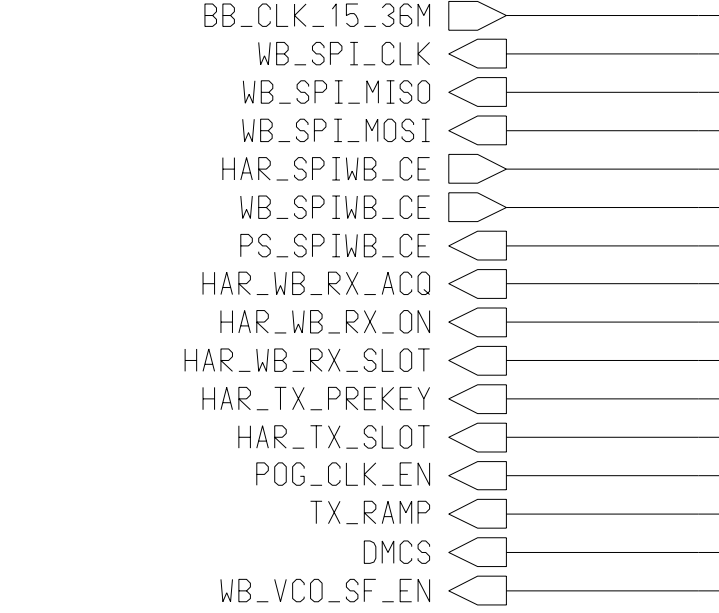
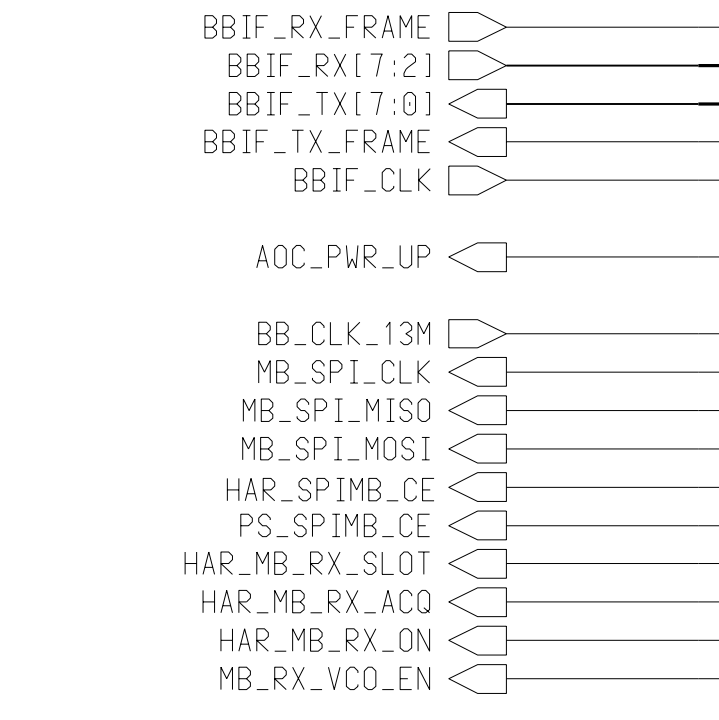
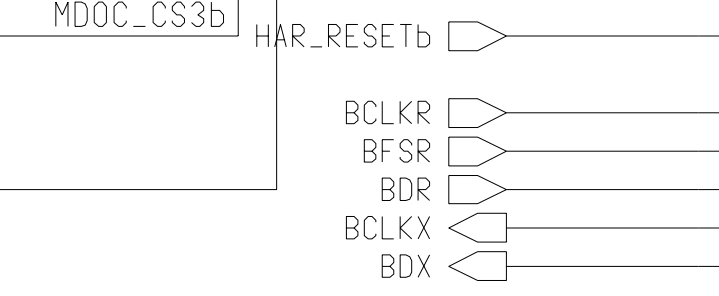
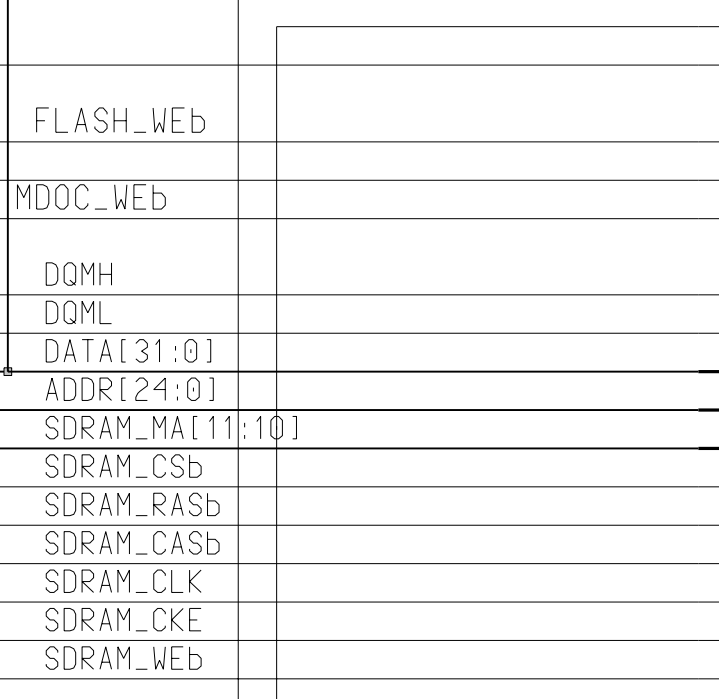
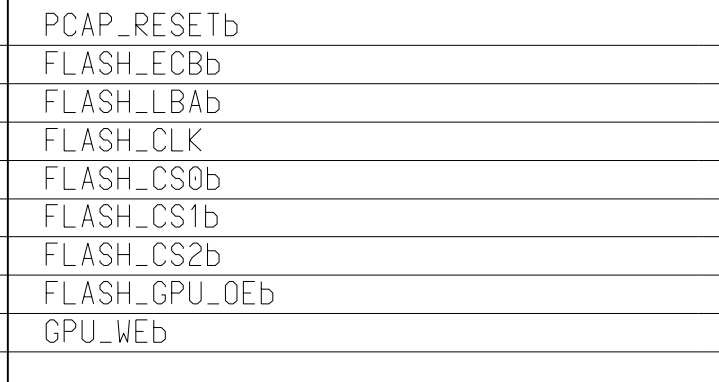
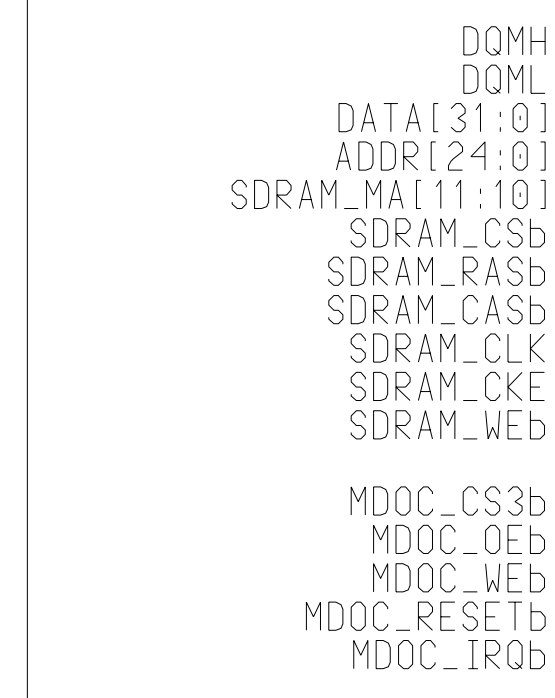
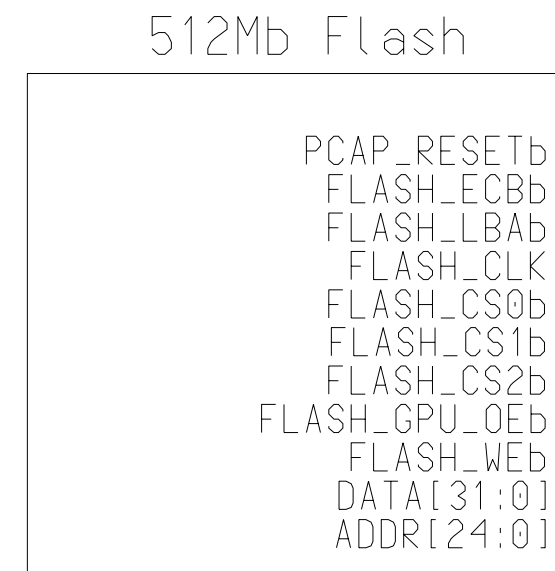
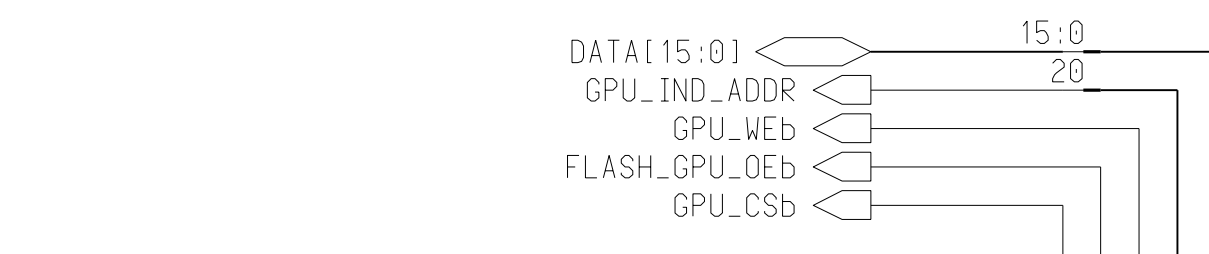
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Drawn by:	Size:		
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QA CHK:	Date:	Time:	
Changed by:			

A

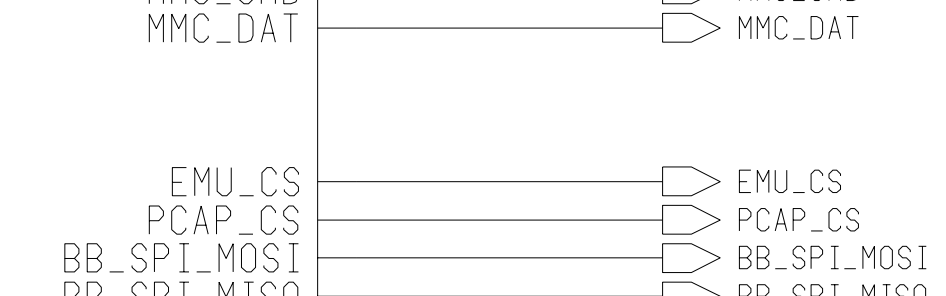
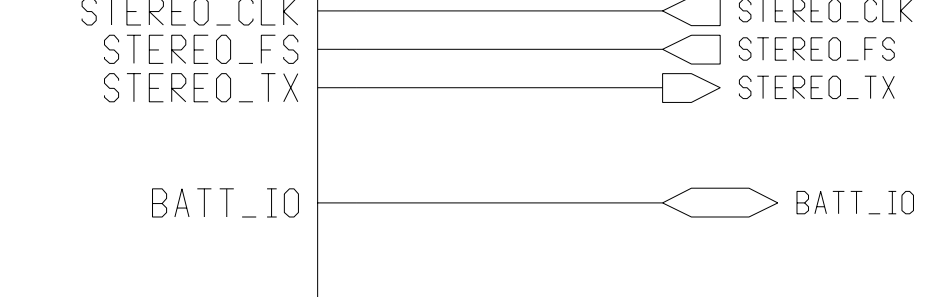
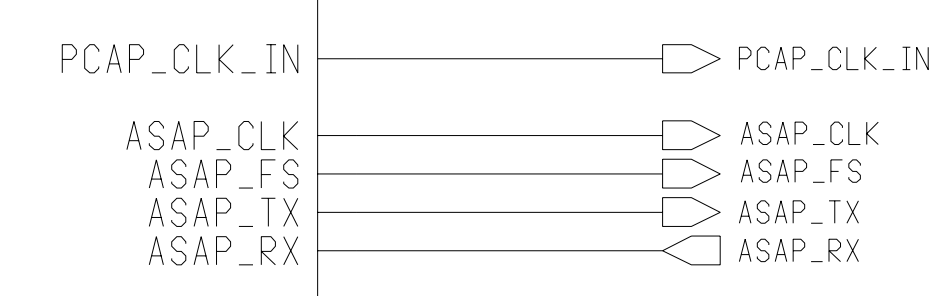
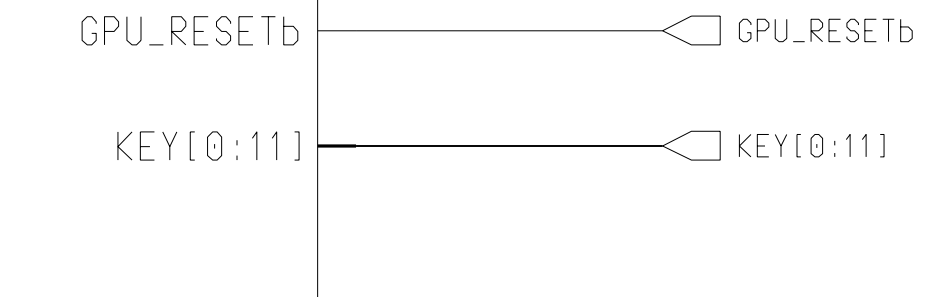
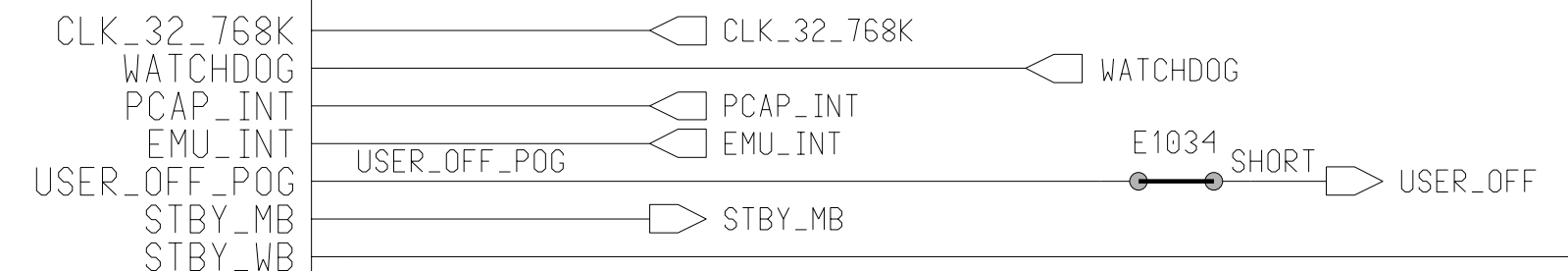
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C

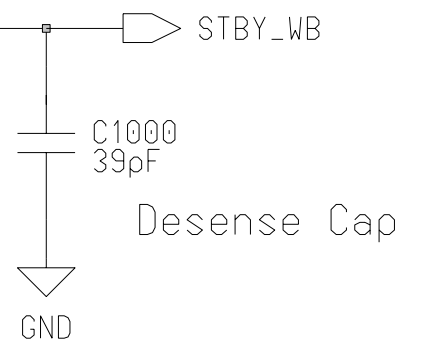
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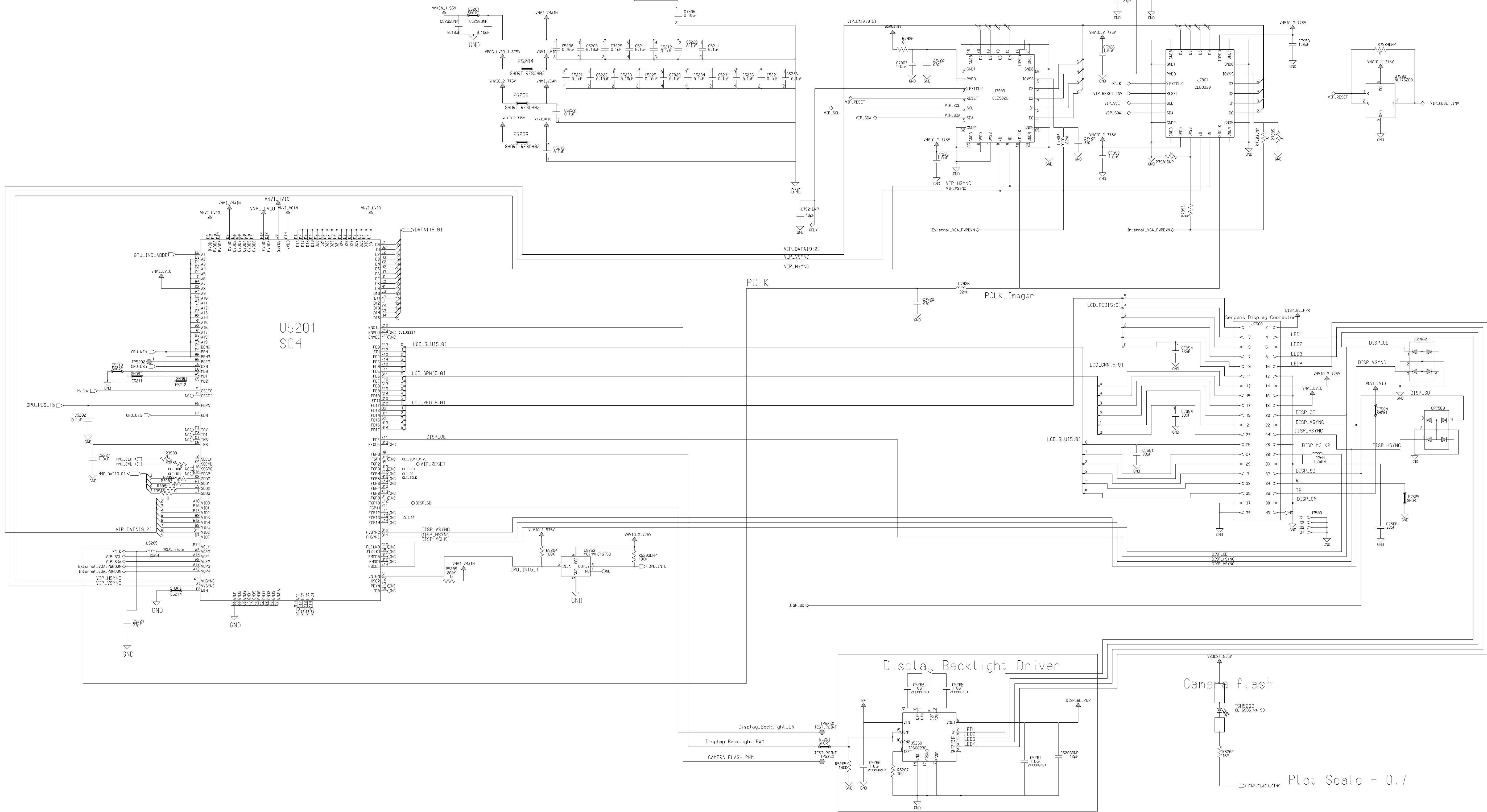
POG Hierarchy Block  
4 Sheets  
MCU and IPCM Peripheral Interface  
Memory & Test Interface  
DSP Interface  
Power  
Wingboard Interface



Production	Development
U1019DNP	U1019
R1038	R1038DNP
R1032DNP	R1032
U1020DNP	U1020

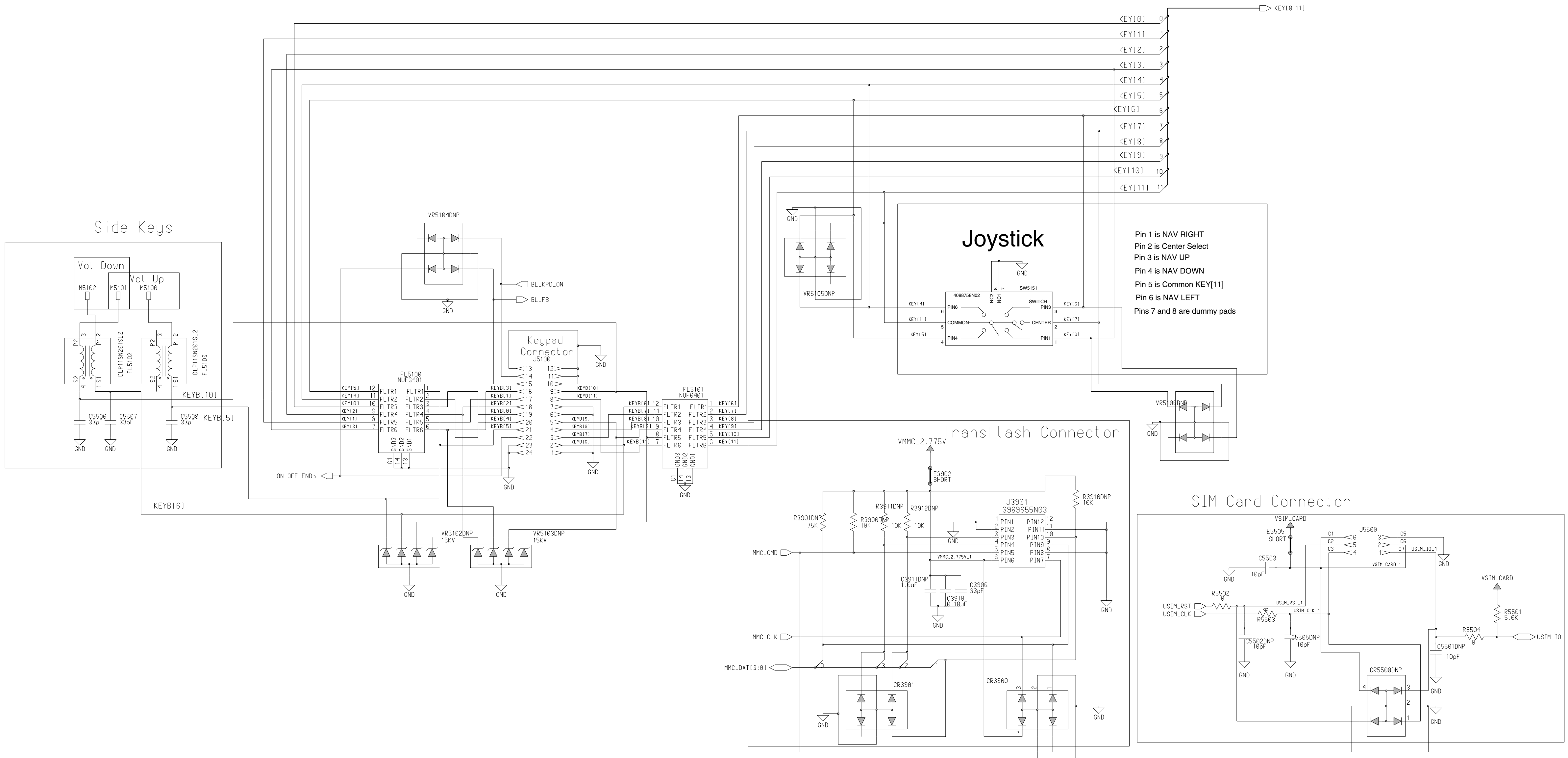




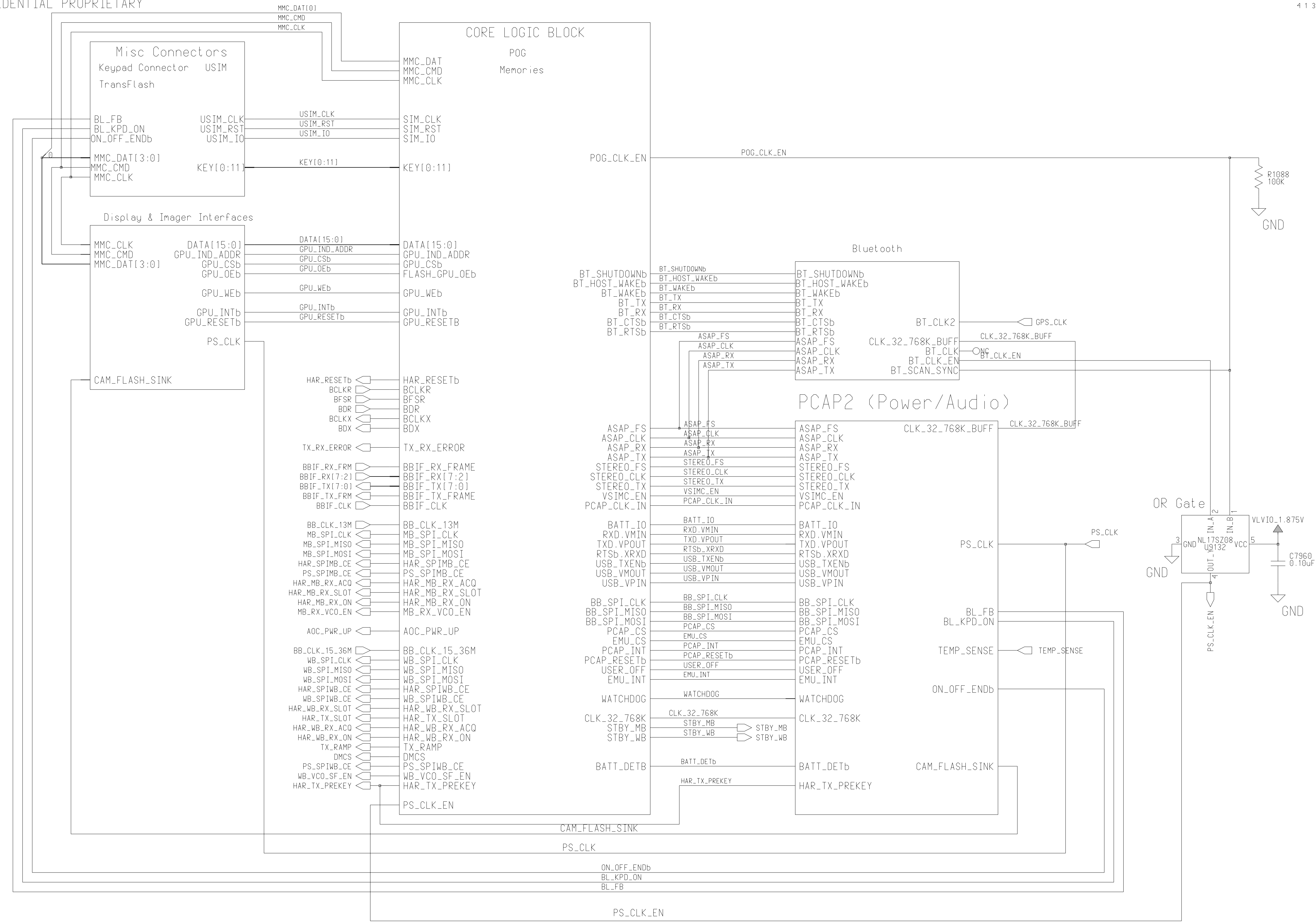


Engineer	Motorola, Inc.		
Drawn by			
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DOC CTRL CHK	REV	Date: 12/22/04	Sheet of
MFG CTRL CHK			
QA CHK			

Plot Scale = 0.7



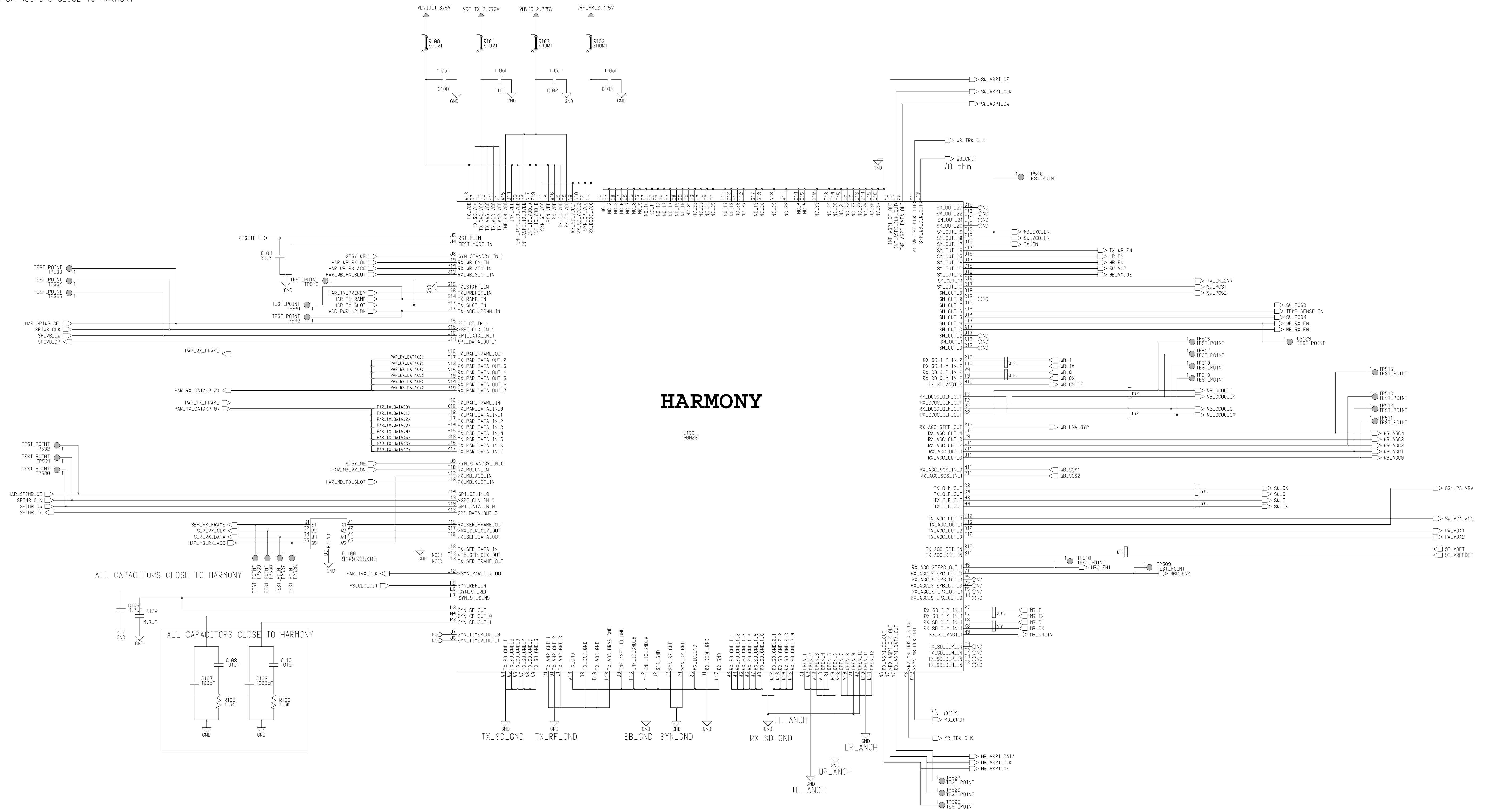
Engineer: Rajesh Verma			
Drawn by: Rajesh Verma			
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DOC CTRL CHK:	REV:	Drawing Number:	Page: OF:
MFG CTRL CHK:	QA CHK: #16748		
Changed by:	Date: Tuesday, December 21, 2004		Time: 3:27:32 pm



TP placeholders for pins with NC

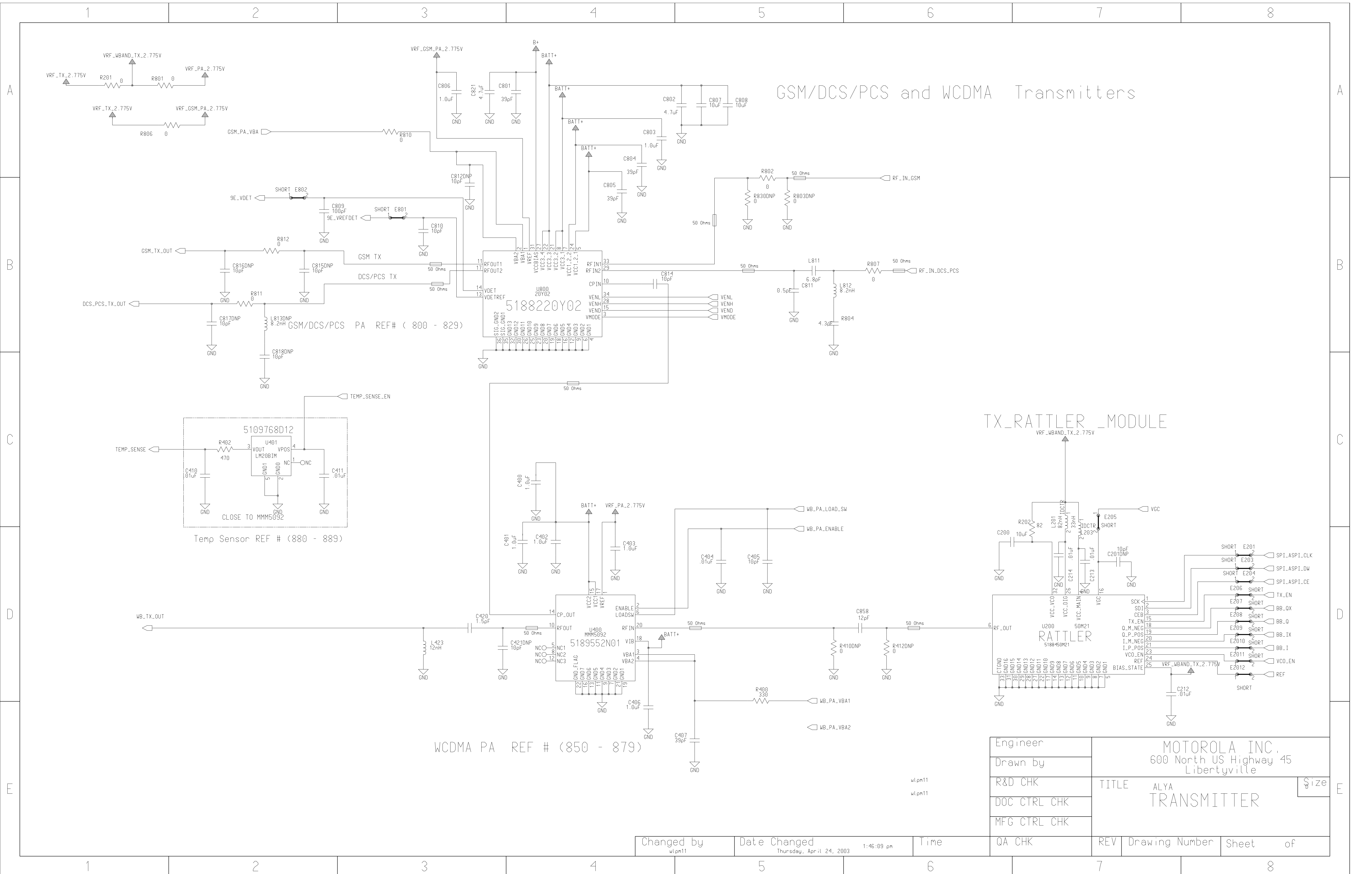
SYNTHESIS CAPACITORS CLOSE TO HARMONY

SUPPLIES INTERCONNECTIONS FAR FROM HARMONY



Engineer Chris Leyh	MOTOROLA INC. 600 North US Highway 45 Libertyville		
Drawn by ccl003			
R&D CHK	TITLE	Size D	
DOC CTRL CHK	HARMONY: ALYA		
MFG CTRL CHK	REV	Drawing Number	Sheet of
QA CHK	1		

Changed by ccl003 7 Date Changed Thursday, April 17, 2003 Time 12:06:46 pm

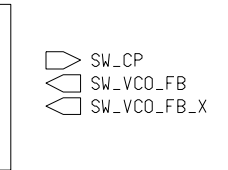


Engineer	<b>MOTOROLA INC.</b> 600 North US Highway 45 Libertyville		
Drawn by			
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DOC CTRL CHK	<b>TRANSMITTER</b>		
MFG CTRL CHK	REV	Drawing Number	Sheet of
QA CHK			

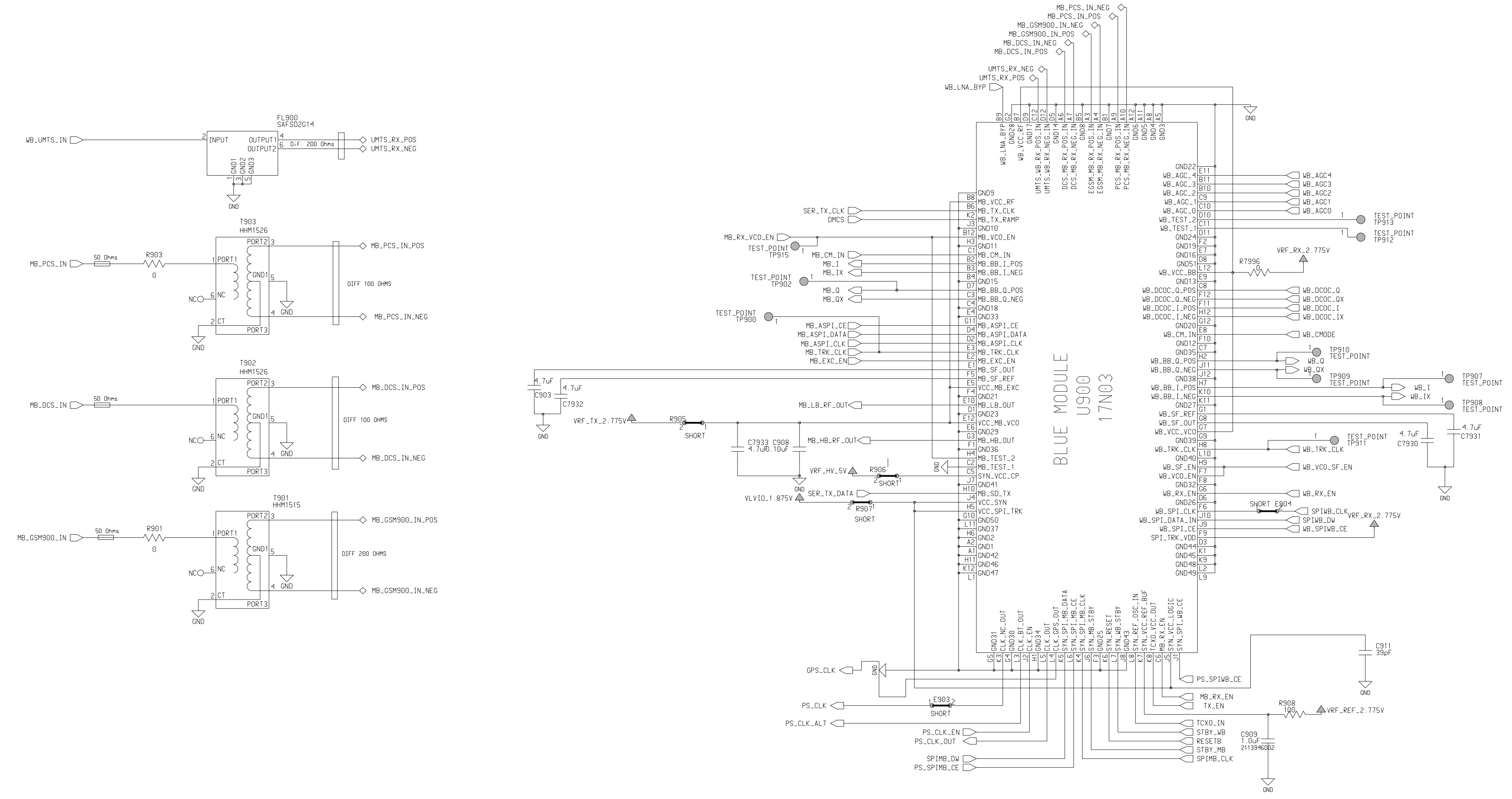
Changed by: wlpn11  
 Date Changed: Thursday, April 24, 2003 1:46:09 pm  
 Time

# ORG. Blue Module VERSION 2.0

DO NOT DELETE



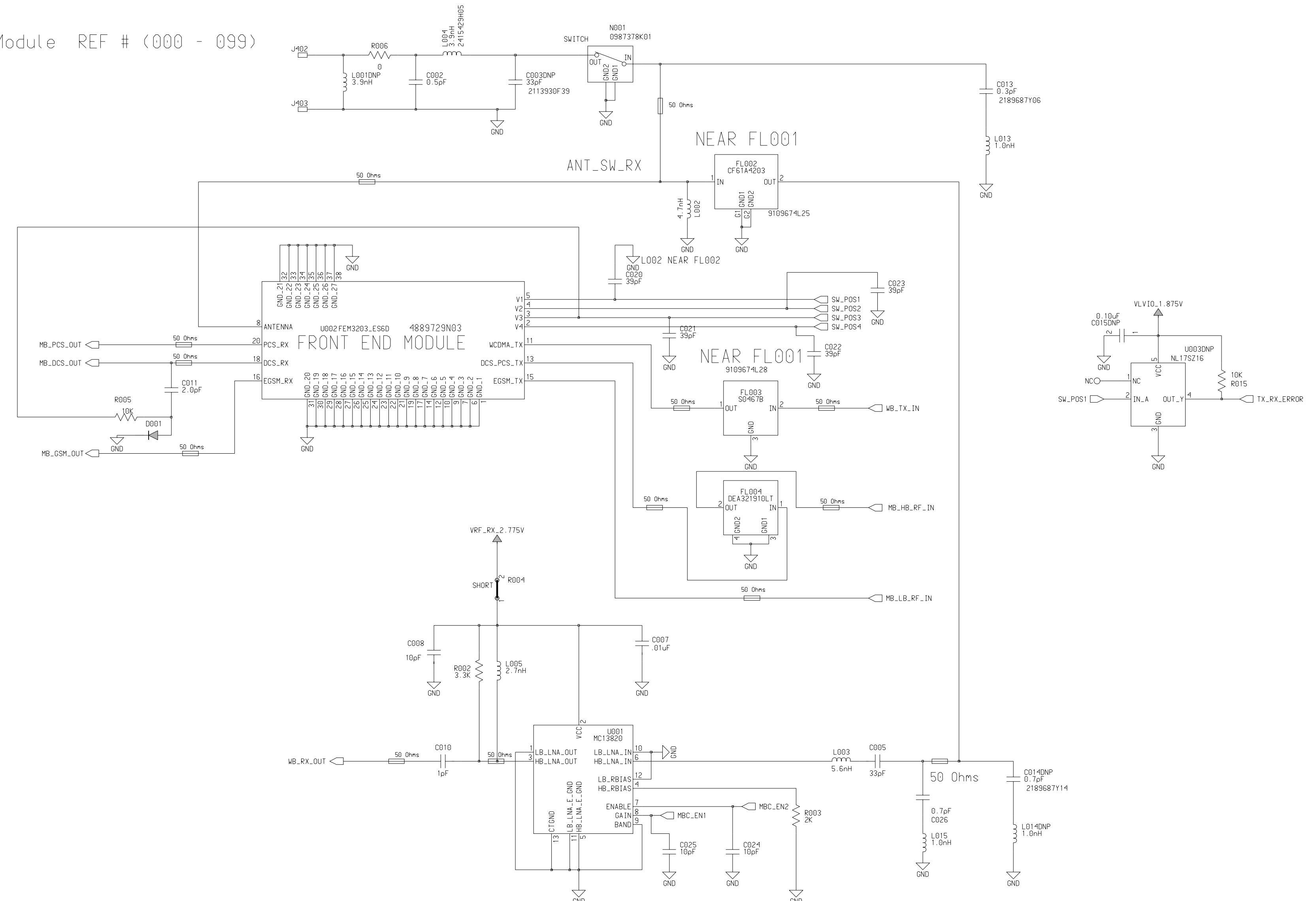
TPs for test\_1 & test\_2



Engineer Chris Leyh	COMPANY NAME		
Drawn by Chris Leyh	Address City		
R&D CHK	TITLE	Size D	
DOC CTRL CHK	Rx/Syn: ALYA		
MFG CTRL CHK			
QA CHK	REV 1.0	Drawing Number	Sheet of

Changed by ccl003 Date Changed Thursday, April 10, 2003 Time 9:50:27 am

Front End Module REF # (000 - 099)

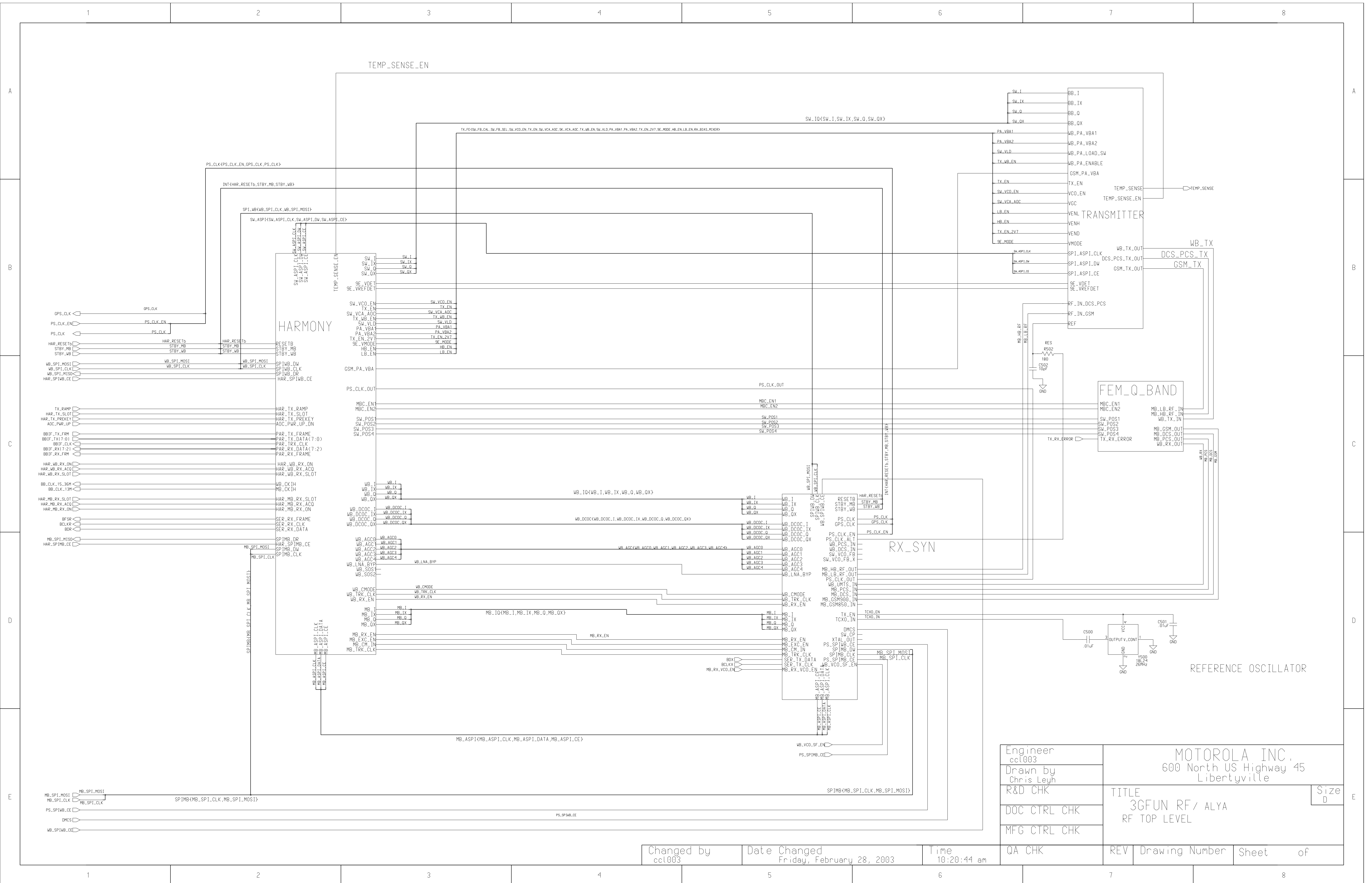


Engineer Chris Leyh	MOTOROLA INC. 600 North US Highway 45 Libertyville			
Drawn by ccl003				
R&D CHK	TITLE	Size D		
DOC CTRL CHK	Quad Band Front End Module ALYA			
MFG CTRL CHK	QA CHK	REV Rev 1	Drawing Number	Sheet of

Changed by  
ccl003

Date Changed  
Thursday, April 17, 2003

Time  
8:25:56 am

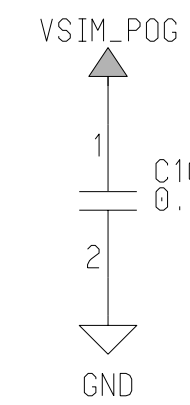
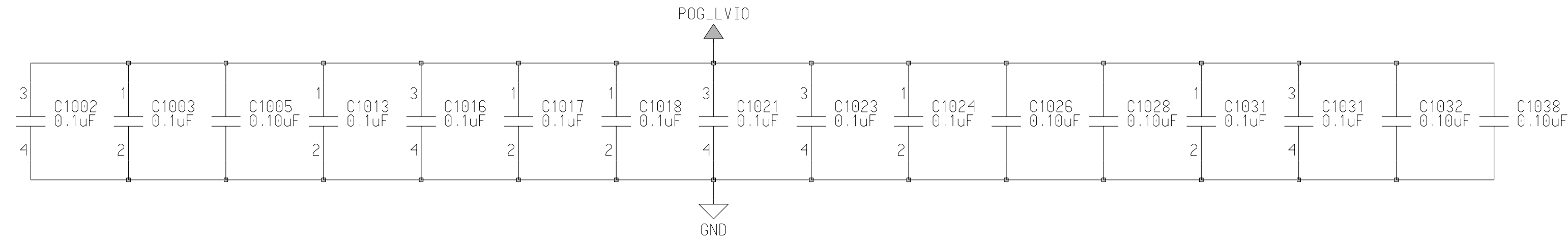
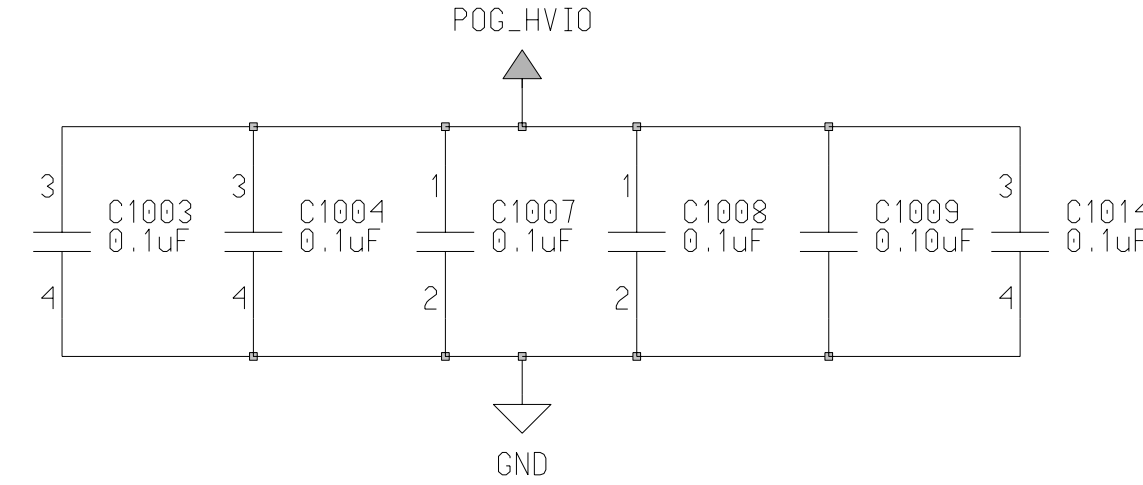
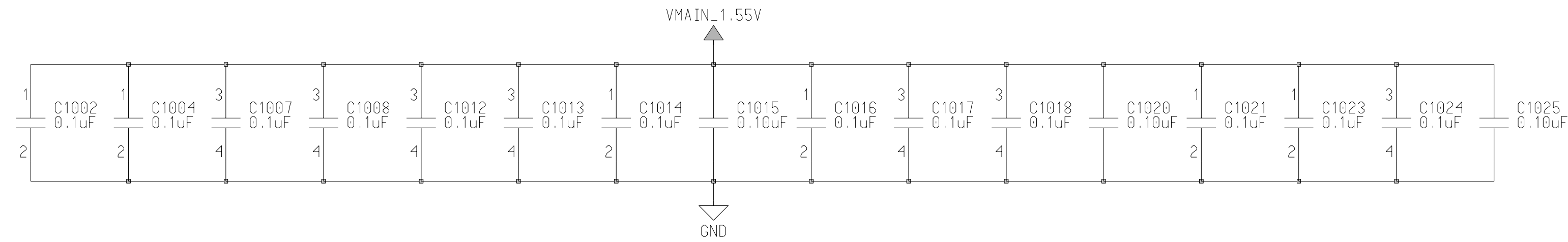
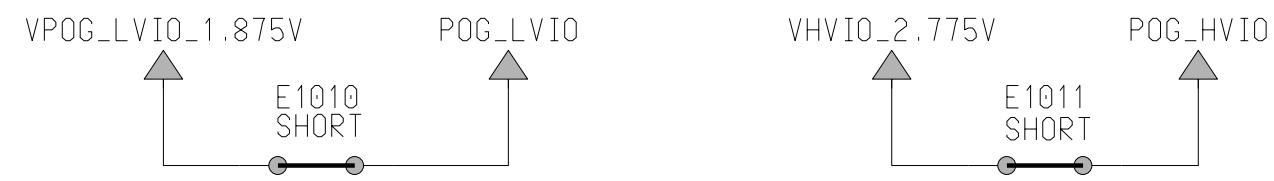


Engineer ccl003	MOTOROLA INC. 600 North US Highway 45 Libertyville		
Drawn by Chris Leyh			
R&D CHK	TITLE 3GFUN RF/ ALYA RF TOP LEVEL	Size D	
DOC CTRL CHK	REV	Drawing Number	
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QA CHK	REV	Drawing Number	

Changed by ccl003 Date Changed Friday, February 28, 2003 Time 10:20:44 am

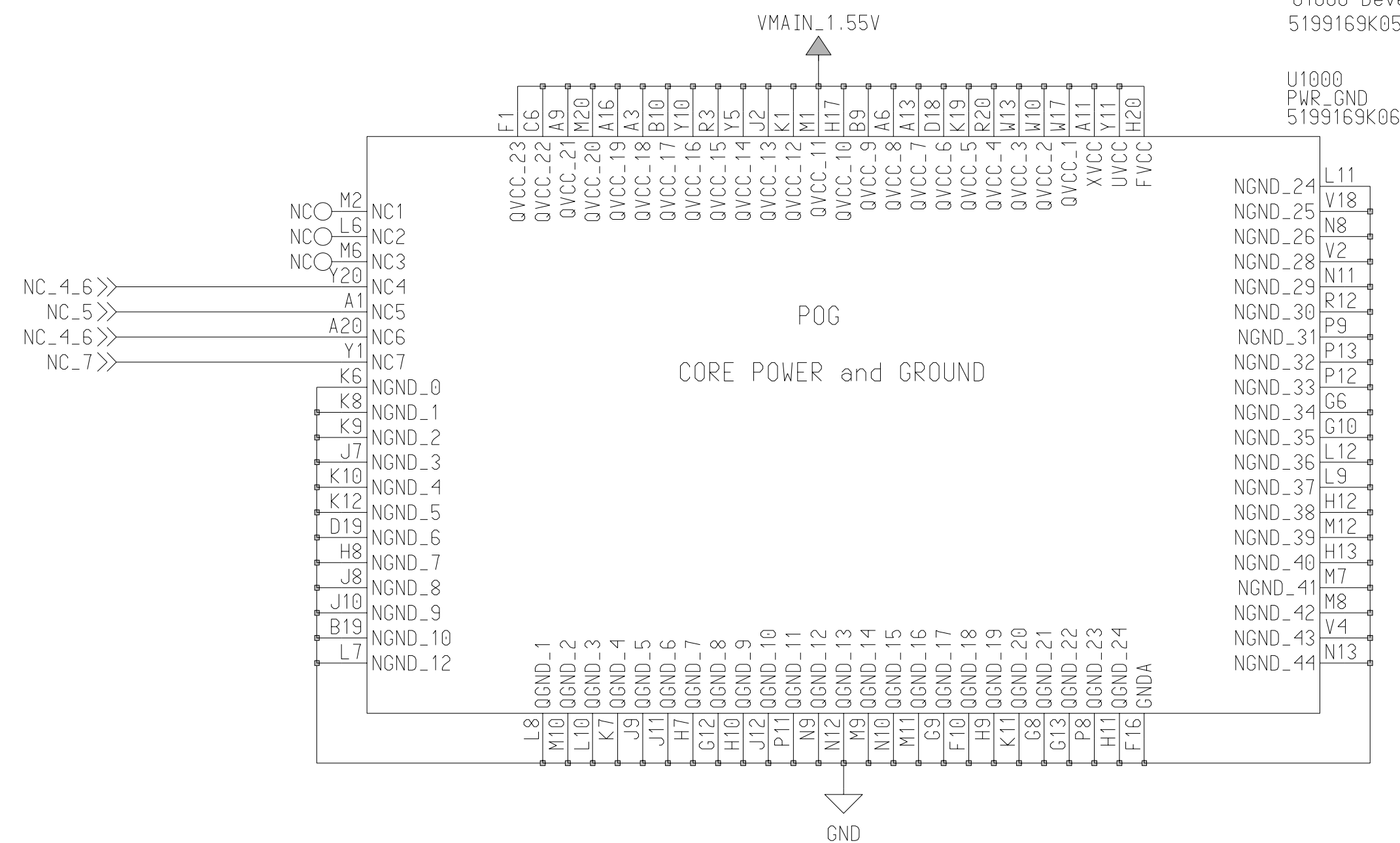


Engineer:	MOTOROLA INC.		
Drawn by:			
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MFG CTRL CHK:	Power Interface		
QA CHK:	REV:	Drawing Number:	Page: Of:
Changed by:	Date:	Time:	



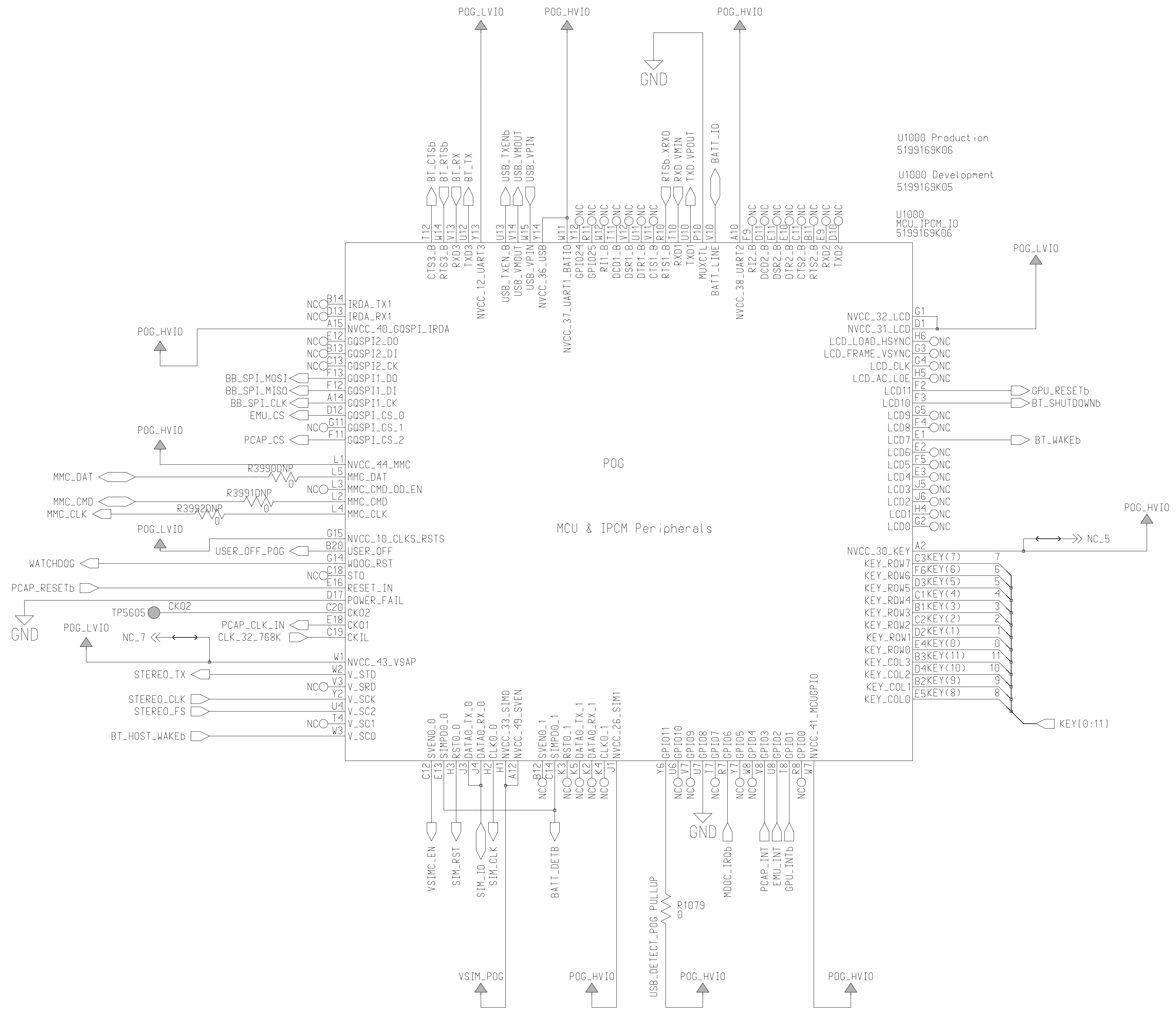
U1000 Production  
5199169K06

U1000 Development  
5199169K05



U1000  
PWR\_GND  
5199169K06

Engineer:	MOTOROLA INC.		
Drawn by:			
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DOC CTRL CHK:	Peripheral Interface		
MFG CTRL CHK:	REV: P9	Drawing Number:	Page: Of:
QA CHK:	Date:	Time:	
Changed by:			



U1000 Production  
5199169K06

U1000 Development  
5199169K05

U1000  
MCU\_IPCM\_IO  
5199169K06

POG


MCU & IPCM Peripherals

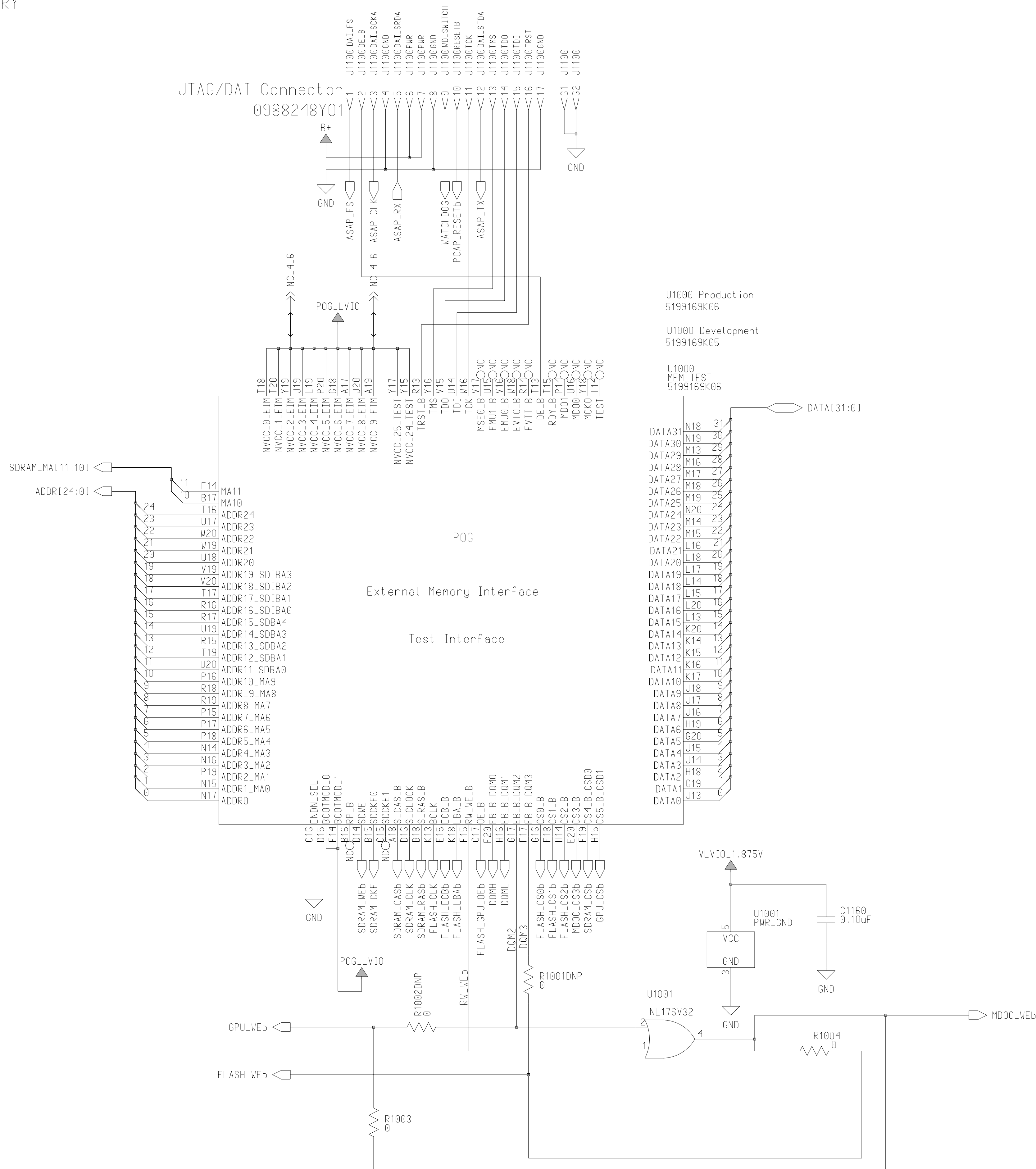
A

B

C

D

Engineer:			
Drawn by:			
R&D CHK:	TITLE: SERPENS POG	Size:	
DOC CTRL CHK:	Memory & Test Interface		
MFG CTRL CHK:	REV:	Drawing Number:	Page:
QA CHK:	Date:	Of:	Time:
Changed by:			

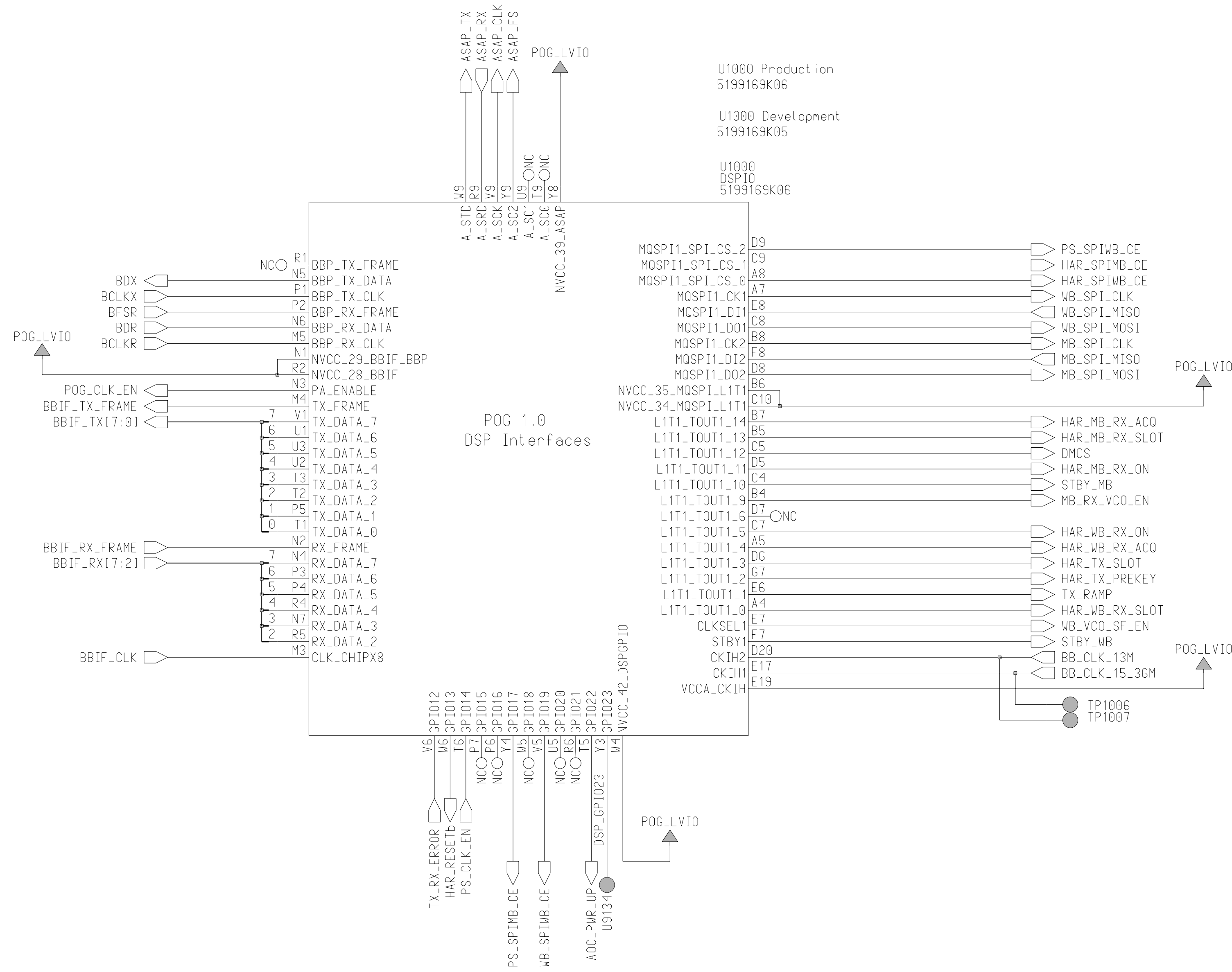


U1000 Production  
5199169K06

U1000 Development  
5199169K05

U1000  
MEM\_TEST  
5199169K06

Engineer:	MOTOROLA INC.		
Drawn by:			
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DOC CTRL CHK:	POG		
MFG CTRL CHK:	DSP Interface		
QA CHK:	REV: P9	Drawing Number: 8488453Y07	Page: Of:
Changed by:	Date:	Time:	



U1000 Production  
5199169K06

U1000 Development  
5199169K05

U1000  
DSP10  
5199169K06

POG 1.0  
DSP Interfaces

TP1006  
TP1007