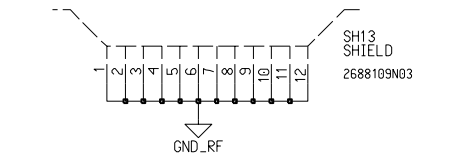
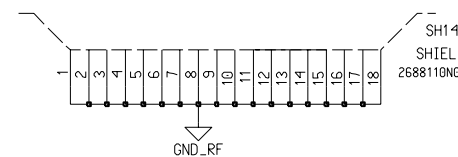
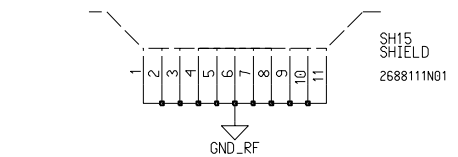
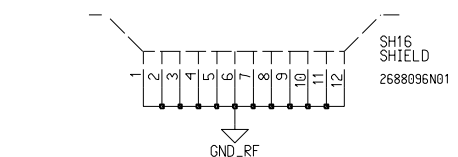
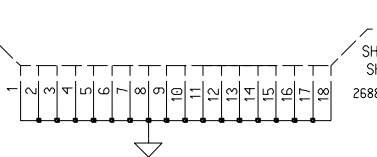
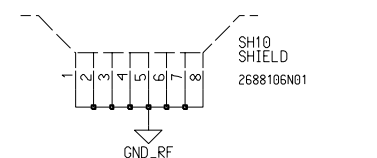
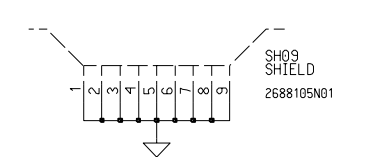
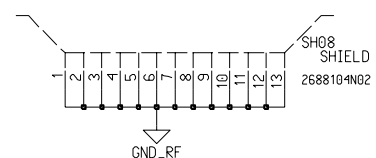
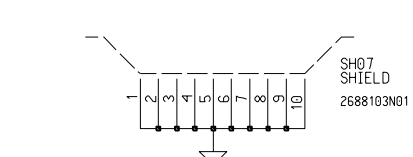
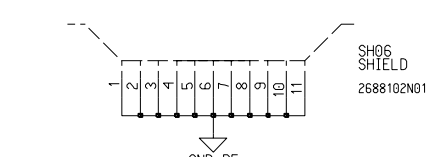
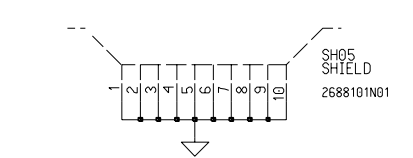
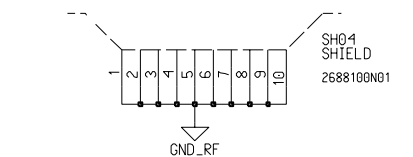
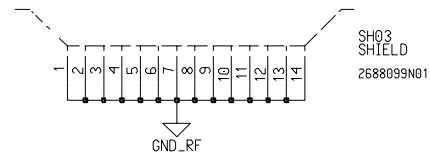
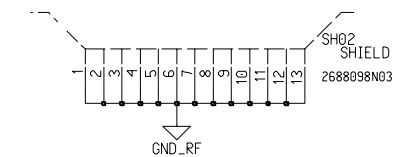
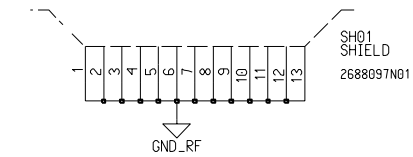


MOTOROLA CONFIDENTIAL PROPRIETARY

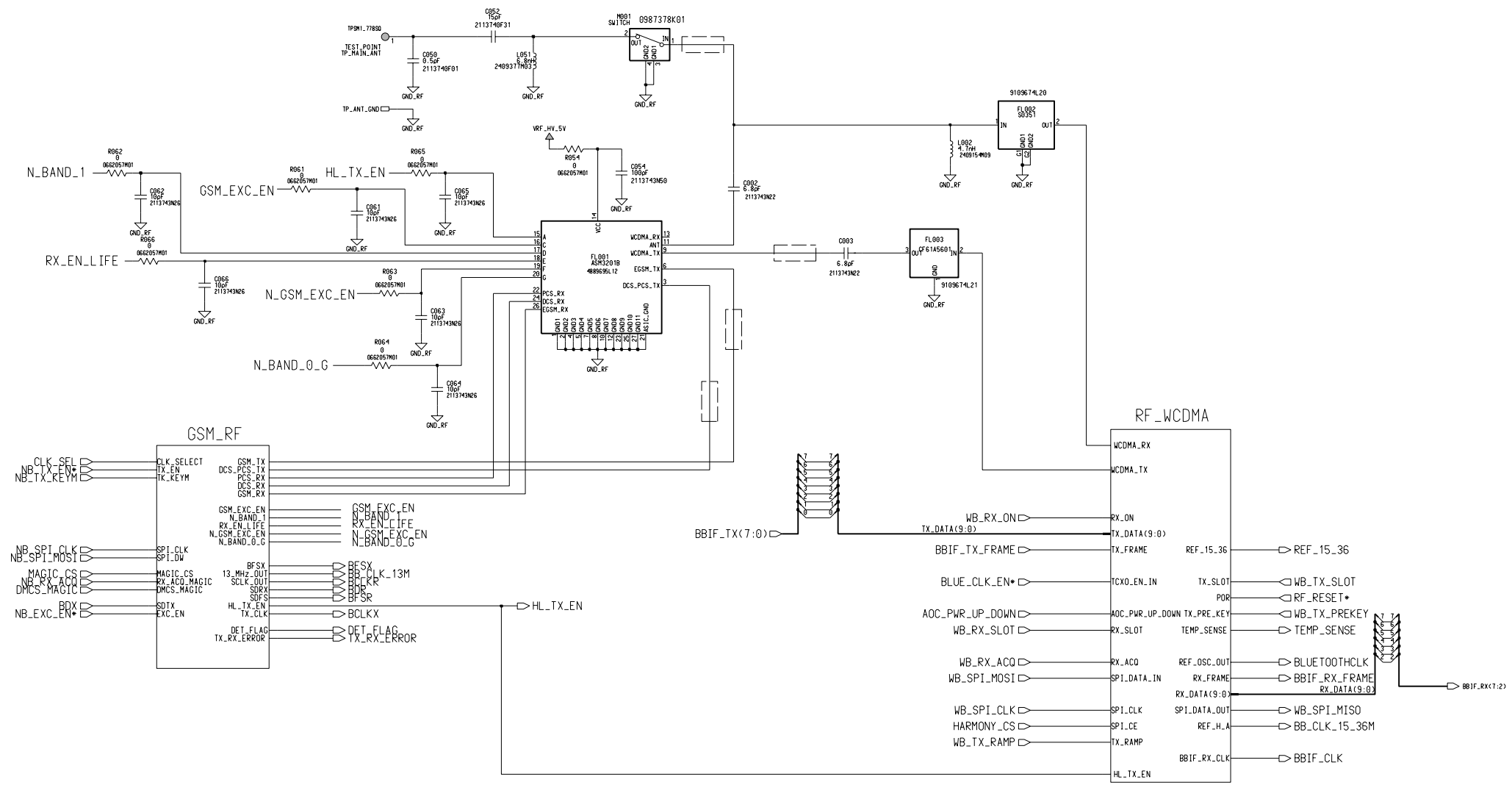
Baseband

RF

BB_CLK_15_36M	BB_CLK_15_36M	BB_CLK_15_36M
TEMP_SENSE	TEMP_SENSE	TEMP_SENSE
BBIF_RX_FRM	BBIF_RX_FRAME	BBIF_RX_FRAME
BBIF_TX_FRM	BBIF_TX_FRAME	BBIF_TX_FRAME
BBIF_CLK	BBIF_CLK	BBIF_CLK
BBIF_TX(7:0)	BBIF_TX(7:0)	BBIF_TX(7:0)
BBIF_RX(7:2)	BBIF_RX(5:0)	BBIF_RX(7:2)
TX_RX_ERROR	TX_RX_ERROR	TX_RX_ERROR
HARMONY_CS	HARMONY_CS	HARMONY_CS
WB_SPI_MISO	WB_SPI_MISO	WB_SPI_MISO
WB_SPI_CLK	WB_SPI_CLK	WB_SPI_CLK
WB_SPI_MOSI	WB_SPI_MOSI	WB_SPI_MOSI
RF_RESET*	RF_RESET*	RF_RESET*
BLUE_CLK_EN*	BLUE_CLK_EN*	BLUE_CLK_EN*
WB_RX_ON	WB_RX_ON	WB_RX_ON
WB_TX_RAMP	WB_TX_RAMP	WB_TX_RAMP
WB_RX_ACQ	WB_RX_ACQ	WB_RX_ACQ
WB_TX_SLOT	WB_TX_SLOT	WB_TX_SLOT
WB_RX_SLOT	WB_RX_SLOT	WB_RX_SLOT
AOC_PWR_UP	AOC_PWR_UP_DOWN	AOC_PWR_UP_DOWN
WB_TX_PREKEY	WB_TX_PREKEY	WB_TX_PREKEY
NB_RX_ACQ	NB_RX_ACQ	NB_RX_ACQ
NB_TX_KEYM	NB_TX_KEYM	NB_TX_KEYM
NB_TX_EN*	NB_TX_EN*	NB_TX_EN*
NB_EXC_EN*	NB_EXC_EN*	NB_EXC_EN*
DET_FLAG	DET_FLAG	DET_FLAG
CLK_SEL	CLK_SEL	CLK_SEL
MAGIC_CS	MAGIC_CS	MAGIC_CS
NB_SPI_MOSI	NB_SPI_MOSI	NB_SPI_MOSI
NB_SPI_CLK	NB_SPI_CLK	NB_SPI_CLK
REF_15_36	REF_15_36 (For GPS)	REF_15_36
DMCS_MAGIC	DMCS_MAGIC	DMCS_MAGIC
BDX	BDX	BDX
BFSR	BFSR	BFSR
BCLKX	BCLKX	BCLKX
BCLKR	BCLKR	BCLKR
BDR	BDR	BDR
BFSX	BFSX	BFSX
BB_CLK_13M	BB_CLK_13M	BB_CLK_13M
BLUETOOTHCLK	BLUETOOTHCLK	BLUETOOTHCLK
HL_TX_EN	HL_TX_EN	HL_TX_EN

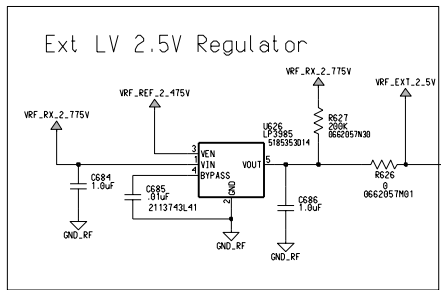


Engineer: Ed Naddeo			
Drawn by: Ed Naddeo			
R&D CHK:	TITLE: Talon Integrated	Size: 11x17	
DOC CTRL CHK:	Top Level		
MFG CTRL CHK:	REV: 8487729N03	Page:	Of:
QA CHK:	Date: January 7, 2003	Time: 10:43:18 am	
Changed by: Ed Naddeo			

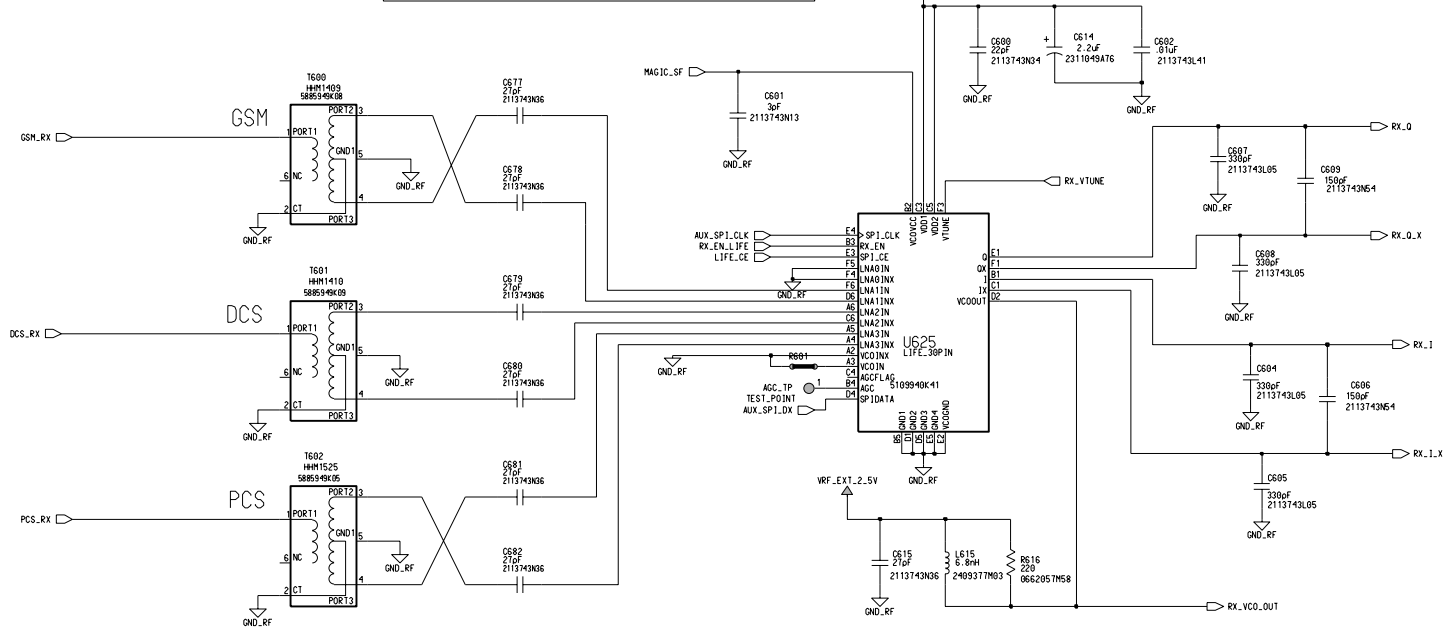


Engineer Dave Suarez	MOTOROLA INC., 600 N US HWY 45 Libertyville, IL		Size D
Drawn by Dave Suarez	TITLE RF_TOP		
R&D CHK			
DOC CTRL CHK			
MFG CTRL CHK			
QA CHK	REV P6	Drawing Number 8487729N06	Sheet of

Changed by Dave Suarez Date Changed May 1, 2003 Time 3:59:05 pm



LIFE IC
REF # 600-649



Engineer Brian Elfers	COMPANY NAME		
Drawn by Brian Elfers	Address City		
R&D CHK	TITLE GSM.Rx	Size D	
DOC CTRL CHK			
MFG CTRL CHK			
QA CHK	REV P3	Drawing Number 8487729N03	Sheet of

Changed by
Dave Suarez

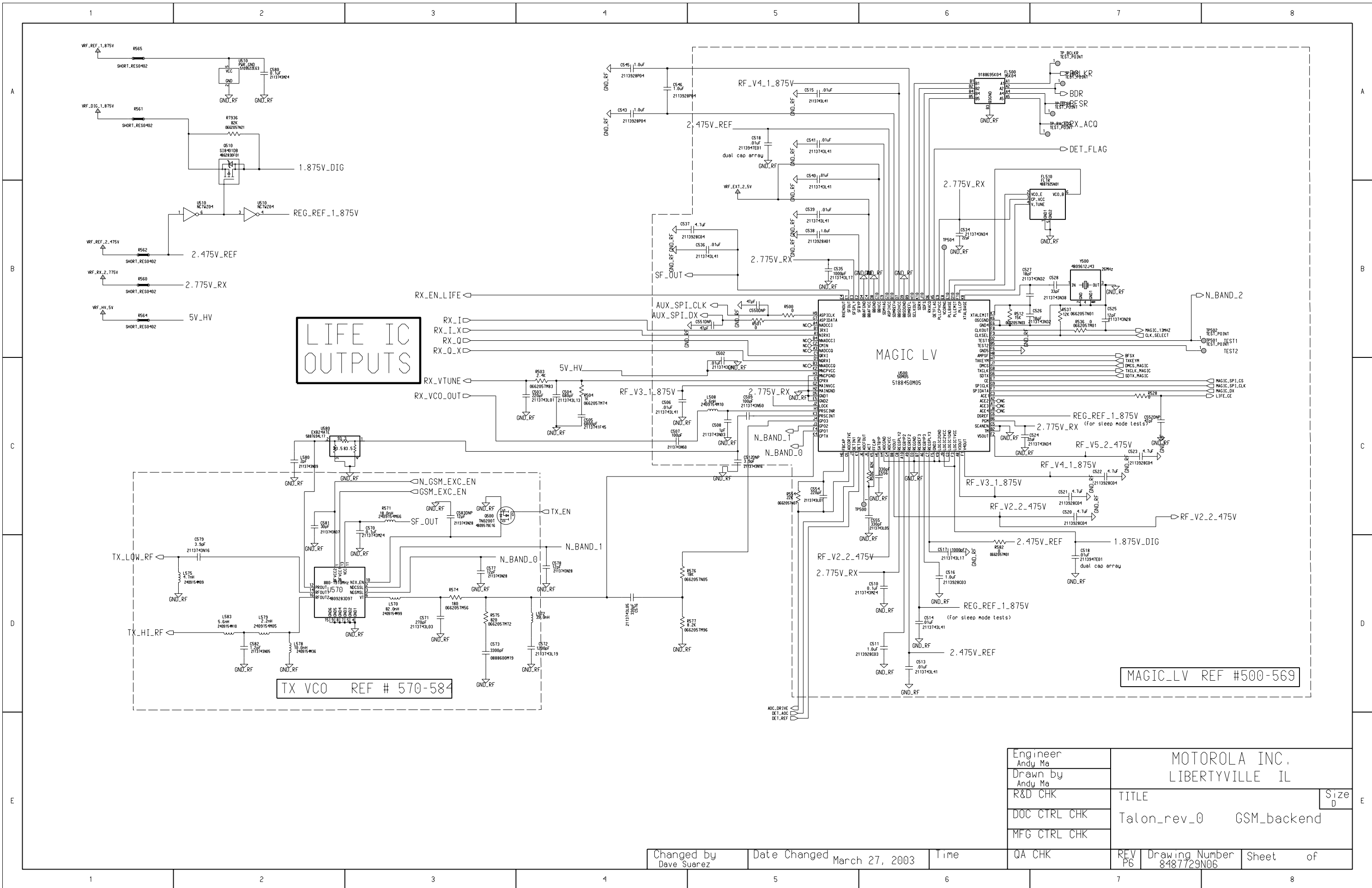
Date Changed
January 21, 2003

Time

1 2 3 4 5 6 7 8

A
B
C
D
E

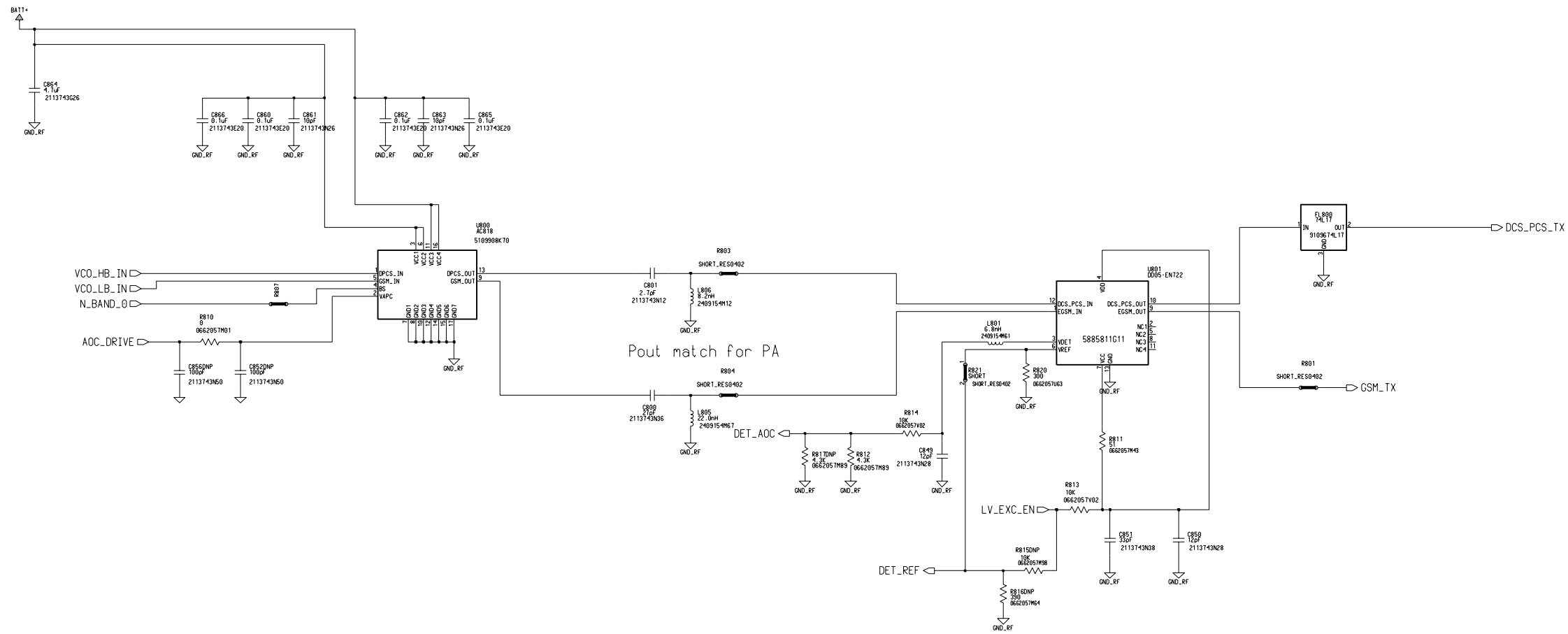
1 2 3 4 5 6 7 8



Engineer Andy Ma	MOTOROLA INC. LIBERTYVILLE IL		
Drawn by Andy Ma	TITLE		
R&D CHK	Talon_rev_0	GSM_backend	Size D
DOC CTRL CHK			
MFG CTRL CHK			
QA CHK	REV P6	Drawing Number 8487729N06	Sheet of

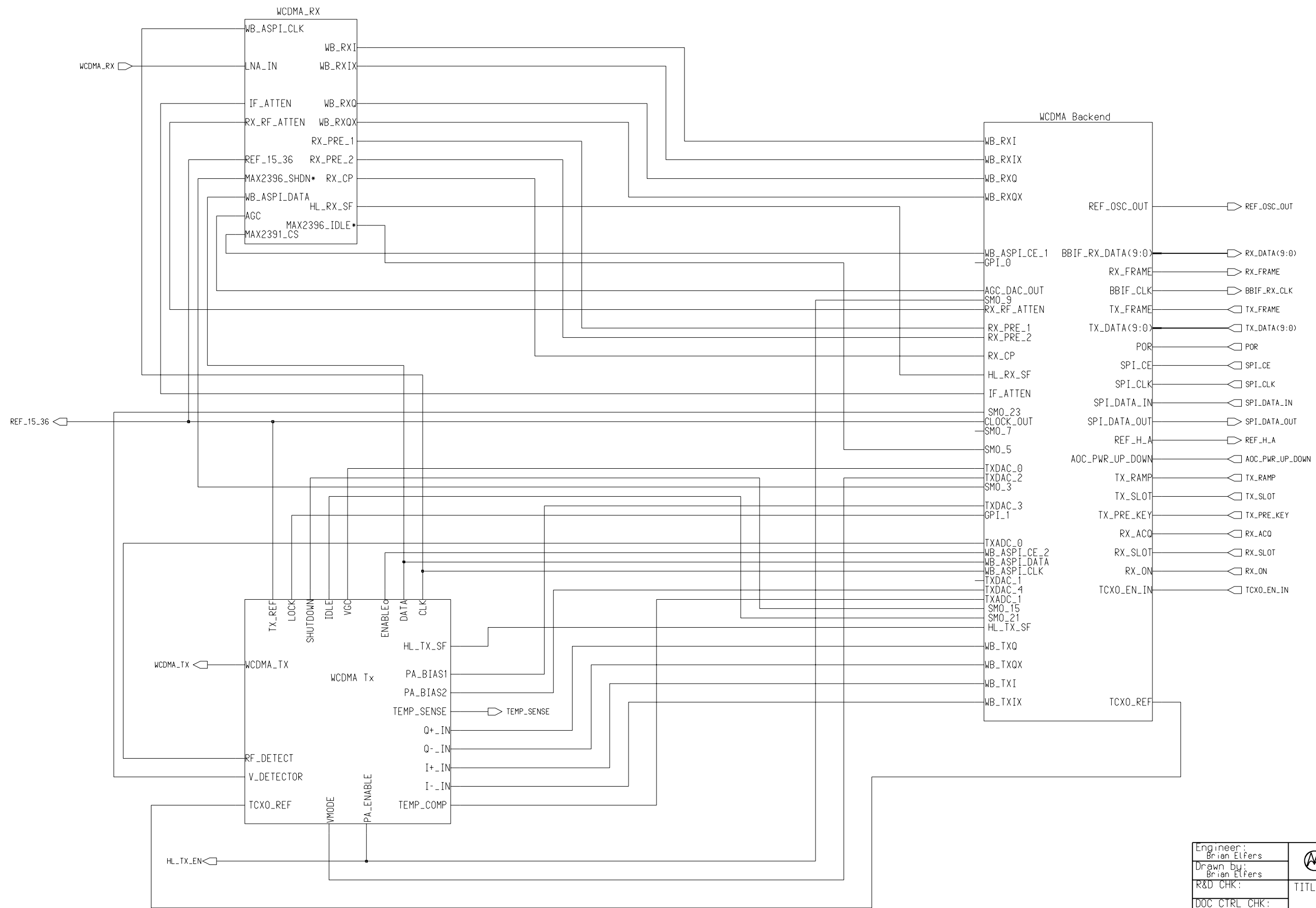
Changed by Dave Suarez	Date Changed March 27, 2003	Time
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GSM Transmitter



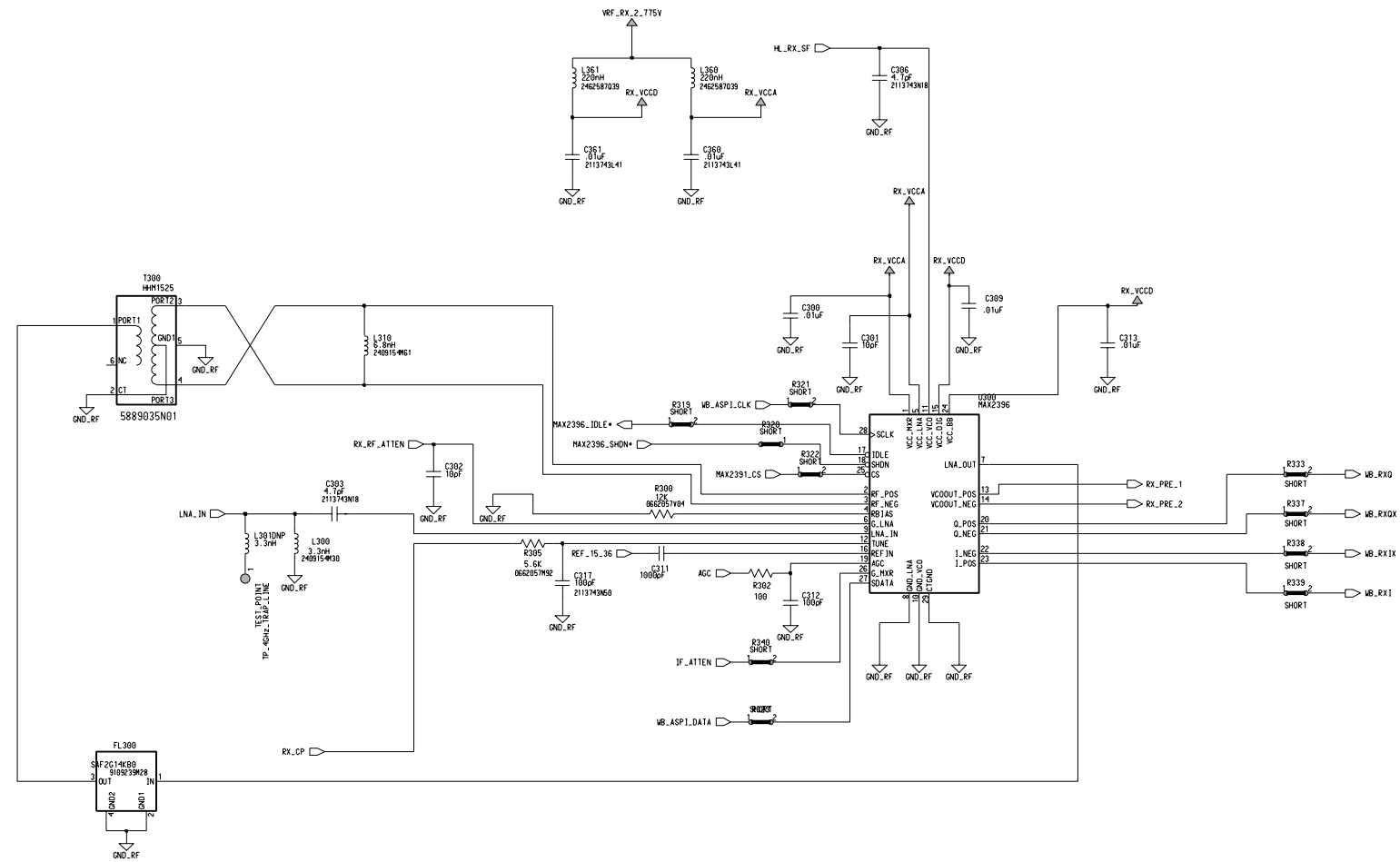
Engineer Paul Manente	COMPANY NAME		
Drawn by Paul Manente	Address City		
R&D CHK	TITLE GSM_TX-D	Size D	
DOC CTRL CHK	GSM_Transmitter		
MFG CTRL CHK	REV PG	Drawing Number 8487729N06	Sheet of

Changed by Dave Suarez Date Changed June 12, 2003 Time

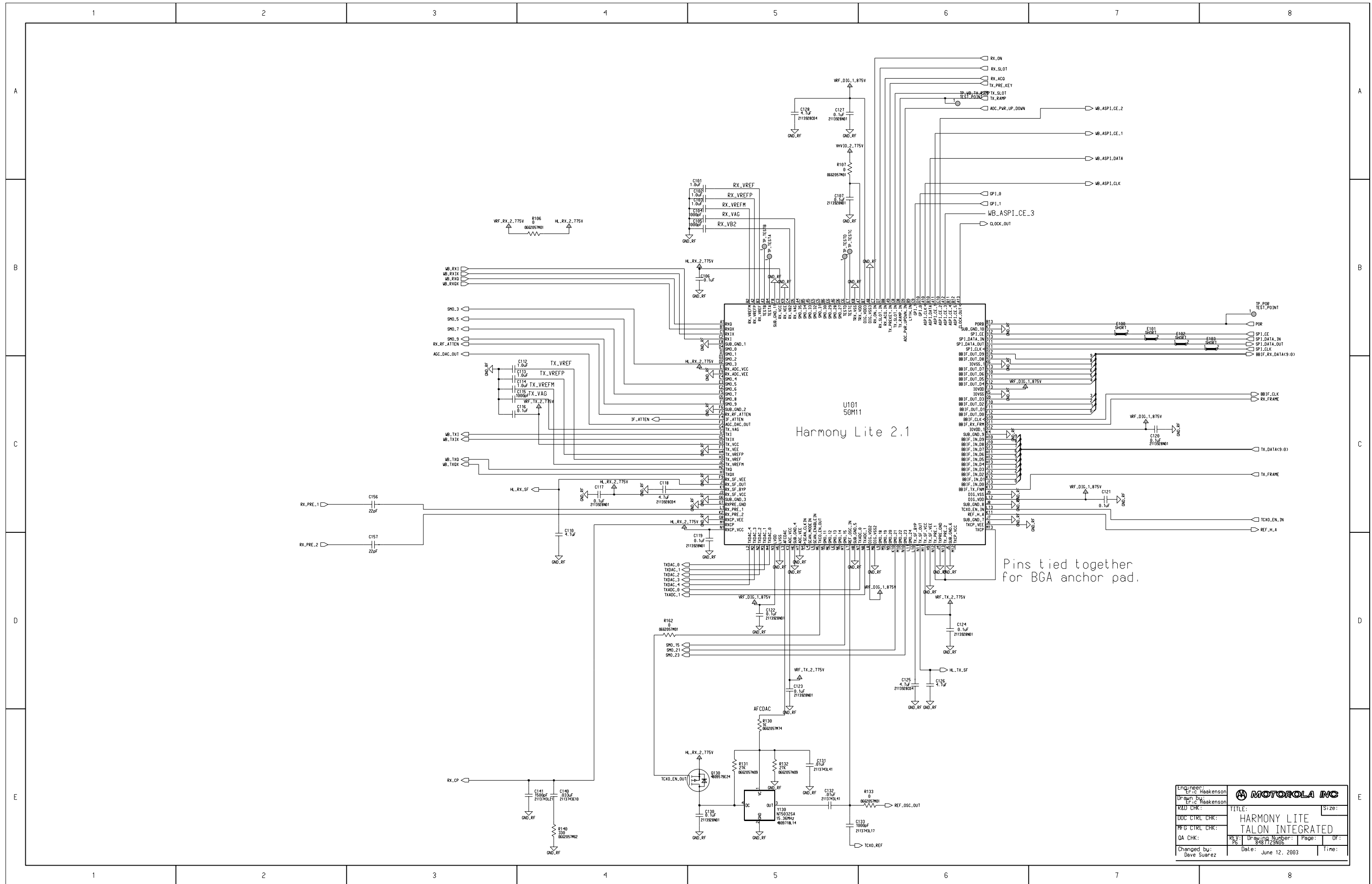


Engineer: Brian Elfers			
Drawn by: Brian Elfers			
R&D CHK:	TITLE: Talon_rev_0	Size: 11x17	
DOC CTRL CHK:			
MFG CTRL CHK:			
QA CHK:	REV: P3	Drawing Number: 8487729N03	Page: OF:
Changed by: Dave Suarez	Date: Friday, April 20, 2001	Time: 4:07:28 pm	

1
MOTOROLA INTERNAL USE ONLY



Engineer:	Randy Kressner		
Drawn By:	Randy Kressner	TITLE:	WCDMA_Rx
QA CHK:		Size:	D
DOC CTRL CHK:			
MFG CTRL CHK:			
QA CHK:		REV:	PS
Changed by:	Dave Suarez	Drawing Number:	848172R06
		Page:	1
		Date:	June 12, 2003
		Time:	4:33:16 pm



Pins tied together for BGA anchor pad.

ENG: neer, Eric Haskenson		
Dr: Bur, Dav, Eric Haskenson	TITLE: HARMONY LITE TALON INTEGRATED	Size:
R&D CHK:	DOC CTRL CHK:	Page:
MFG CTRL CHK:	QA CHK:	Page:
Changed by: Dave Suarez	Date: June 12, 2003	Time:

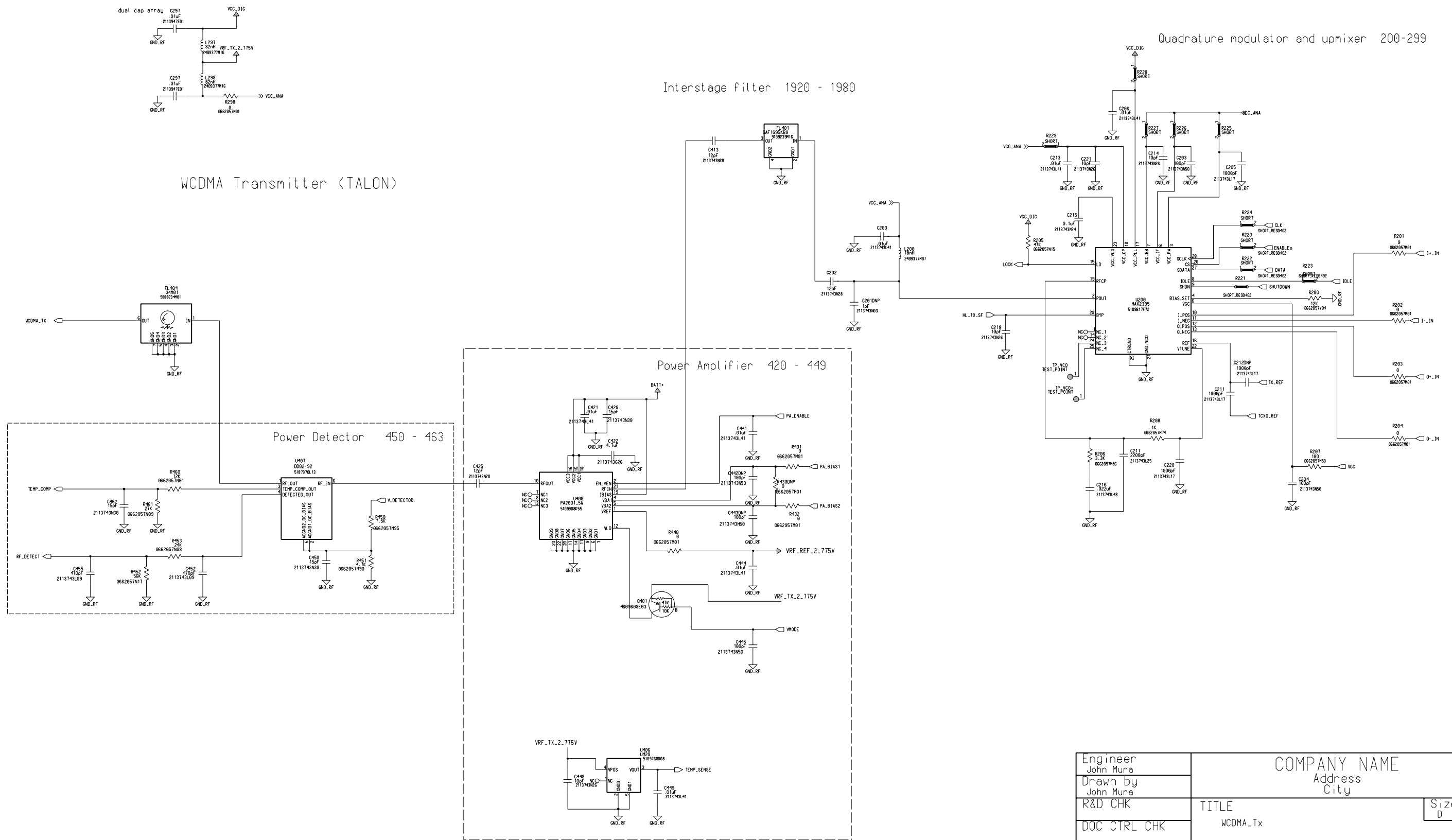
WCDMA Transmitter (TALON)

Quadrature modulator and upmixer 200-299

Interstage filter 1920 - 1980

Power Amplifier 420 - 449

Power Detector 450 - 463



Engineer John Mura	COMPANY NAME Address City		
Drawn by John Mura	TITLE WCDMA_Tx	Size D	
R&D CHK			
DOC CTRL CHK			
MFG CTRL CHK			
QA CHK	REV P6	Drawing Number 8487729N06	Sheet 1 of 1

Changed by Dave Suarez Date Changed June 12, 2003 Time 4:39:58 pm


1

2

3

4

MOTOROLA CONFIDENTIAL PROPRIETARY

Engineer: John S. Kerr	 MOTOROLA INC.		
Drawn by: Todd Roester			
R&D CHK:	TITLE: Talon Integrated	Size: 11x17	
DOC CTRL CHK:	Baseband Top Level		
MFG CTRL CHK:	REV:	Drawing Number:	Page:
QA CHK:			OF:
Changed by: Ed Naddeo	Date: Monday, March 4, 2002	Time: 10:39:56 am	

A

B

C

D

Misc Connectors

- Display Connector USIM
- Keypad Connector IrDA
- Headset Jack Vibrator
- Fun Lights

- FUNLITE_GRN
- FUNLITE_RED
- FUNLITE_BLU
- IRDA_SD
- IRDA_RX
- IRDA_TX
- USIM_CLK
- USIM_RST
- USIM_IO
- USIM_PD*
- LCD(15:8)
- LCD_CLK
- LCD_HSYNC
- LCD_VSYNC
- CLT_SPI_CLK
- CLT_SPI_DATA
- CLT_CS
- DISPLAY_SPI_CLK
- DISPLAY_SPI_DATA
- DISPLAY_CS
- DISPLAY_ON
- FRNT_LIGHT_CTRL
- KEY(0:11)
- GPIO_5V_EN

- RF_RESET*
- BCLKR
- BFSR
- BDR
- BCLKX
- BFSX
- BDX
- DMCS_MAGIC
- BB_CLK_13M
- CLK_SEL
- NB_EXC_EN*
- NB_RX_ACO
- NB_TX_EN*
- NB_SPI_CLK
- NB_SPI_MOSI
- MAGIC_CS
- TX_RX_ERROR
- DET_FLAG
- BBIF_RX_FRM
- BBIF_RX(7:2)
- BBIF_TX(7:0)
- BBIF_TX_FRM
- BBIF_TX_FRAME
- BBIF_CLK
- BB_CLK_15_36M
- WB_TX_RAMP
- WB_RX_SLOT
- WB_TX_SLOT
- WB_TX_PREKEY
- WB_RX_ACO
- WB_RX_ON
- AOC_PWR_UP
- HL_TX_EN

- ALRT+
- ALRT-
- HS_MAKE_DETECT
- HS_MIC
- HS_SPKR_L
- HS_SPKR_R
- ON_OFF_END*
- V_VIB
- EAR_SPKR-
- EAR_SPKR+
- VID_GPSb_SEL

CORE LOGIC BLOCK

- POG
- Memories
- Video
- AGPS
- Embedded SD

- FUNLITE_GRN
- FUNLITE_RED
- FUNLITE_BLU
- IRDA_SD
- IRDA_RX
- IRDA_TX
- USIM_CLK
- USIM_RST
- USIM_IO
- USIM_PD*
- LCD(15:8)
- LCD_CLK
- LCD_HSYNC
- LCD_VSYNC
- CLT_SPI_CLK
- CLT_SPI_DATA
- CLT_CS
- DISPLAY_SPI_CLK
- DISPLAY_SPI_DATA
- DISPLAY_CS
- DISPLAY_ON
- FRNT_LIGHT_CTRL
- KEY(0:11)
- GPIO_5V_EN
- RF_RESET*
- BCLKR
- BFSR
- BDR
- BCLKX
- BFSX
- BDX
- DMCS_MAGIC
- BB_CLK_13M
- CLK_SEL
- NB_EXC_EN*
- NB_RX_ACO
- NB_TX_EN*
- NB_SPI_CLK
- NB_SPI_MOSI
- MAGIC_CS
- TX_RX_ERROR
- DET_FLAG
- BBIF_RX_FRM
- BBIF_RX(7:2)
- BBIF_TX(7:0)
- BBIF_TX_FRM
- BBIF_TX_FRAME
- BBIF_CLK
- BB_CLK_15_36M
- WB_TX_RAMP
- WB_RX_SLOT
- WB_TX_SLOT
- WB_TX_PREKEY
- WB_RX_ACO
- WB_RX_ON
- AOC_PWR_UP
- HL_TX_EN

CE Bus Connector

- VID_RTS*
- LOGIC_SENSE
- VID_CTS*
- SWB+_EN
- HKSW
- MUTE*
- DSEL2
- DSEL1
- DSELO
- OPT1
- OPT2
- DATALOG*

- VID_RTS*
- LOGIC_SENSE
- VID_CTS*
- SWB+_EN
- HKSW
- MUTE*
- DSEL2
- DSEL1
- DSELO
- OPT1
- OPT2
- DATALOG*

- BATT_FDBK
- D+
- D-
- AUDIO_IN
- AUDIO_OUT

Bluetooth

- BLUETOOTH_WAKE*
- BLUETOOTH_INT*
- BLUE_TX
- BLUE_CTS*
- BLUE_RTS*
- BLUE_RX
- WB_SPI_CLK
- WB_SPI_MOSI
- WB_SPI_MISO
- HARMONY_CS
- CLK_32_768K_BUFF

PCAP (Power/Audio)

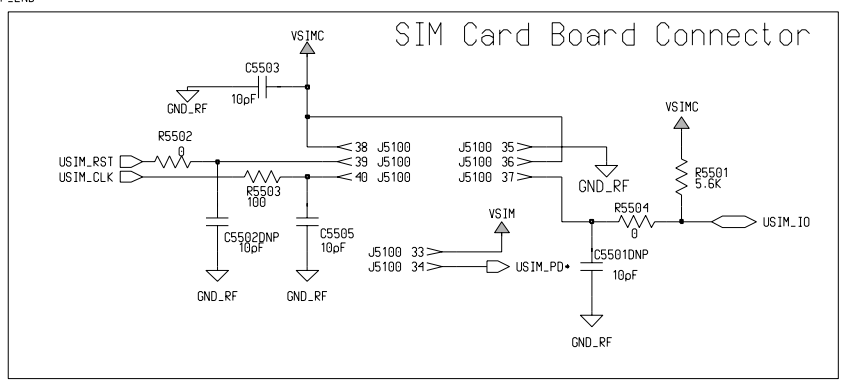
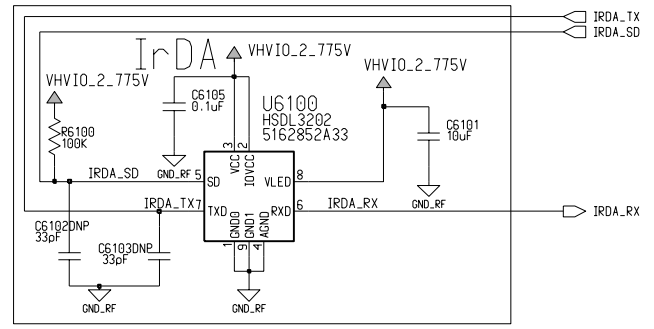
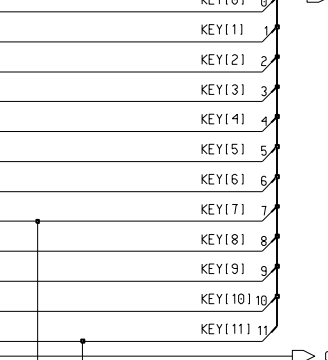
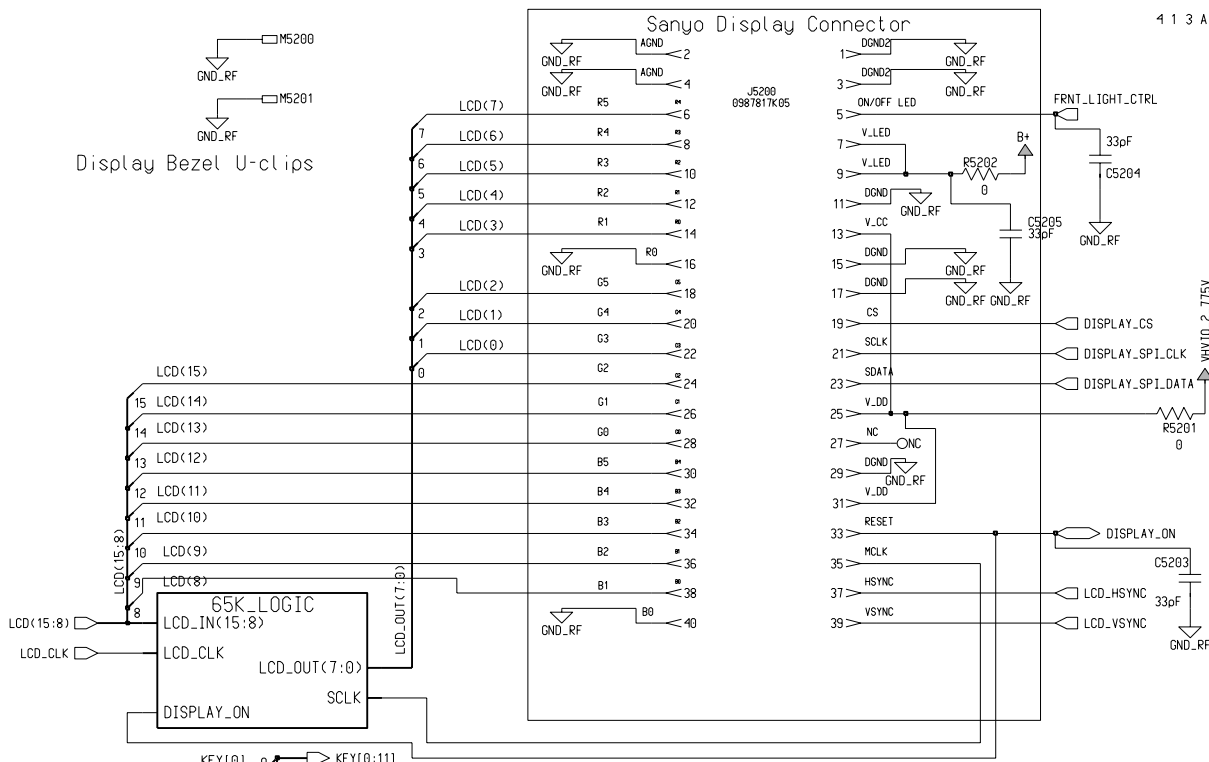
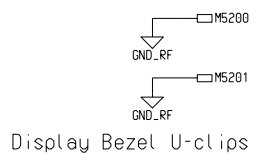
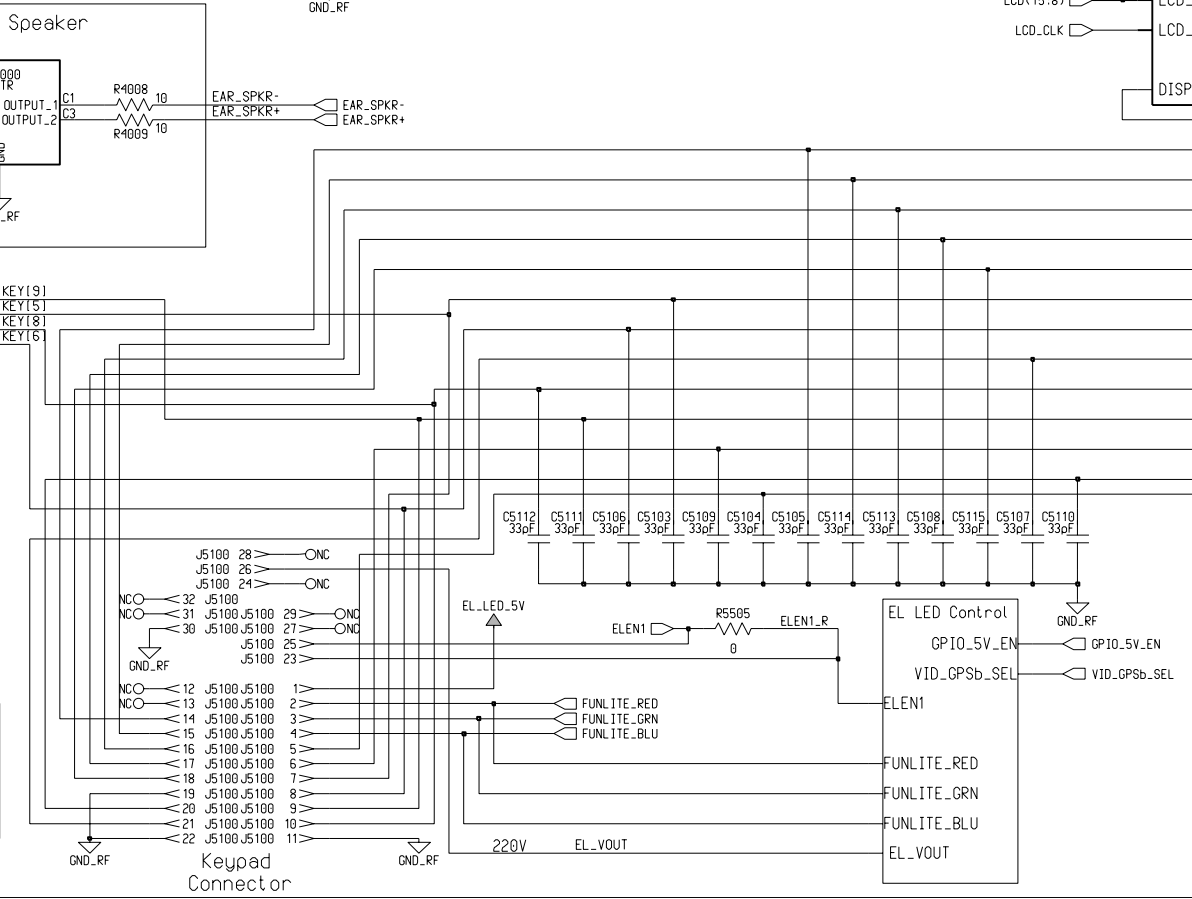
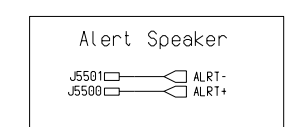
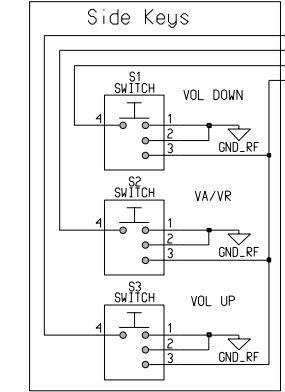
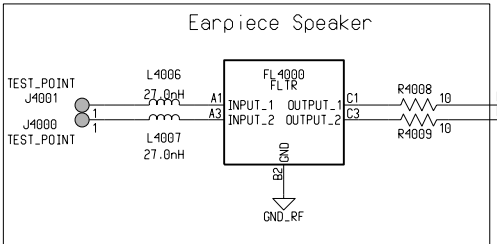
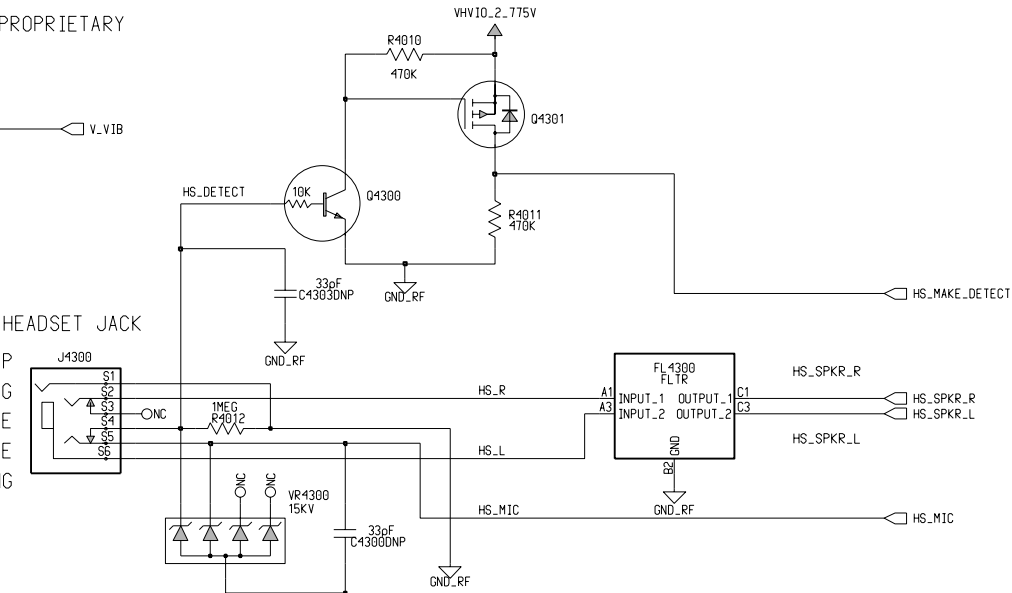
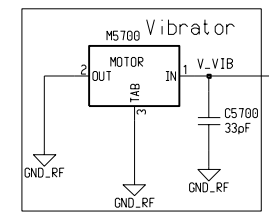
- ASAP_FS
- ASAP_CLK
- ASAP_RX
- ASAP_TX
- STEREO_FS
- STEREO_CLK
- STEREO_TX
- VSIMC_EN
- MUXCTL
- BATT_IO
- RXD.VMIN
- TXD.VPOUT
- RTS*.XRXD
- USB_TXEN*
- USB_VMOUT
- USB_VPIN
- USB_DETECT
- BB_SPI_CLK
- BB_SPI_MISO
- BB_SPI_MOSI
- PCAP_CS
- PCAP_INT
- PCAP_RESET*
- USER_OFF
- POWER_FAIL
- PCAP_MCU_RESET*
- WATCHDOG
- CLK_32_768K
- NB_STANDBY
- VID_GPSb_SEL
- MIDRATE_1
- MIDRATE_2
- NB_TX_KEYM
- BATT_FDBK_SW
- BATT_DETB
- REF_15_36
- NB_TX_KEYM

- EAR_SPKR+
- EAR_SPKR-
- V_VIB
- ELEN1
- ON_OFF_END*
- HS_SPKR_R
- HS_SPKR_L
- HS_MIC
- HS_MAKE_DETECT

- ALRT-
- ALRT+

MOTOROLA CONFIDENTIAL PROPRIETARY

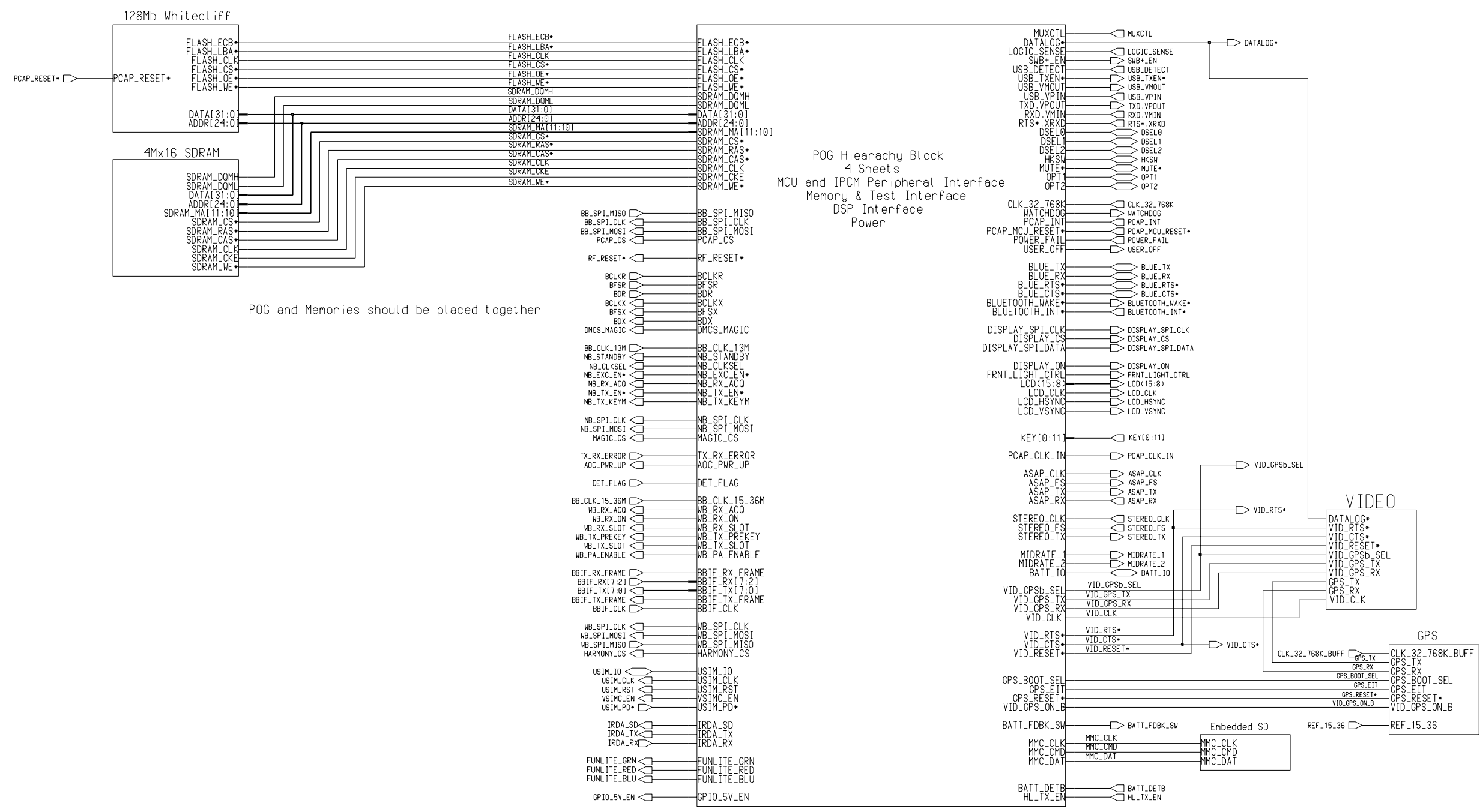
Engineer: Ed Naddeo	MOTOROLA INC.		
Drawn by: Ed Naddeo			
R&D CHK:	TITLE: TALON INTEGRATED	Size: 11x21	
DOC CTRL CHK:	Misc Connectors		
MFG CTRL CHK:	REV: 1	Drawing Number: 1	
QA CHK:	Date: Monday, April 23, 2001	Page: 1	
Changed by: Ed Naddeo		Time: 11:27:59 am	



MOTOROLA CONFIDENTIAL PROPRIETARY

4 1 3 A

Engineer: Ed Naddeo	MOTOROLA INC.		
Drawn by: Ed Naddeo			
R&D CHK:	TITLE: Talon Integrated Core Logic	Size: 11x21	
DOC CTRL CHK:			
MFG CTRL CHK:			
QA CHK:	REV: 1.0	Drawing Number: 848888x88	Page: 1
Changed by: wlen01	Date: Friday, March 1, 2002	Time: 1:16:44 pm	



POG and Memories should be placed together

POG Hierarchy Block
4 Sheets
MCU and IPCM Peripheral Interface
Memory & Test Interface
DSP Interface
Power

VIDEO

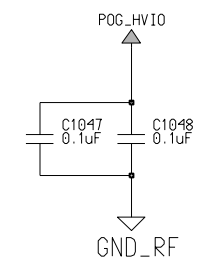
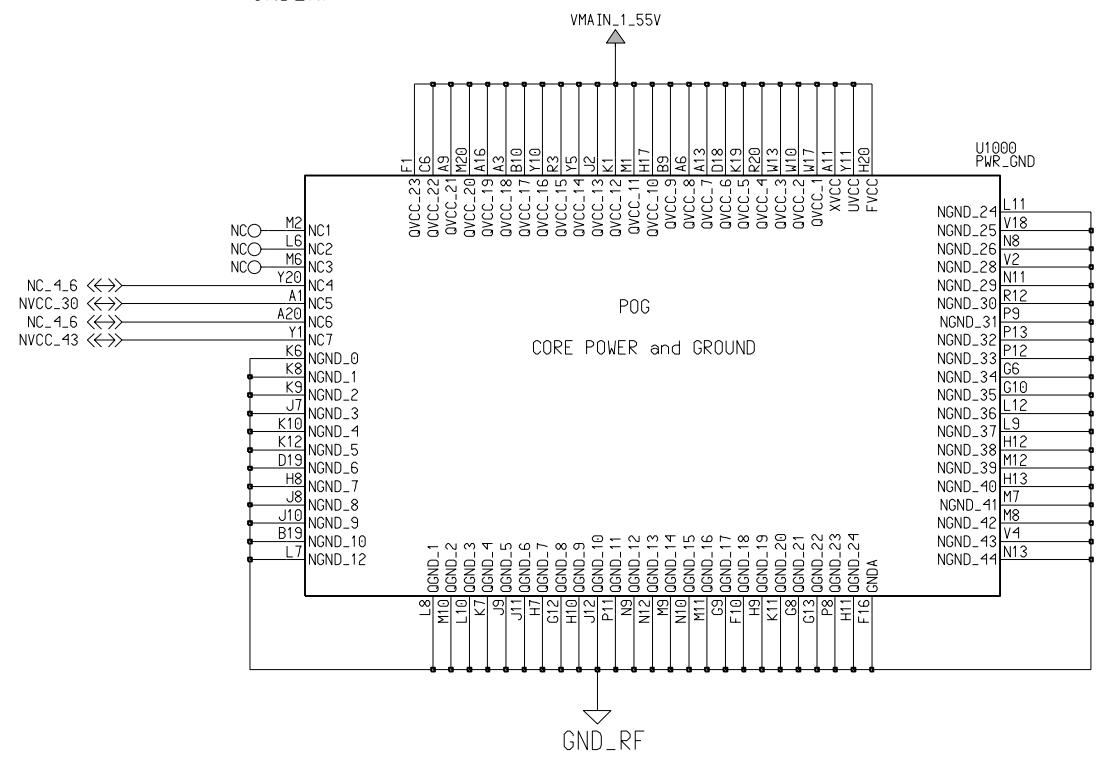
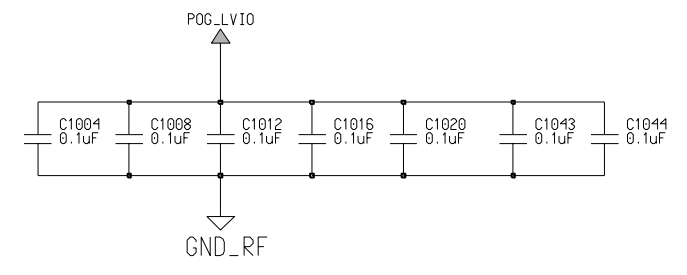
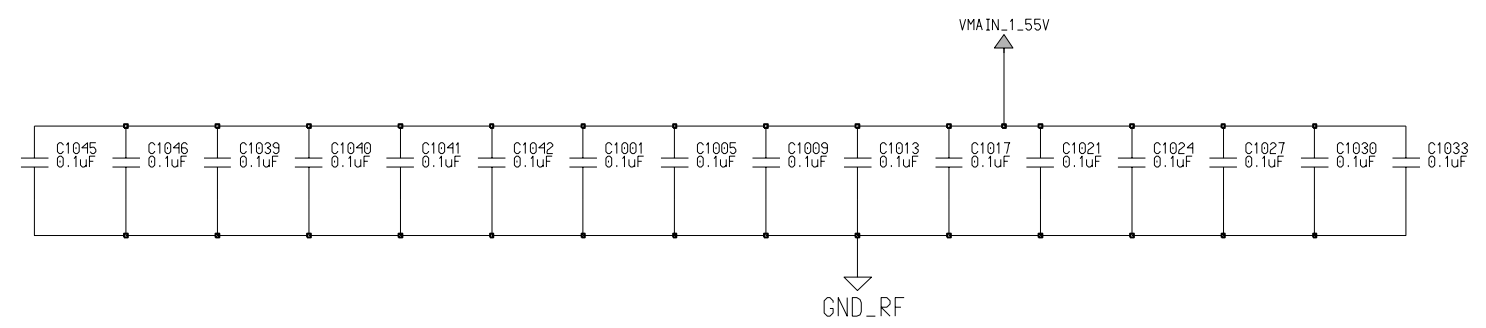
GPS

Embedded SD

MOTOROLA CONFIDENTIAL PROPRIETARY

4 1 3 A

Engineer: Ed Naddeo			
Drawn by: Ed Naddeo			
R&D CHK:	TITLE: Talon Integrated	Size: 11x17	
DOC CTRL CHK:	POG		
MFG CTRL CHK:	Power		
QA CHK:	REV: 1	Drawing Number: 8488888x88	Page: 1 OF 4
Changed by: wlen01	Date: Tuesday, February 26, 2002	Time: 2:56:17 pm	



A

B

C

D

MOTOROLA CONFIDENTIAL PROPRIETARY

A

B

C

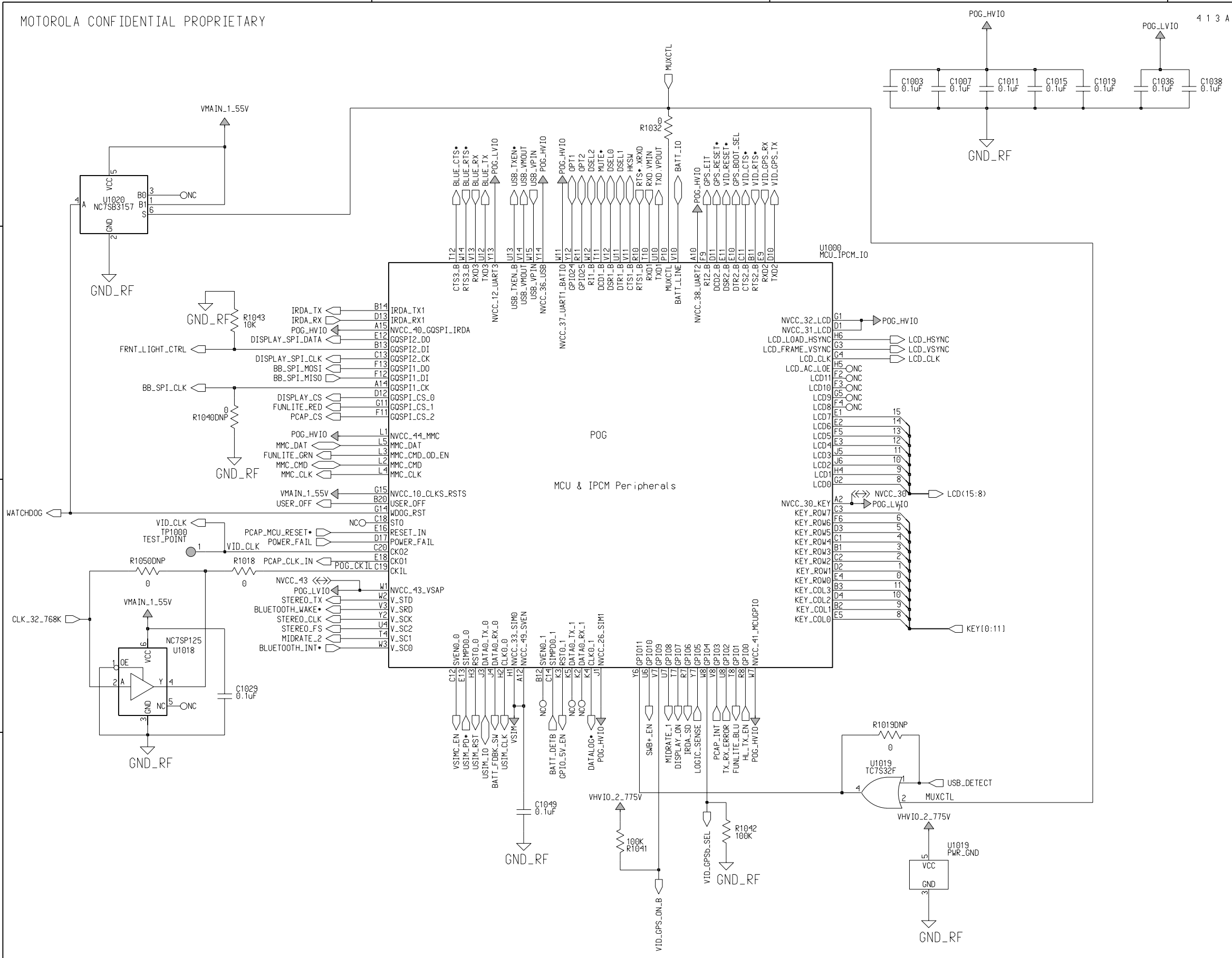
D

1

2

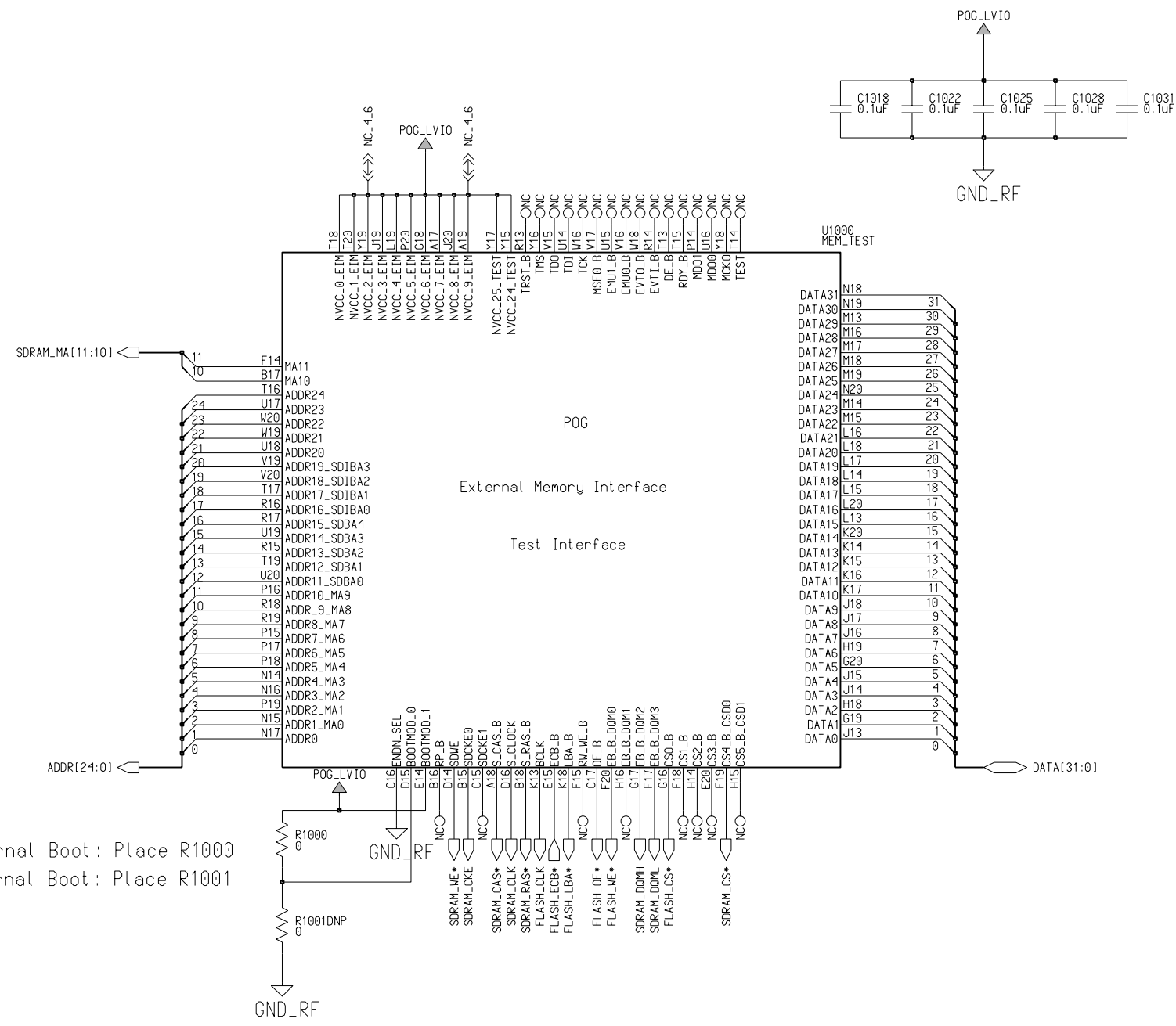
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4



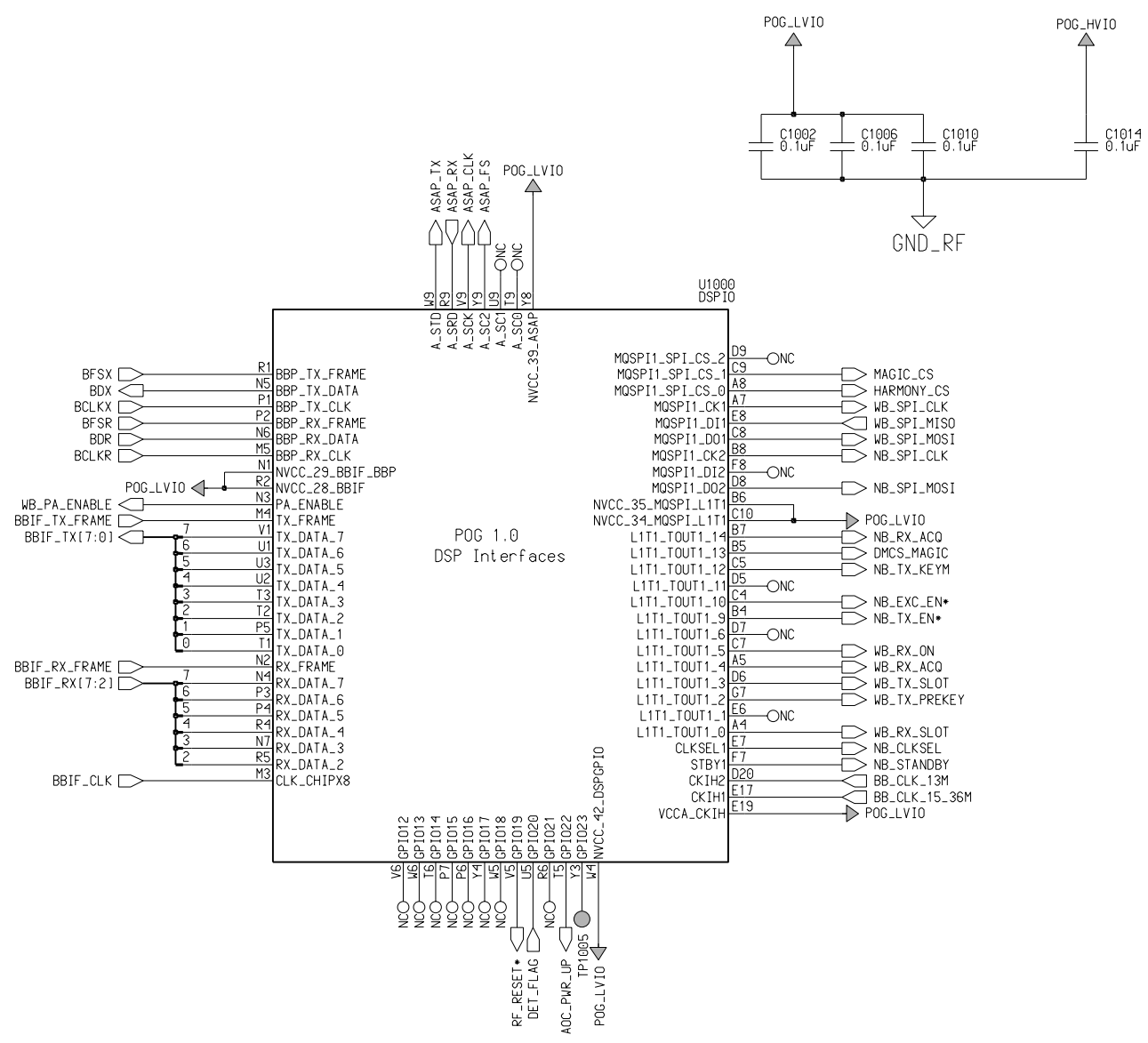
Engineer: Ed Naddeo	MOTOROLA INC.		
Drawn by: Ed Naddeo			
R&D CHK:	TITLE: TALON Integrated	Size: 11x17	
DOC CTRL CHK:	POG		
MFG CTRL CHK:	Accessories Interface		
QA CHK:	REV: 1.0	Drawing Number: 848888x88	Page: 2
Changed by: wlen01	Date: Tuesday, February 26, 2002	Time: 4:39:04 pm	

Engineer: Ed Naddeo	TITLE: Talon Integrated			Size: 11x17
Drawn by: Ed Naddeo	R&D CHK:			
DOC CTRL CHK:	MFG CTRL CHK:			
QA CHK:	REV: 1.0	Drawing Number: 848888x88	Page: 3	OF: 4
Changed by: wlen01	Date: Tuesday, February 26, 2002		Time: 6:15:09 pm	




Internal Boot : Place R1000
 External Boot : Place R1001

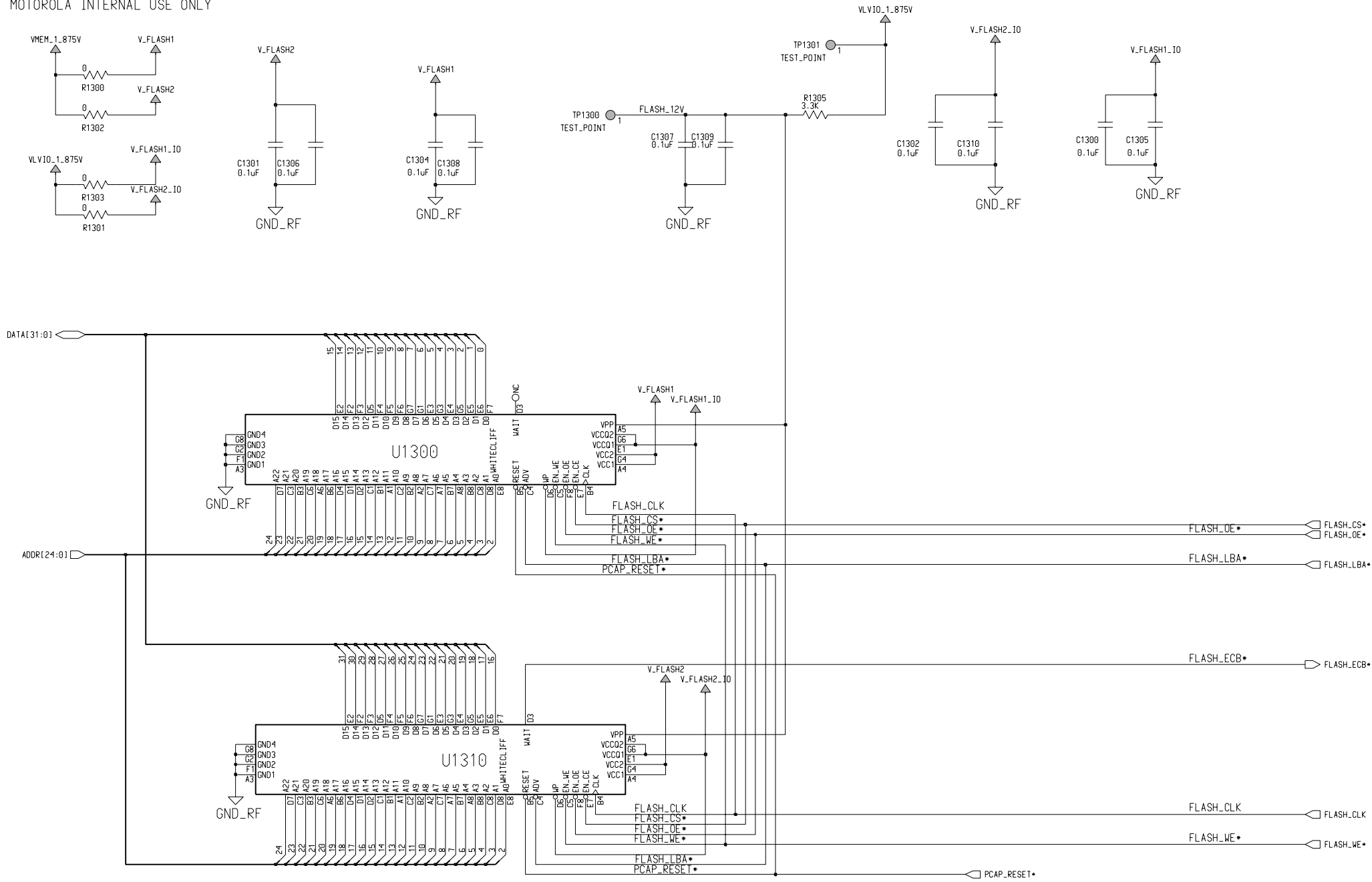
Engineer: Ed Naddeo	TITLE: Talon Integrated			Size: 11x17
Drawn by: Ed Naddeo	POG			
R&D CHK:	DSP Interface			
DOC CTRL CHK:	REV: 1.0	Drawing Number: 848888x88	Page: 4	OF: 4
MFG CTRL CHK:	Date: Tuesday, February 26, 2002			Time: 6:16:44 pm
QA CHK:	Changed by: wlen01			



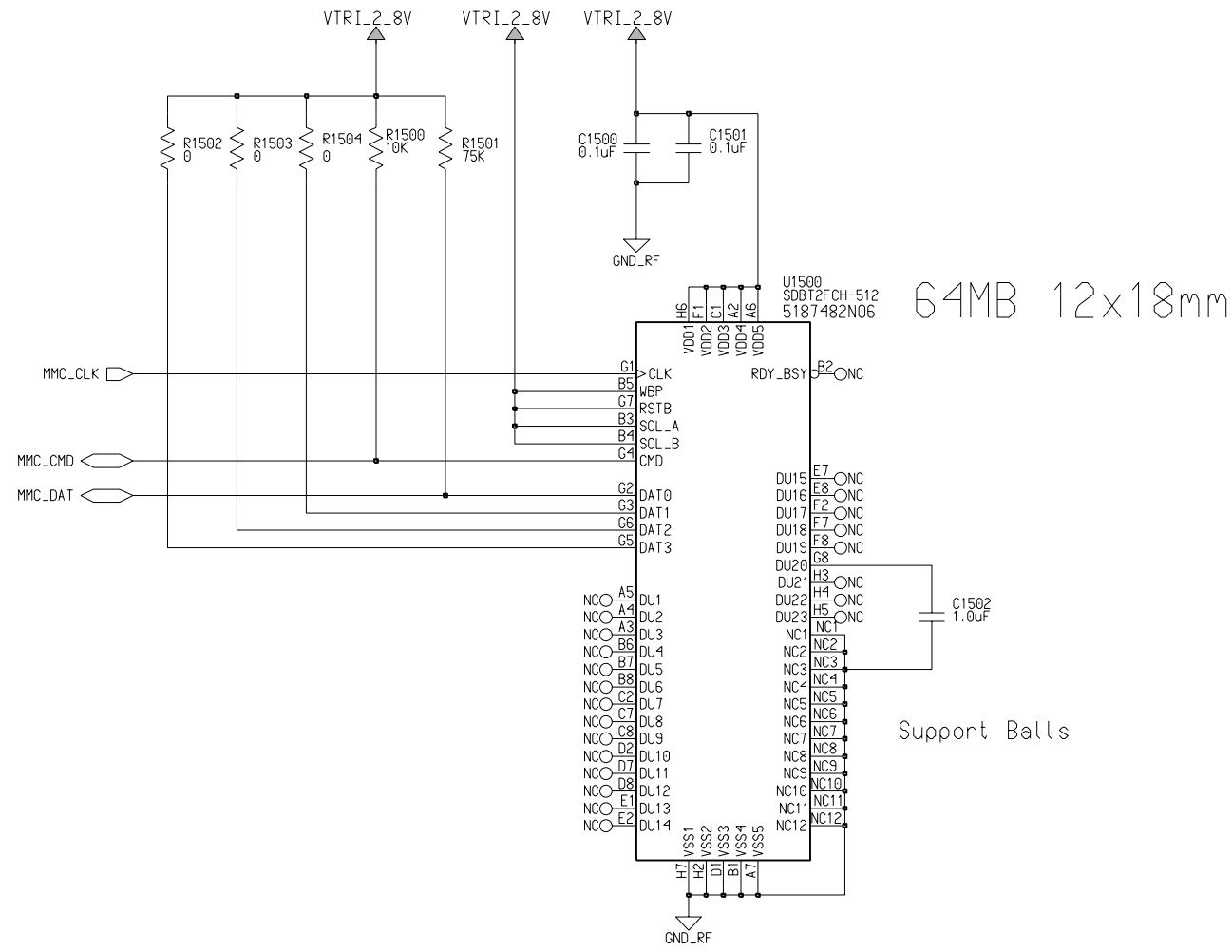
MOTOROLA INTERNAL USE ONLY

4 1 3 A

Engineer: Ed Naddeo			
Drawn by: Ed Naddeo			
R&D CHK:	TITLE: Talon Integrated Flash Memory	Size: 11x21	
DOC CTRL CHK:	REV: 0.1	Drawing Number:	Page: 1
MFG CTRL CHK:	Date: Monday, May 6, 2002	Time: 9:32:29 am	
QA CHK:			
Changed by: Chris Pipe			



Engineer: Ed Naddeo	TITLE: Talon Integrated		
Drawn by: Ed Naddeo	Size: 11x17		
R&D CHK:	DOC CTRL CHK:		
MFG CTRL CHK:	QA CHK:		
REV: 1.0	Drawing Number: 84888888x88	Page: 1	OF: 1
Changed by: Chris Pipe	Date: Friday, May 3, 2002	Time: 1:25:03 pm	



A

B

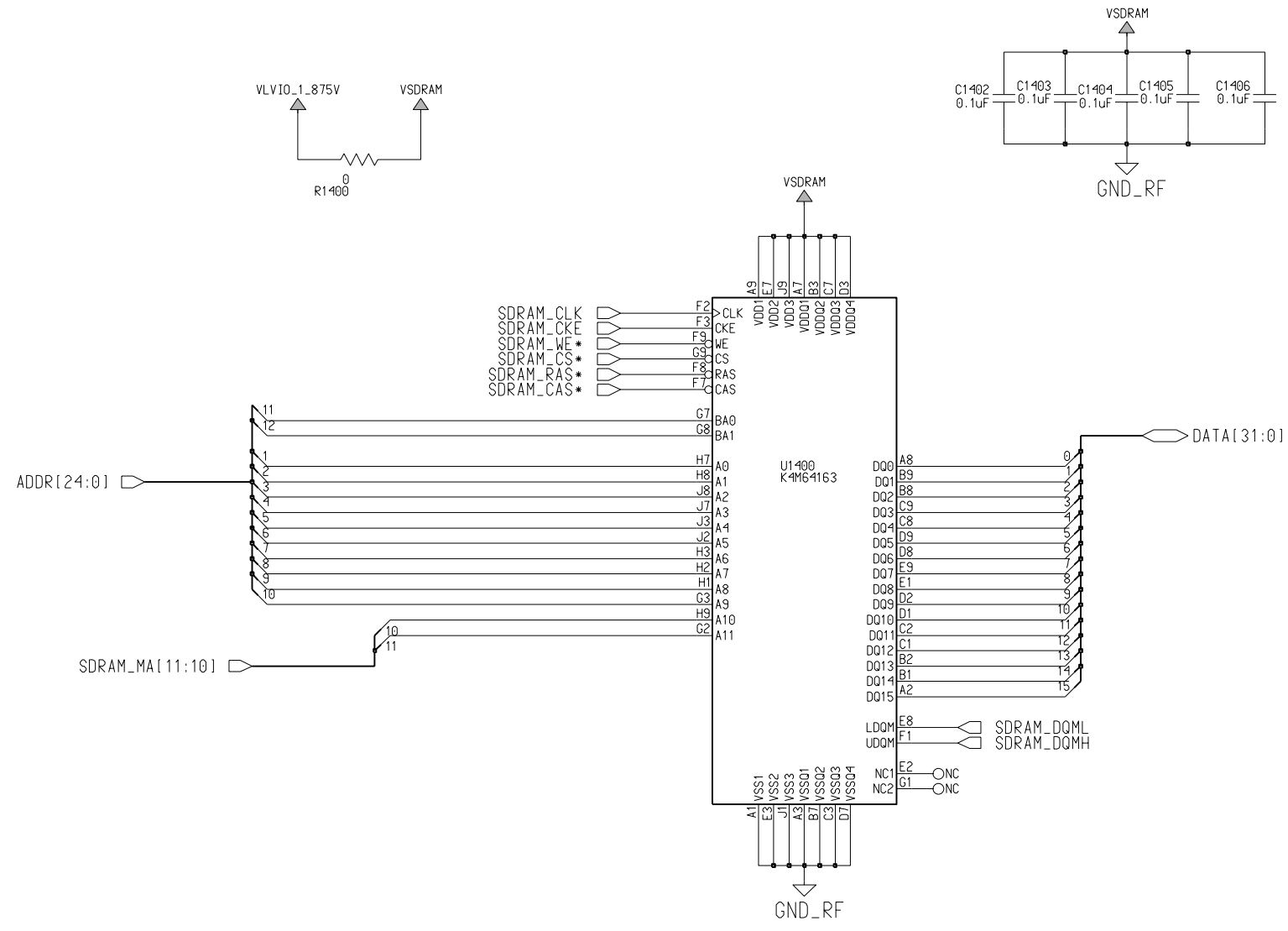
C

D

MOTOROLA CONFIDENTIAL PROPRIETARY

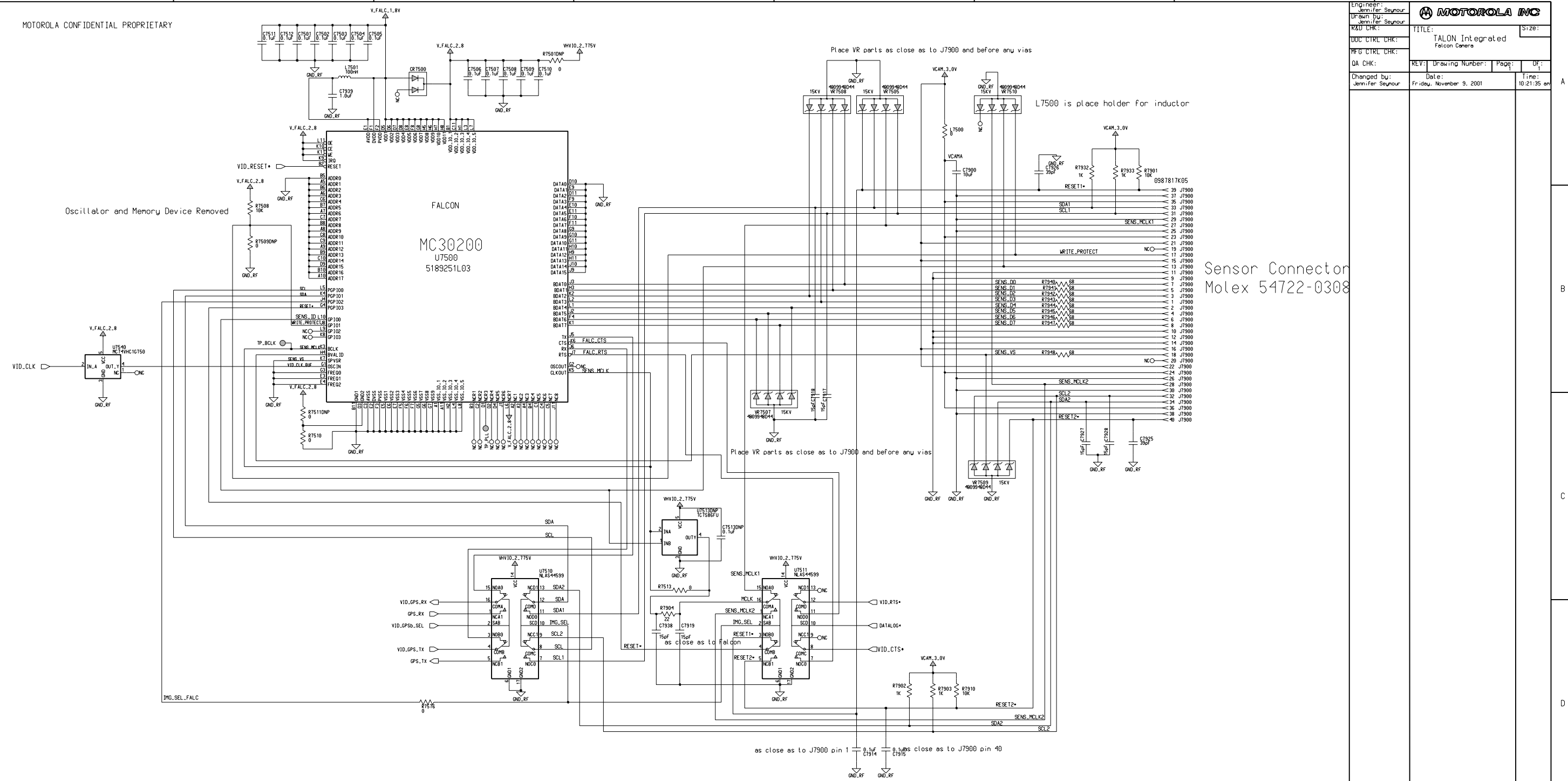
4 1 3 A

Engineer: Ed Naddeo			
Drawn by: Ed Naddeo			
R&D CHK:	TITLE: Talon Integrated	Size: 11x17	
DOC CTRL CHK:	4Mx16 SDRAM		
MFG CTRL CHK:			
QA CHK:	REV: 1.0	Drawing Number: 8488888x88	Page: 1 OF 1
Changed by: ulen01	Date: Friday, March 1, 2002	Time: 3:37:06 pm	




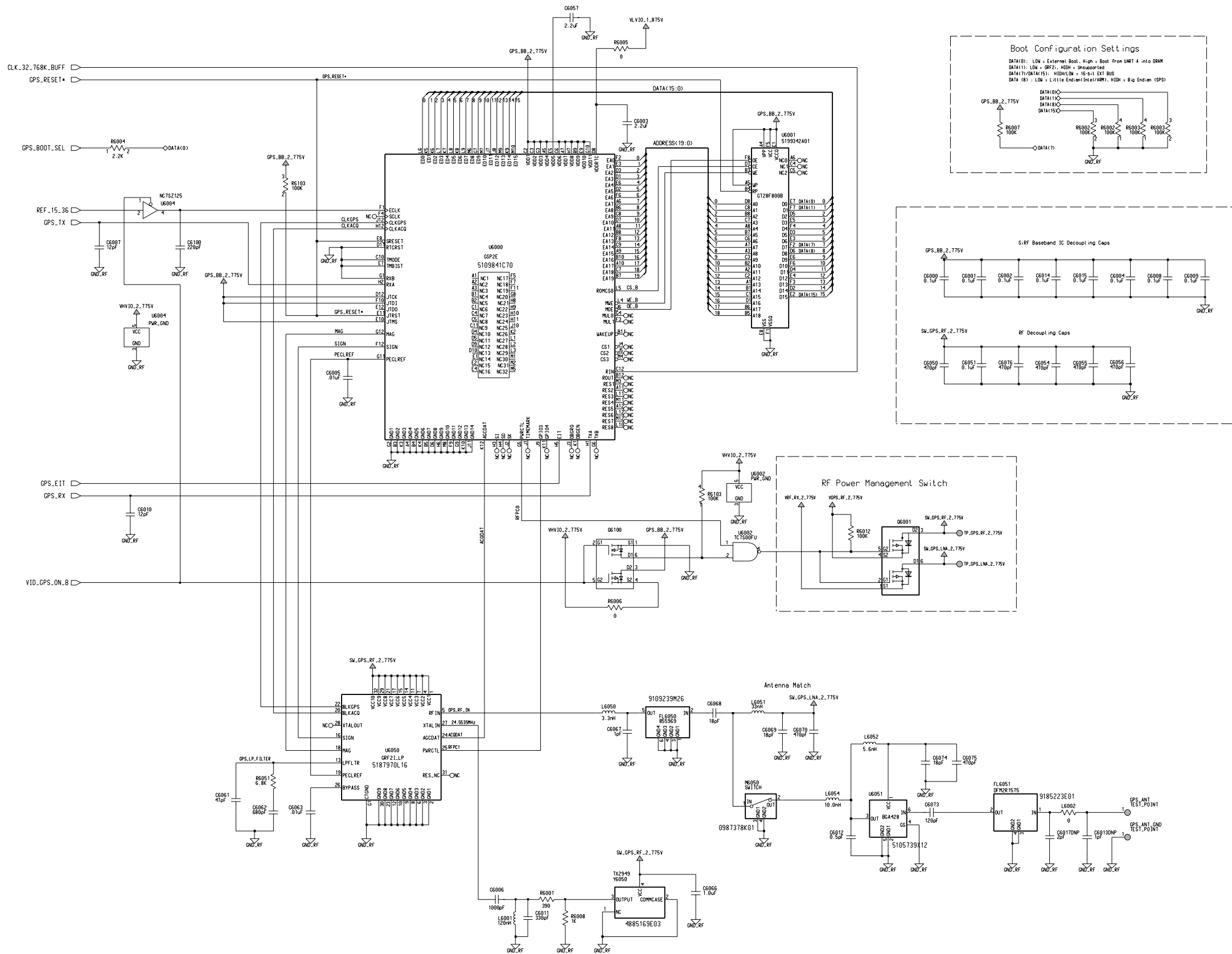
MOTOROLA CONFIDENTIAL PROPRIETARY

Engineer: Jennifer Seunour		
Drawn by: Jennifer Seunour	TITLE: TALON Integrated Falcon Camera	Size:
DUC CTRL CHK:	REV: Drawing Number:	Page: 1
MFS CTRL CHK:	Date: Friday, November 9, 2001	Time: 10:21:35 am
QA CHK:		
Changed by: Jennifer Seunour		



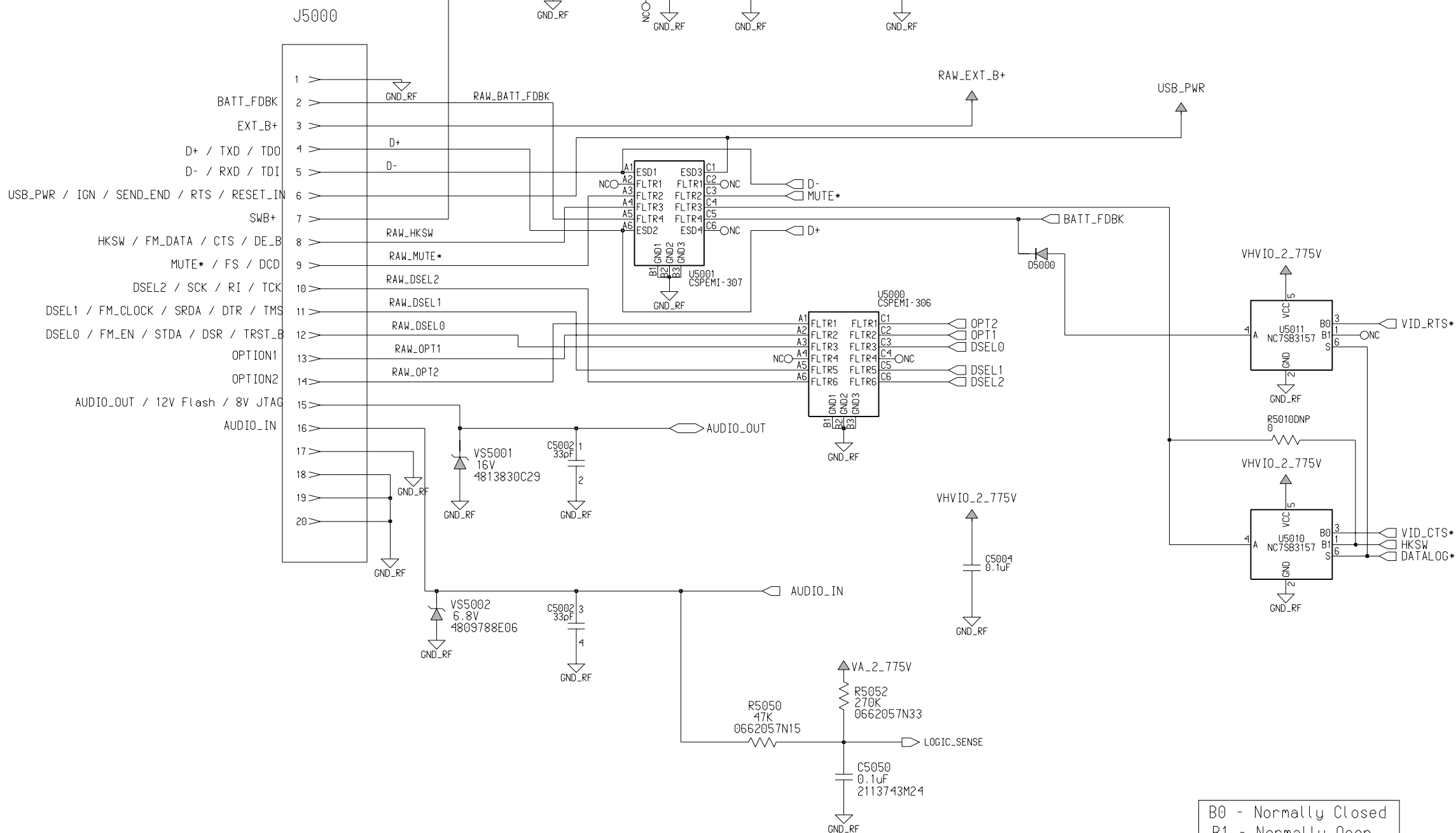
Sensor Connector
Molex 54722-0308

Engineer: J. Abe Keane	 MOTOROLA INC	
Drawn by: J. Abe Keane	TITLE: TALON Integrated GPS Block	Size:
R&D CHK:	SrFSStar/ie/AP Schematic	
QA CHK:	REV: Drawing Number: Page: 1	OK:
Changed by: J. Abe Keane	Date: Monday, September 11, 2001	Time: 10:21:35 am



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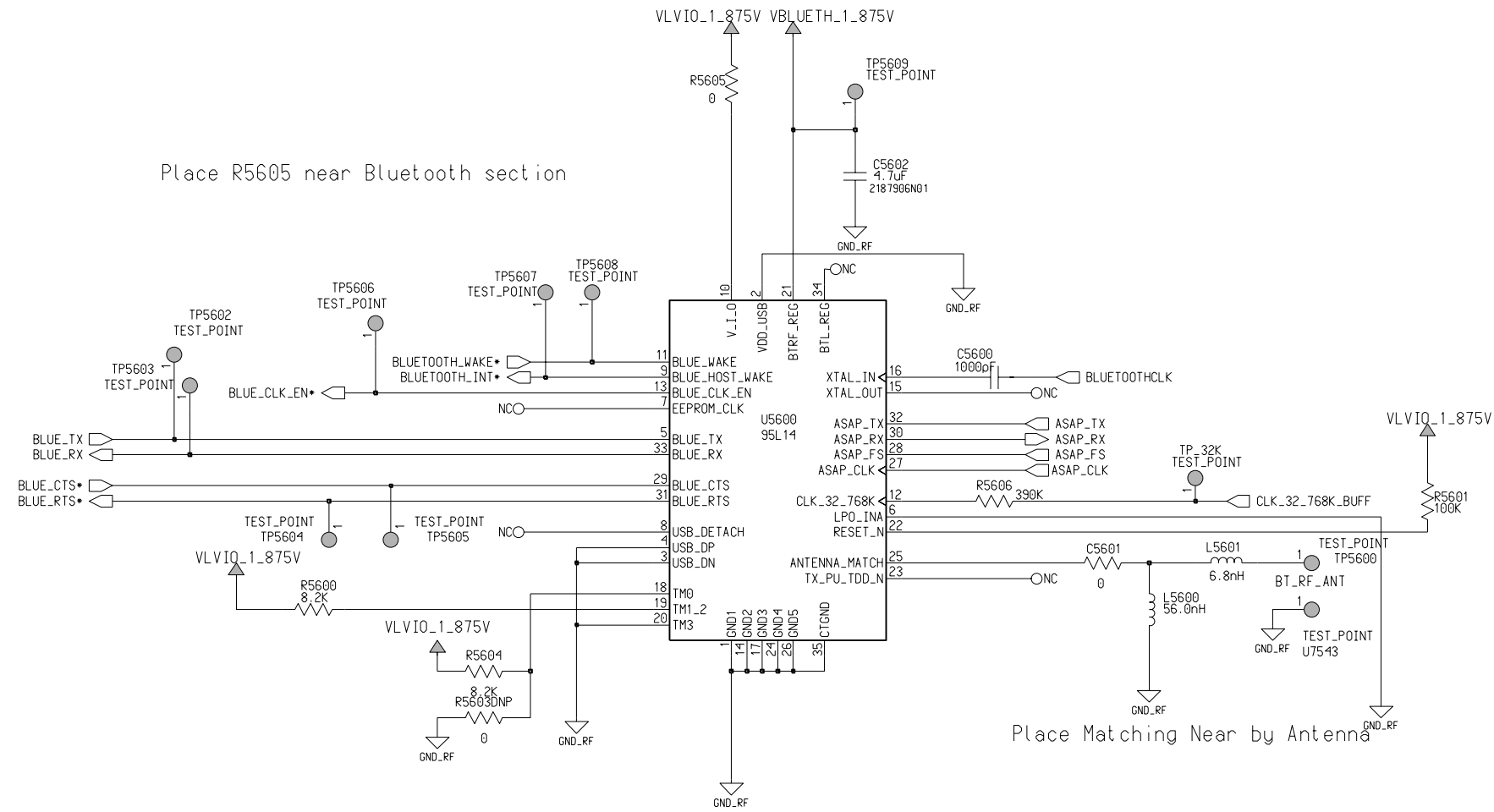
Accessory Connector
CE Connector (0987636K05)

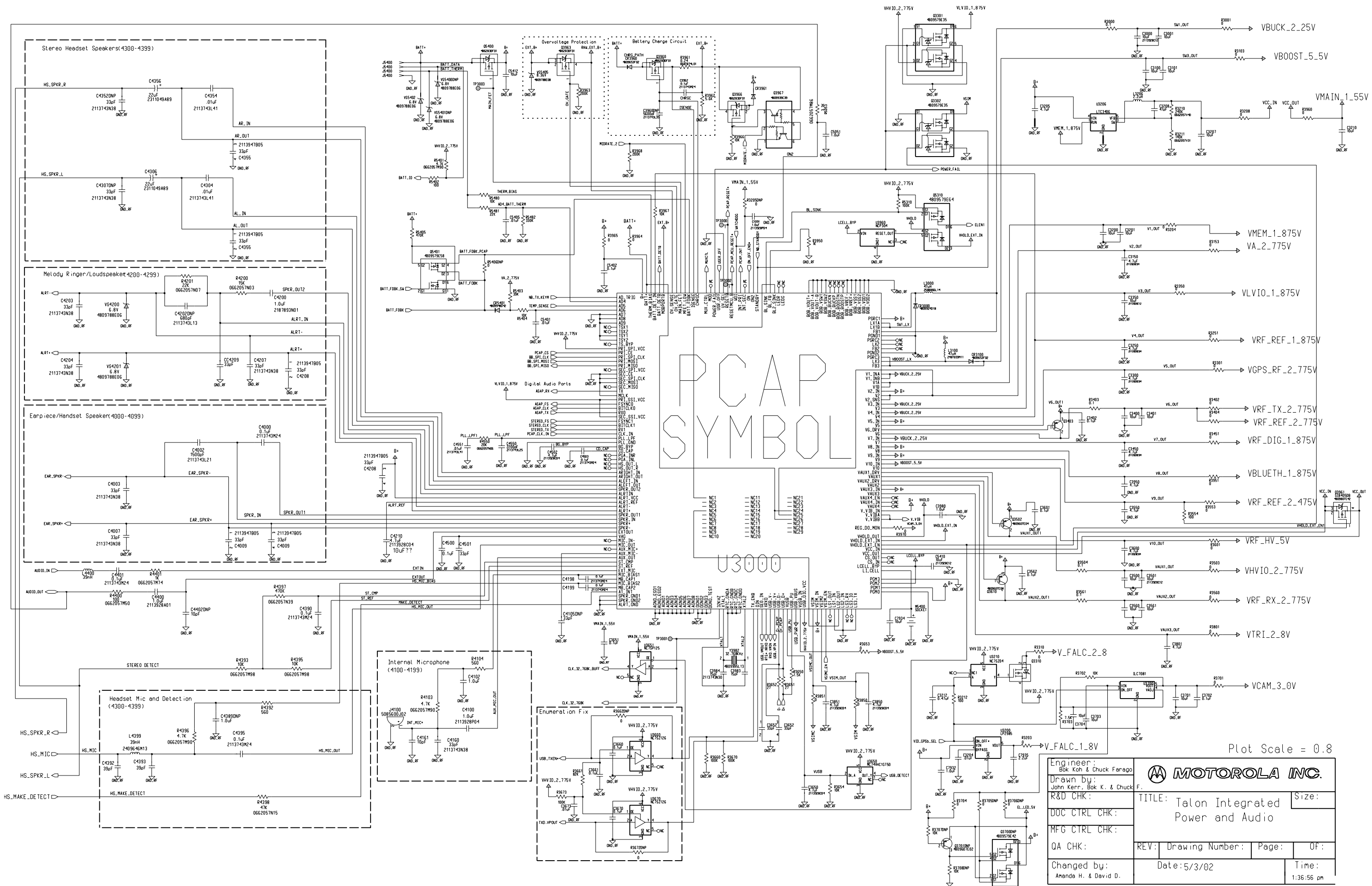


B0 - Normally Closed
B1 - Normally Open
S - Select
A - Common

Engineer: Jennifer Seamour	MOTOROLA INC.		
Drawn by:			
R&D CHK:	TITLE: TALON Integrated	Size:	
DOC CTRL CHK:	CE Accessory Connector		
MFG CTRL CHK:	REV:	Drawing Number:	Page:
QA CHK:			OF:
Changed by: Ed Naddeo	Date: 4/3/02	Time:	

Engineer: Dave Stubbs	MOTOROLA INC.		
Drawn by:	TITLE: TALON Integrated Bluetooth		
R&D CHK:	REV: Drawing Number: Page: OF:		Size:
DOC CTRL CHK:			
MFG CTRL CHK:			
QA CHK:			
Changed by: Dave Suarez	Date: February 24, 2003	Time:	





PCAP
SYMBOL

Plot Scale = 0.8

Engineer: Bok Koh & Chuck Farago Drawn by: John Kerr, Bok K. & Chuck F. R&D CHK: DOC CTRL CHK: MFG CTRL CHK: QA CHK: Changed by: Amanda H. & David D.	MOTOROLA INC. TITLE: Talon Integrated Power and Audio REV: Drawing Number: Page: OF: Date: 5/3/02 Time: 1:36:56 pm
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A835: Side 1 Layout (P6 Build)

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